

JET WAY INFORMATION

MODEL: J-694AS

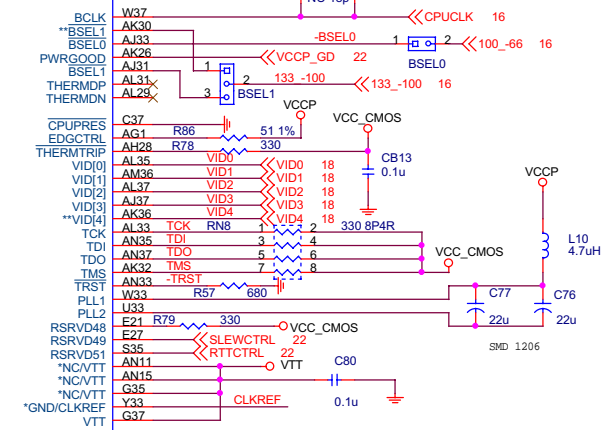
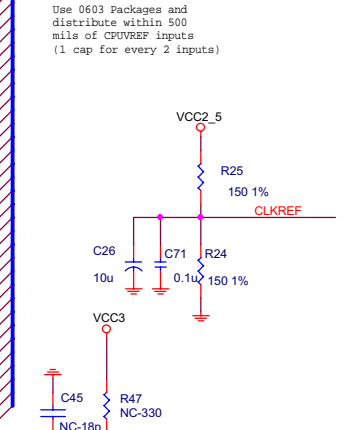
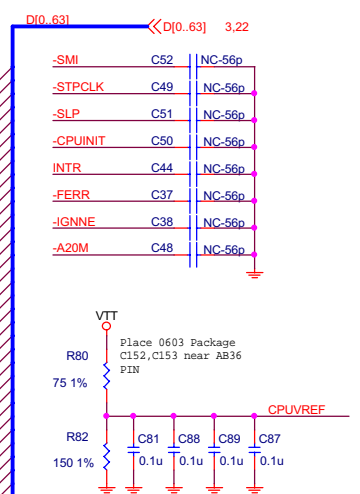
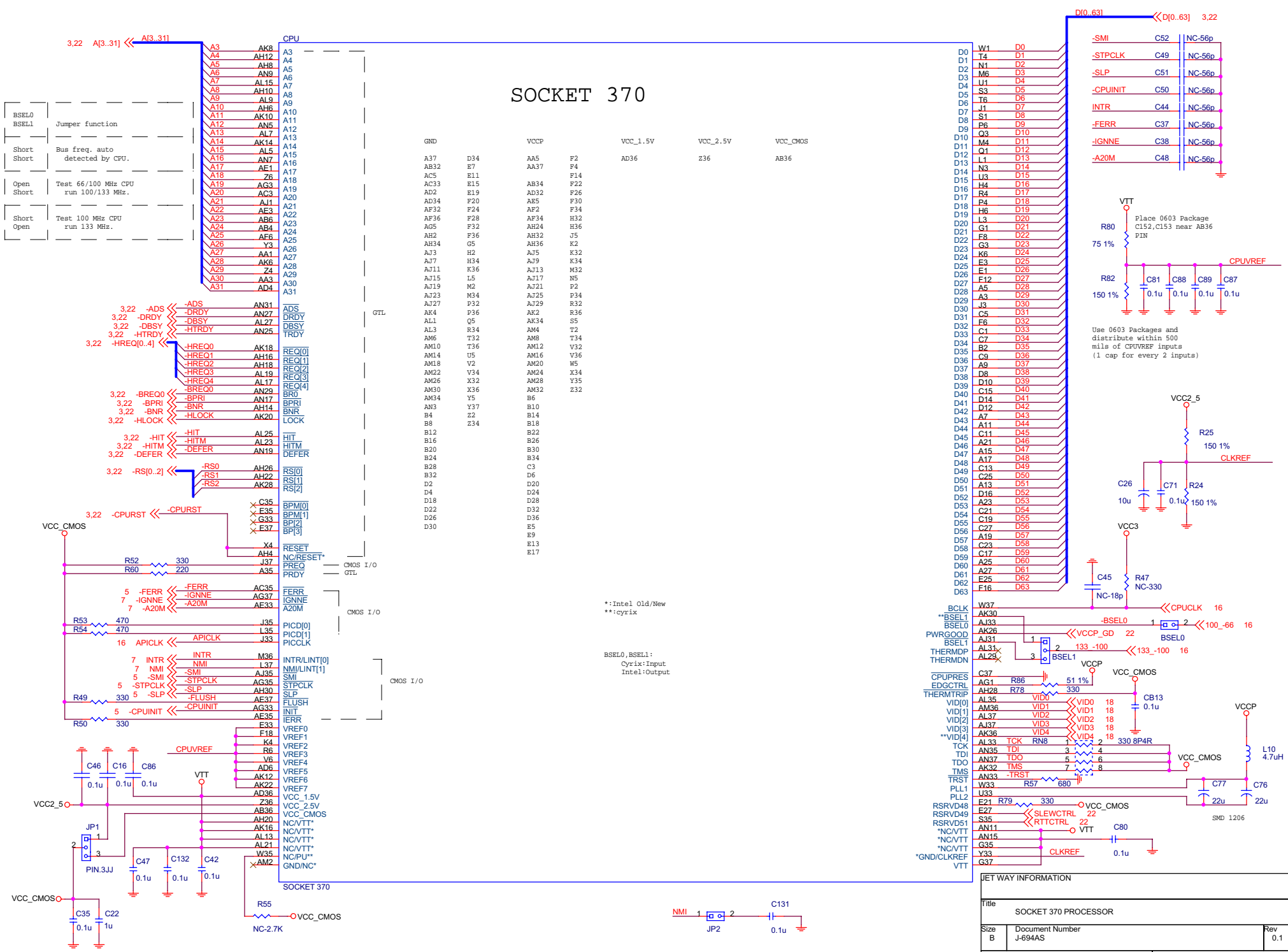
(SOCKET 370+VT82C694X+VT82C686B+AGP 4X+1 AMR)

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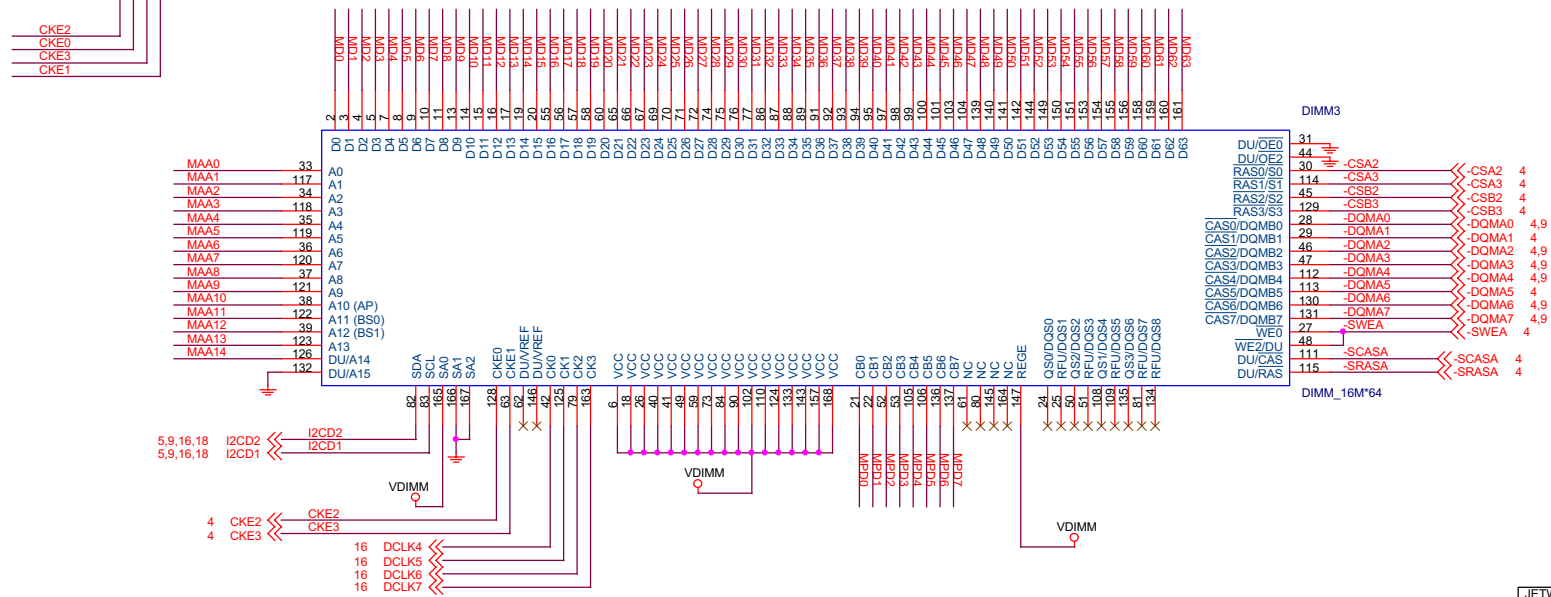
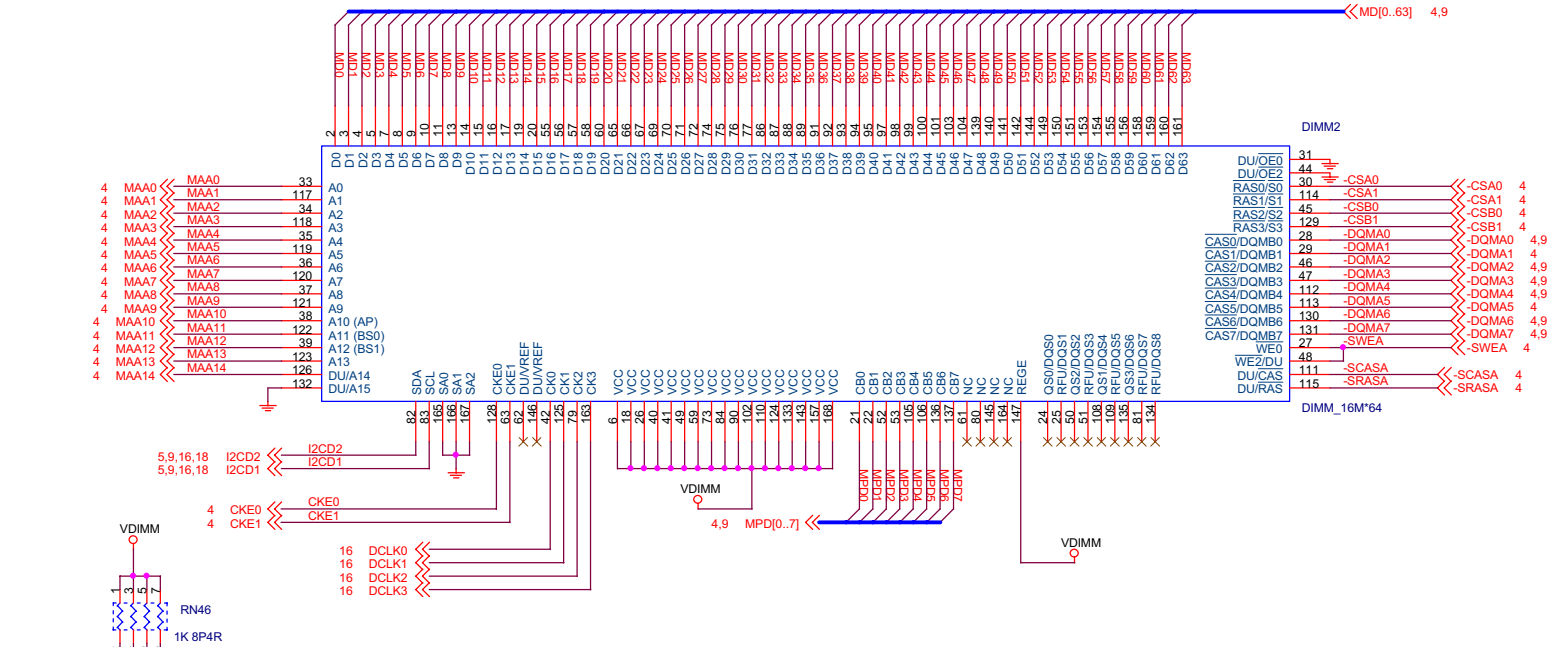
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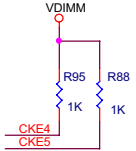
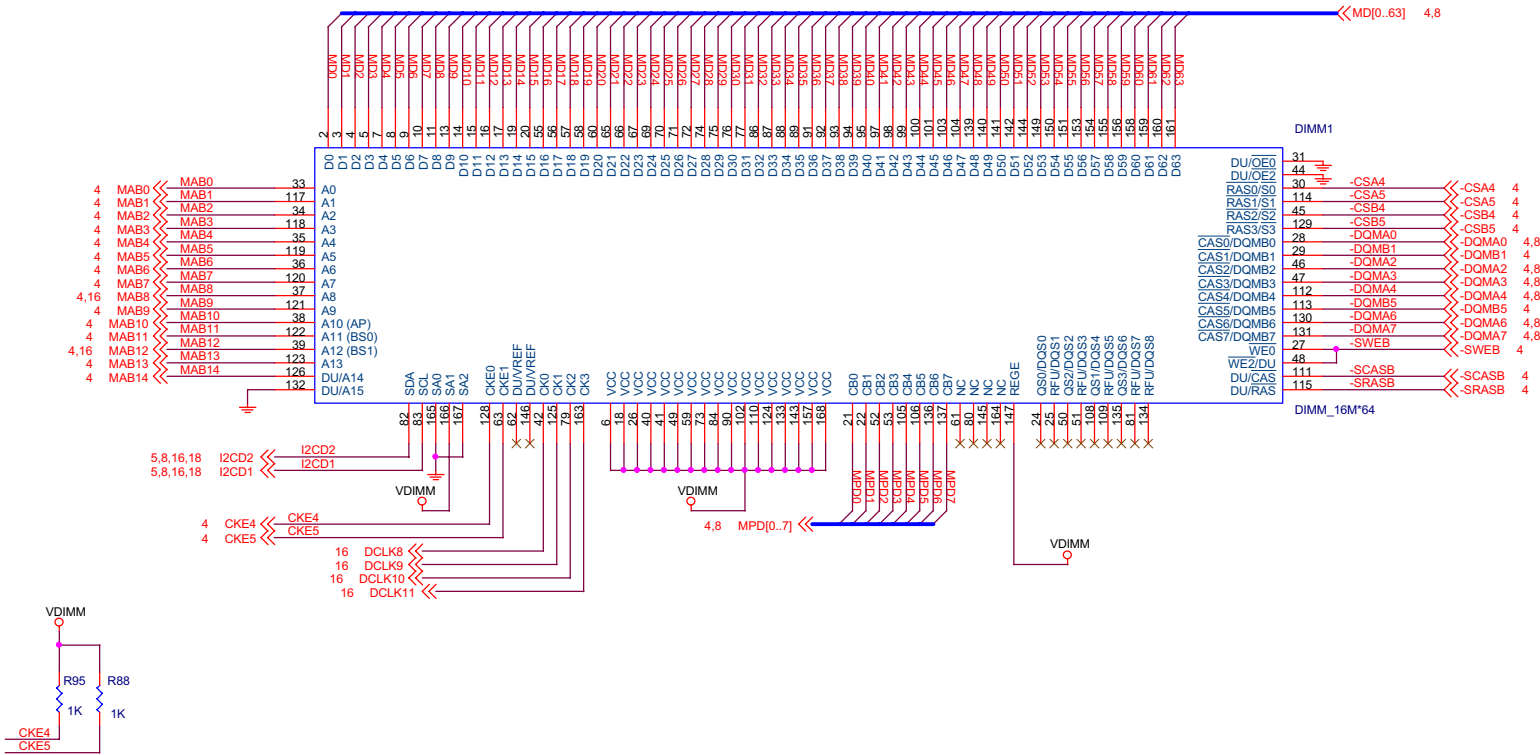
SOCKET 370



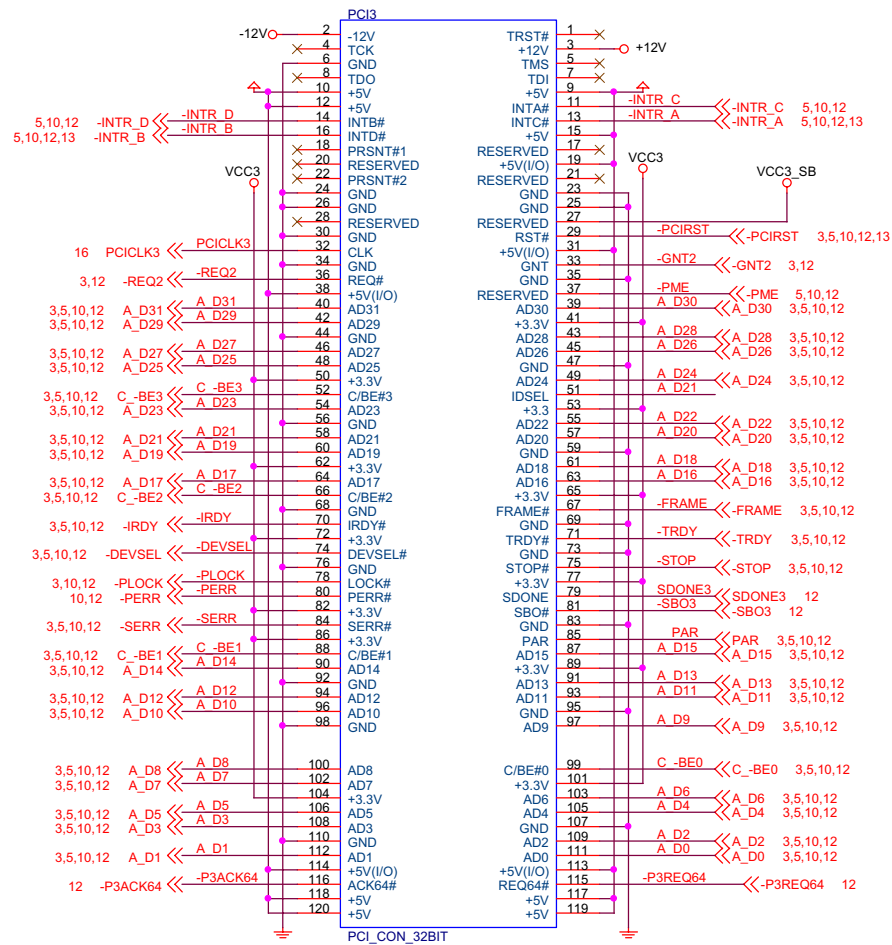
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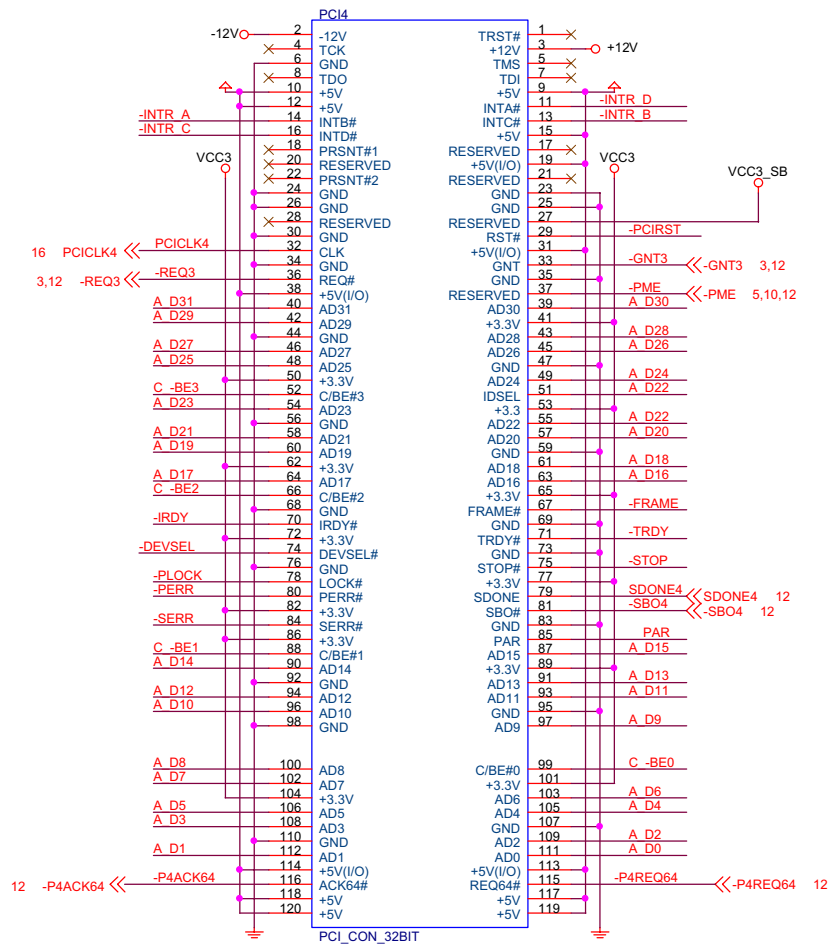
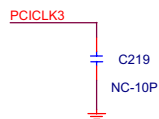
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SDRAM			
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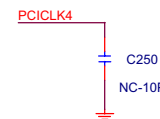
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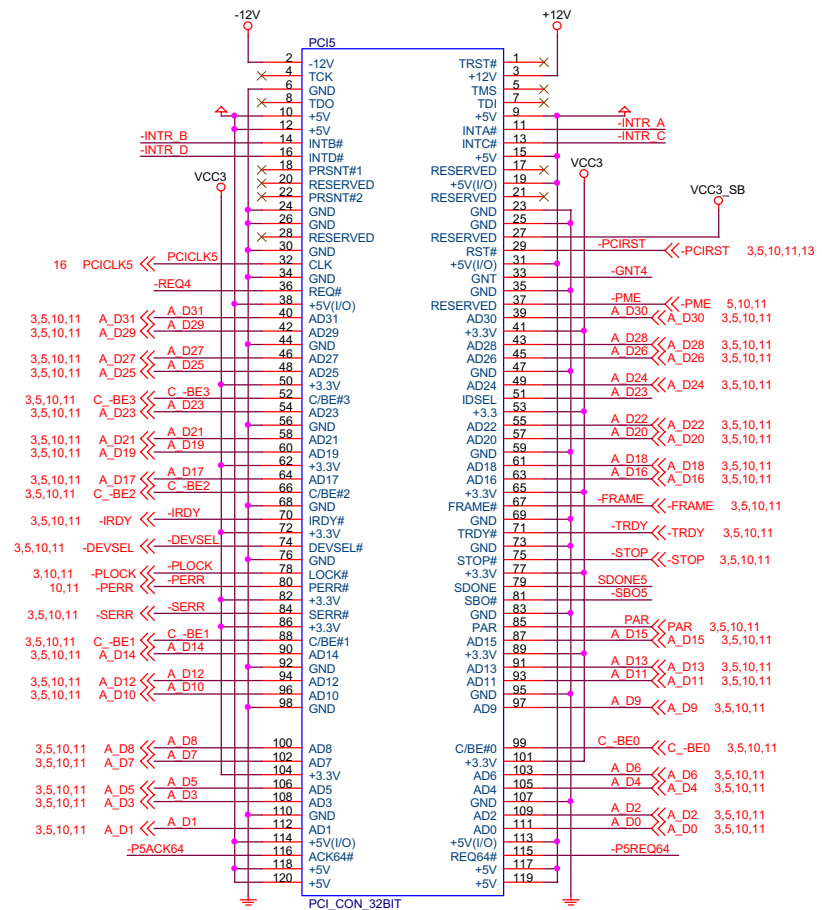
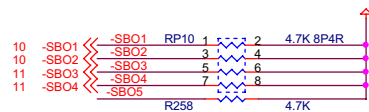
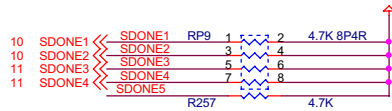
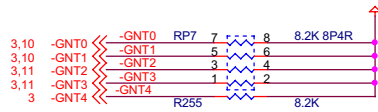
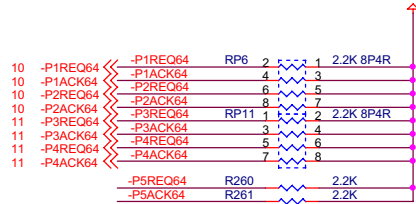
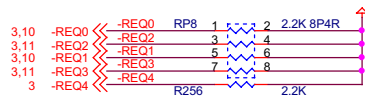
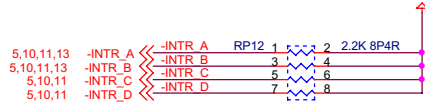
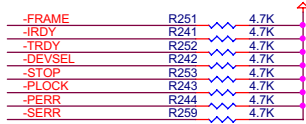
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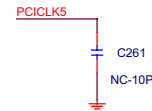
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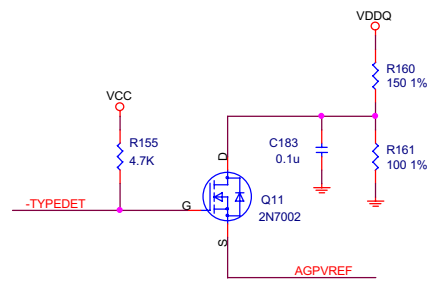
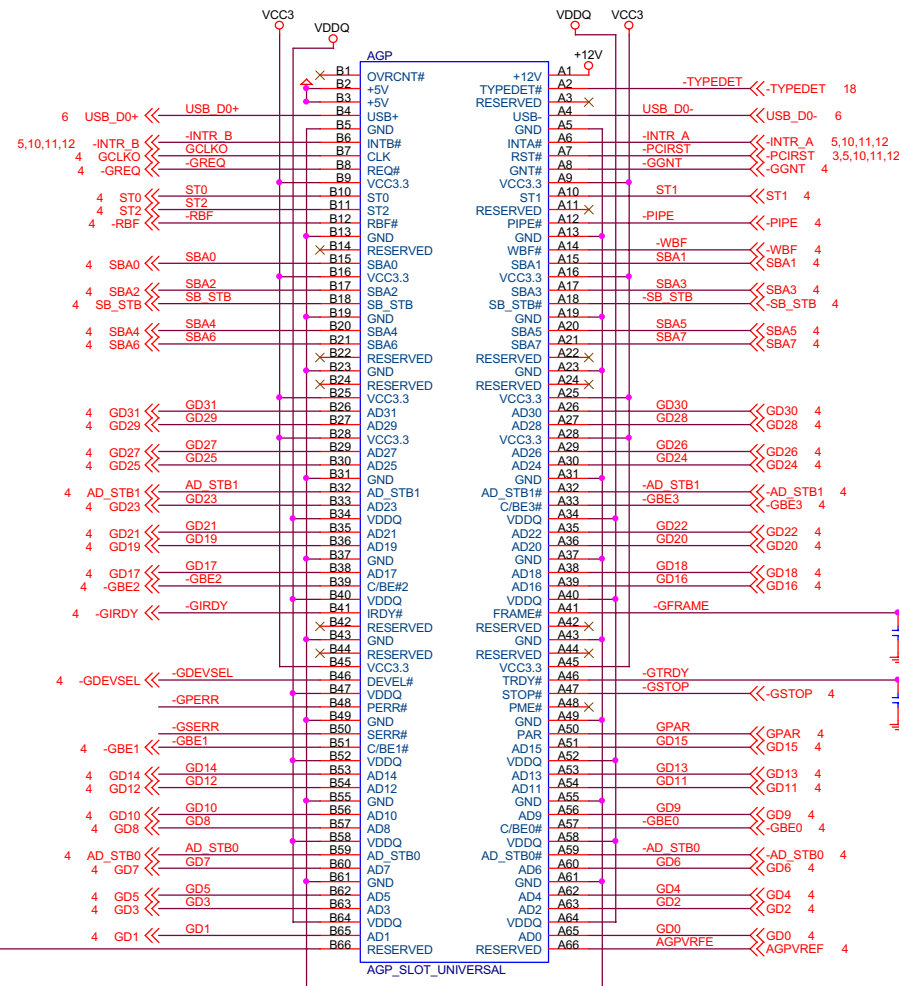
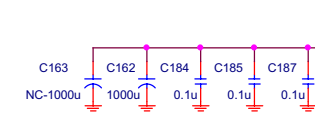
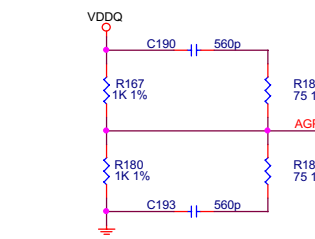
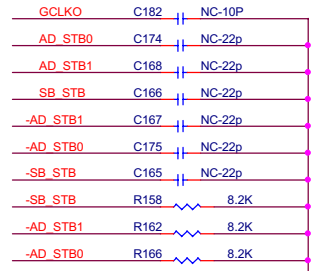
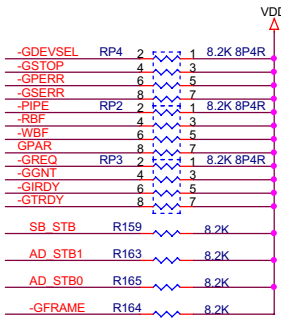


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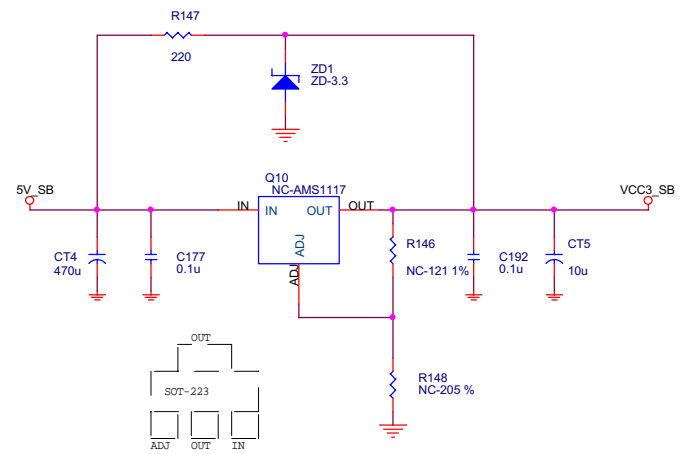
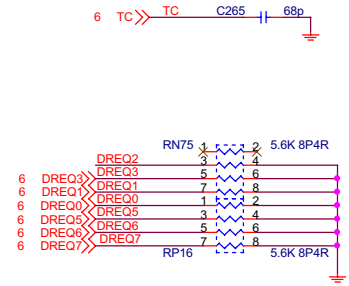
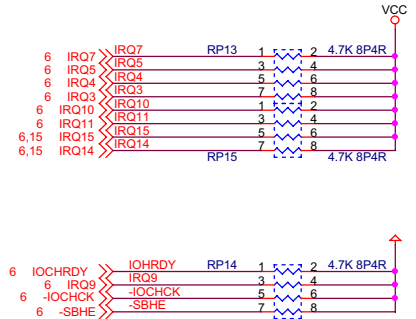
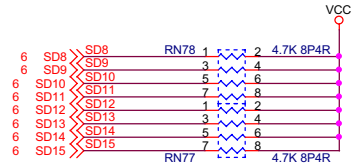
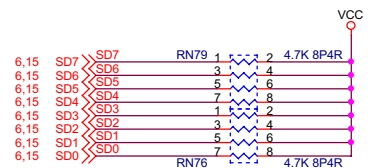
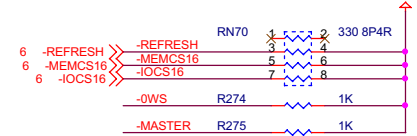
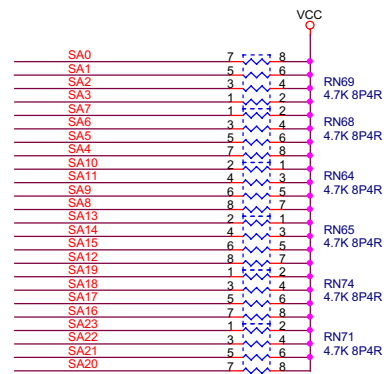
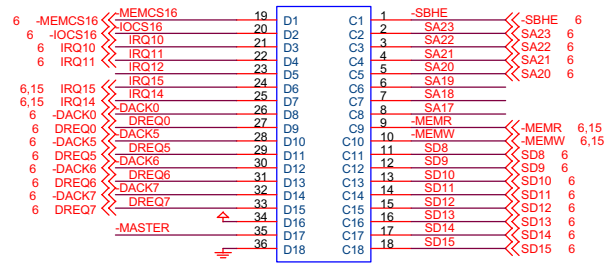
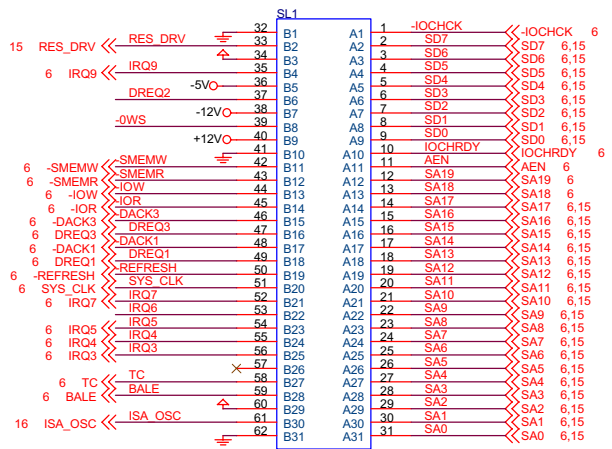


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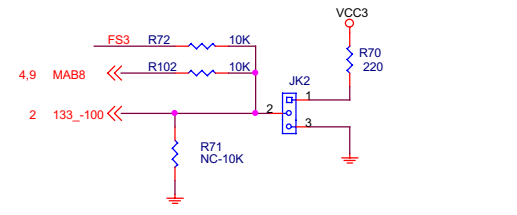
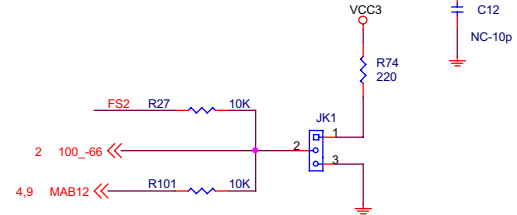
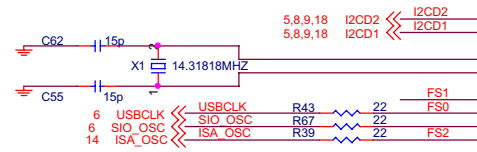
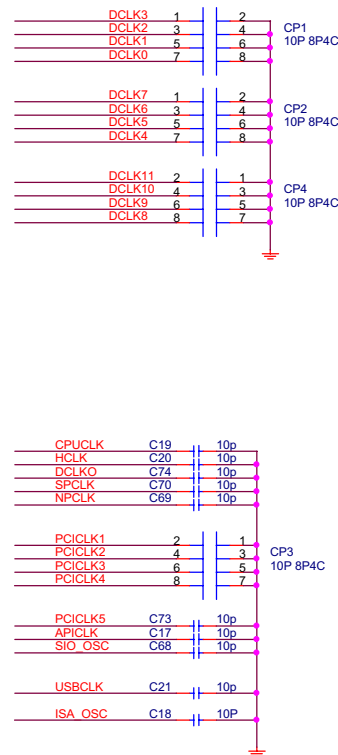
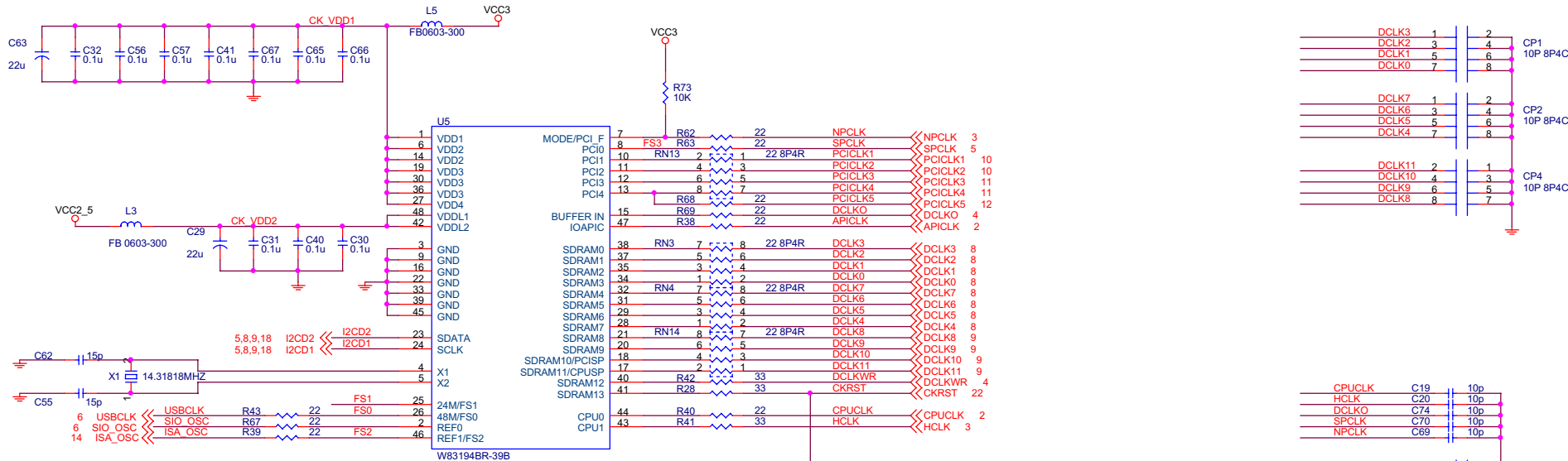
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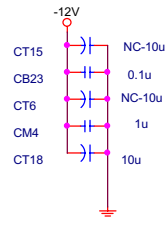
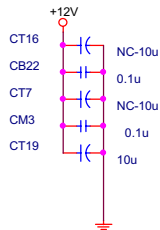
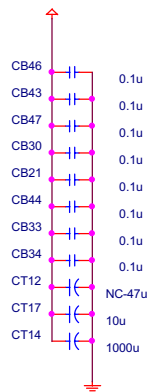
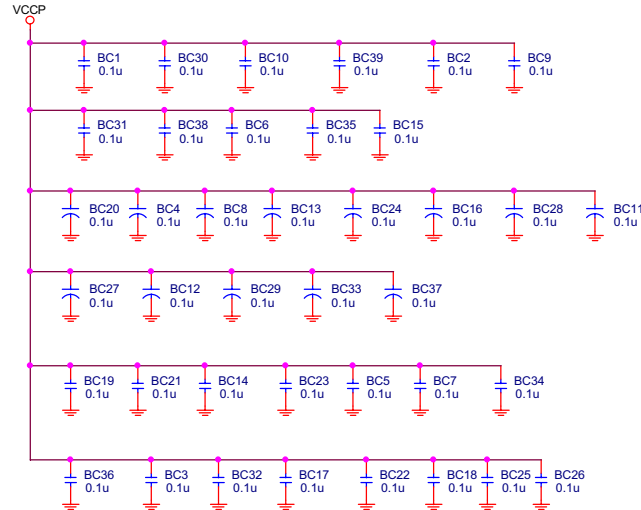
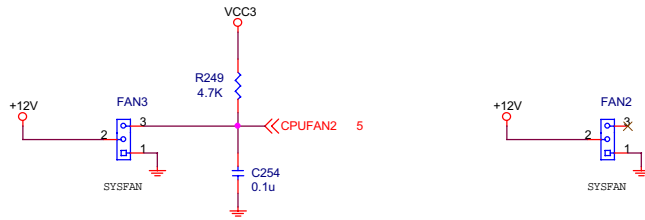
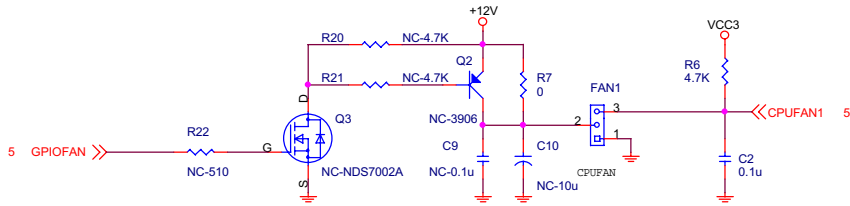
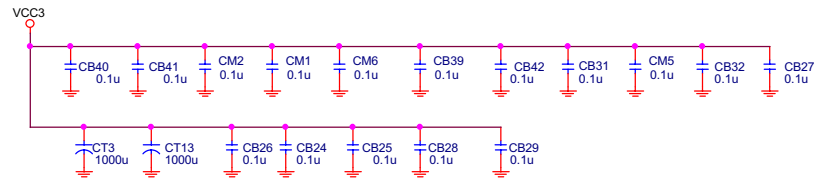
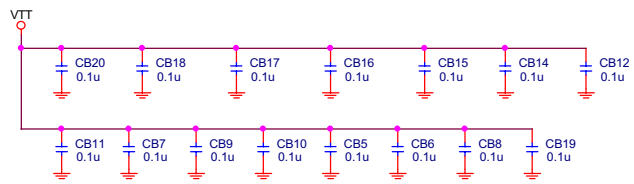


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JK2	JK1	Bus Frequency
2-3	2-3	66 MHz
2-3	1-2	100 MHz
1-2	1-2	133 MHz

JETWAY INFORMATION.		
Title CLOCK SYNTHESIZER		
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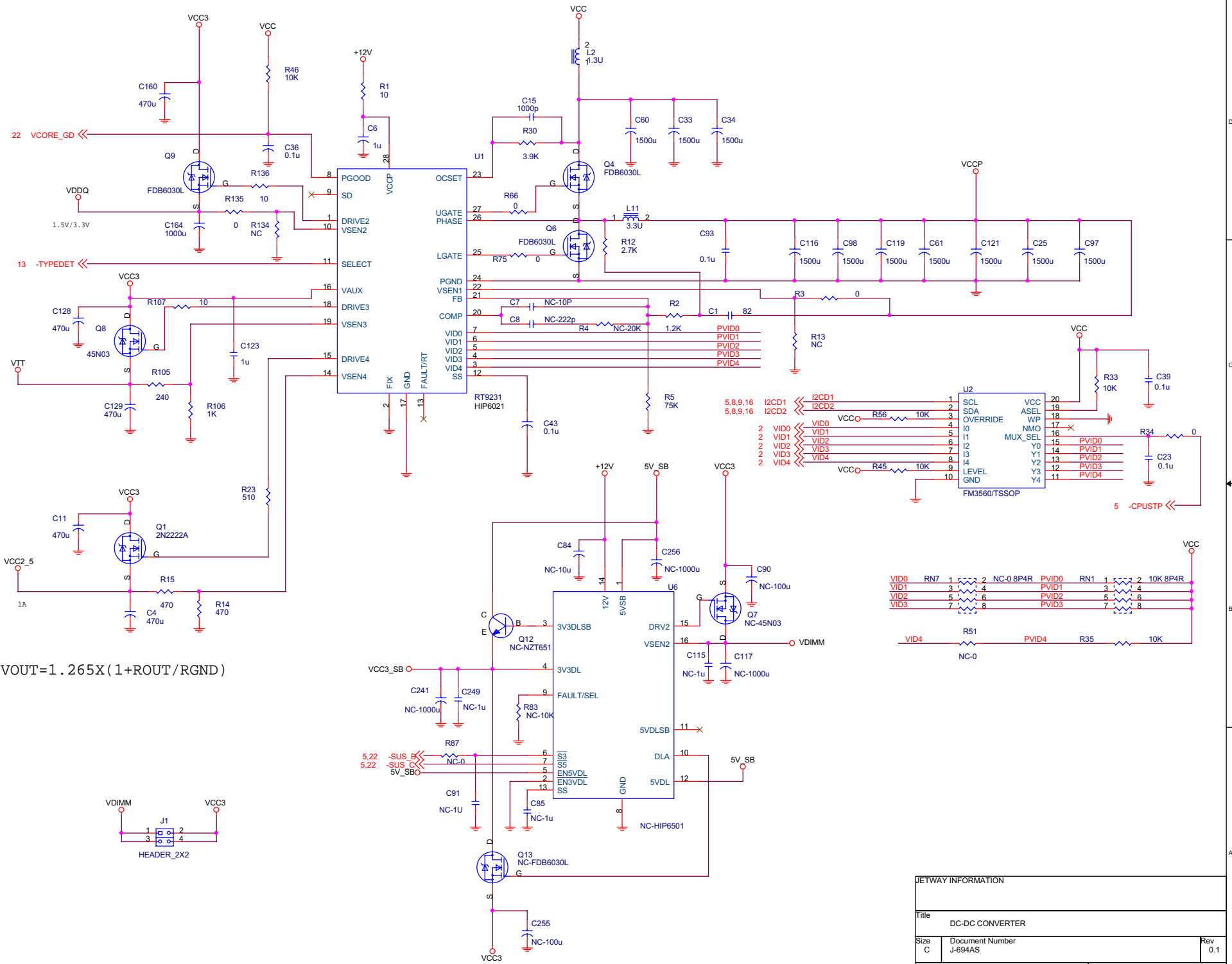


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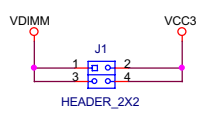
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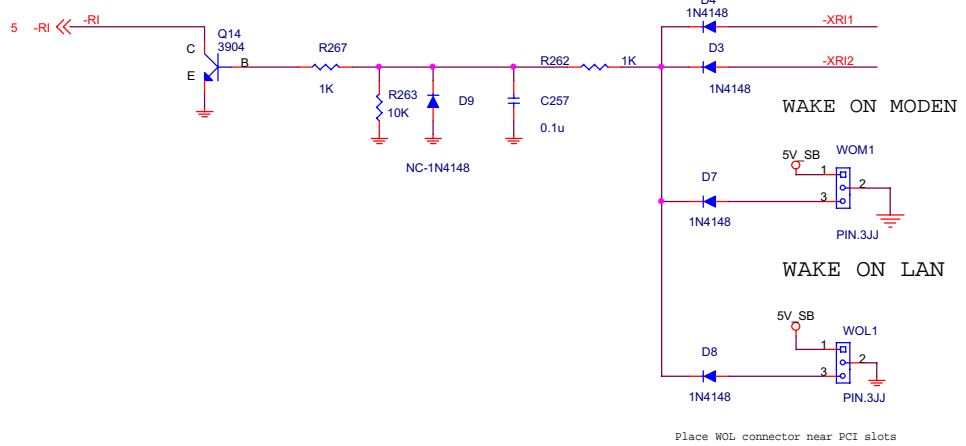


$$V_{OUT} = 1.265X(1 + R_{OUT}/R_{GND})$$



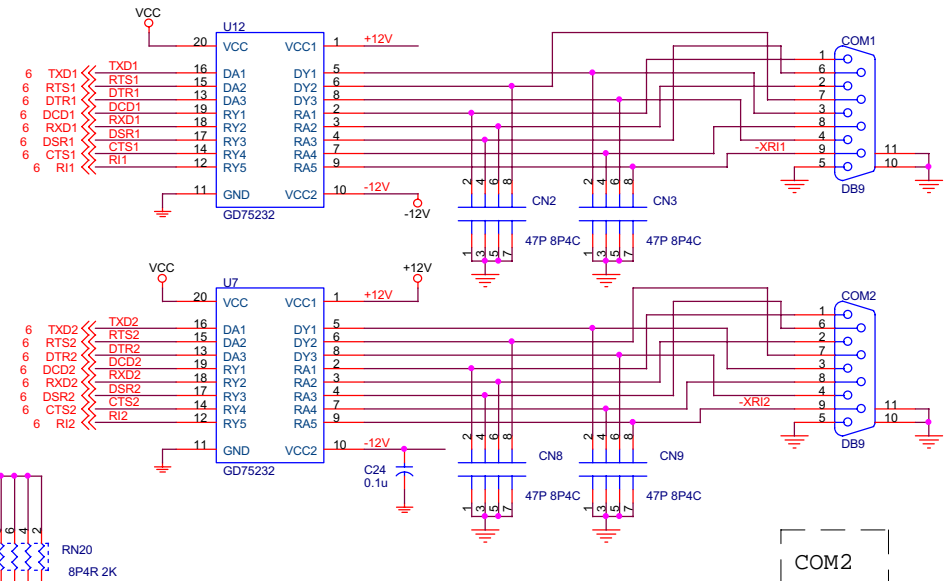
JETWAY INFORMATION			
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RING IN



COM1 , COM2

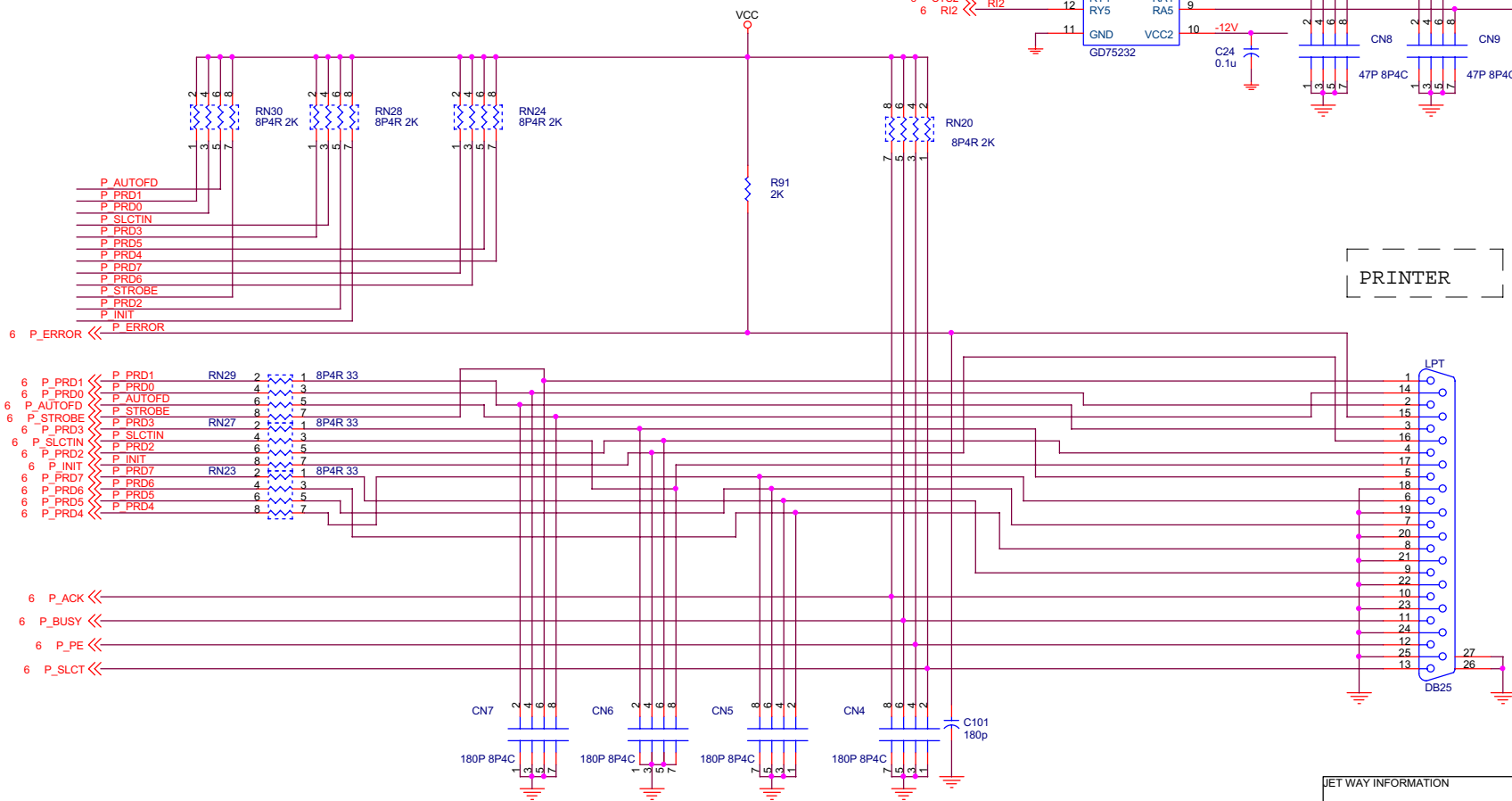
with Bipolar drivers and receivers



COM1

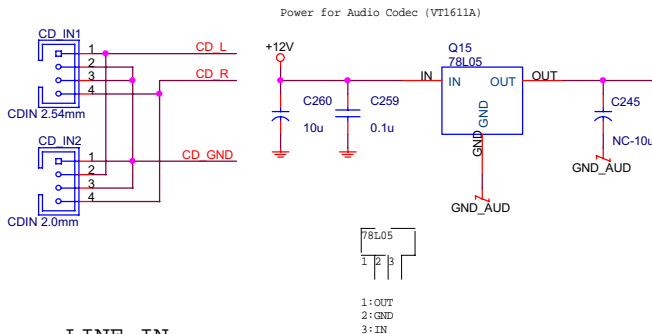
COM2

PRINTER

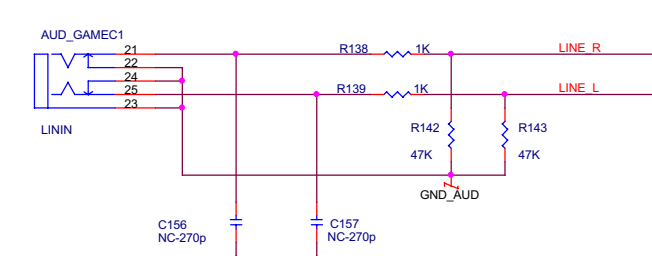


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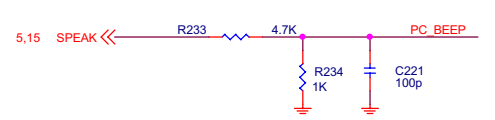
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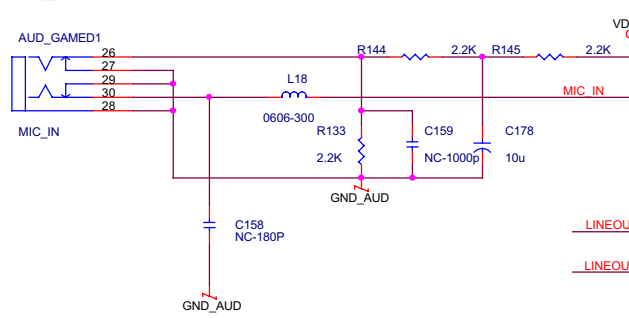
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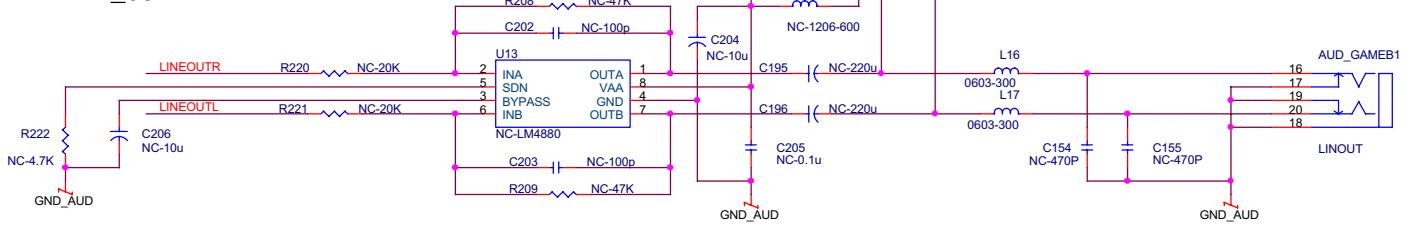
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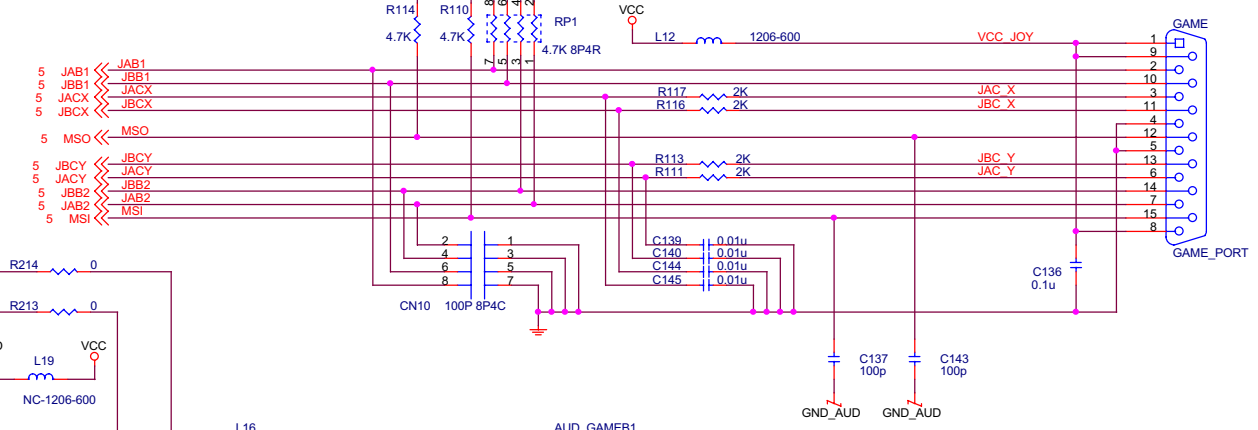
MIC_IN



LINE_OUT

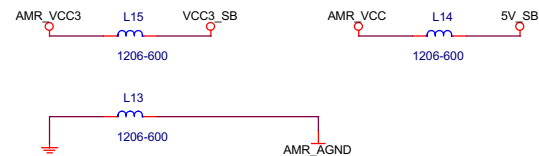
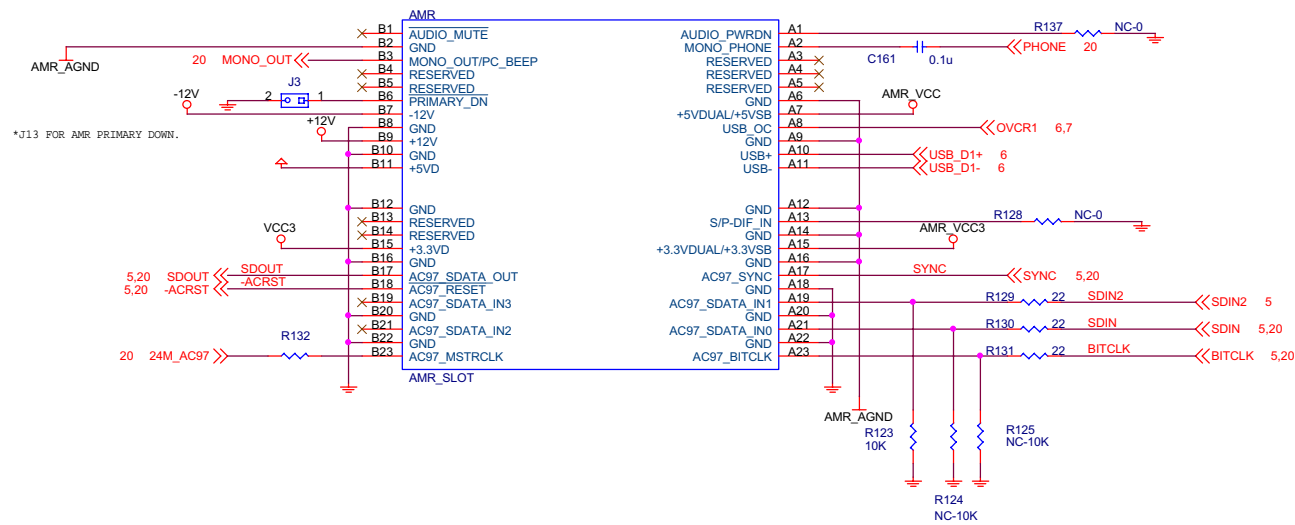


GAME PORT

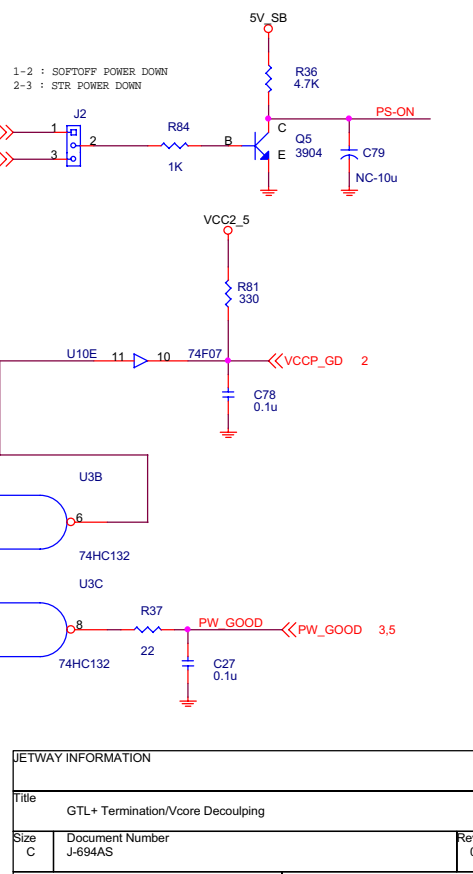
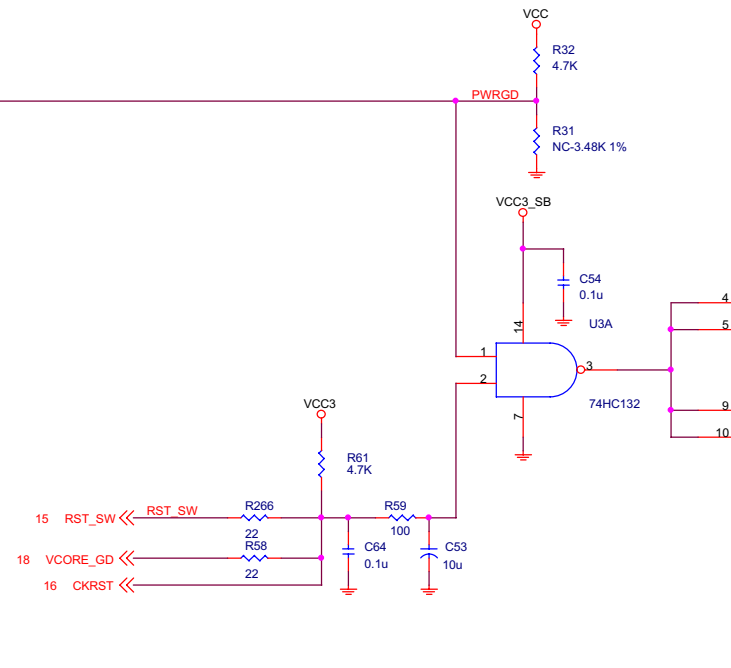
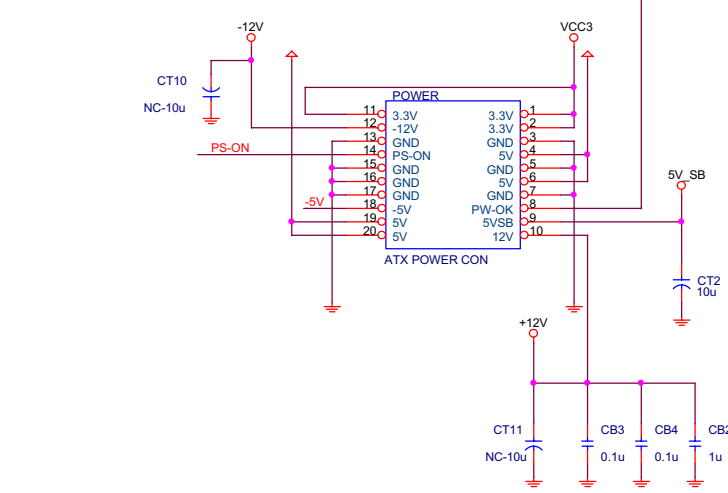
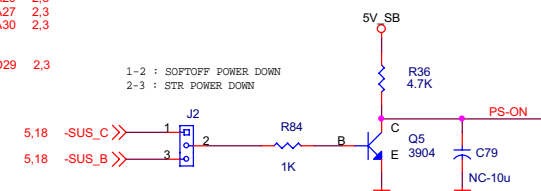
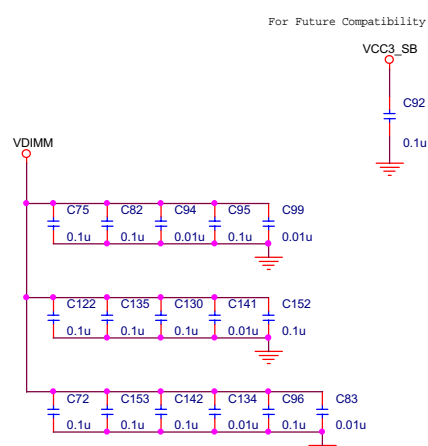
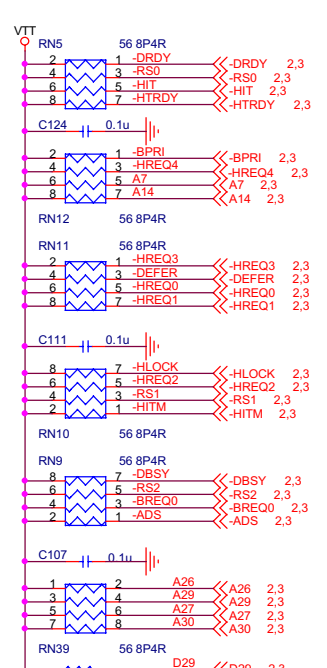
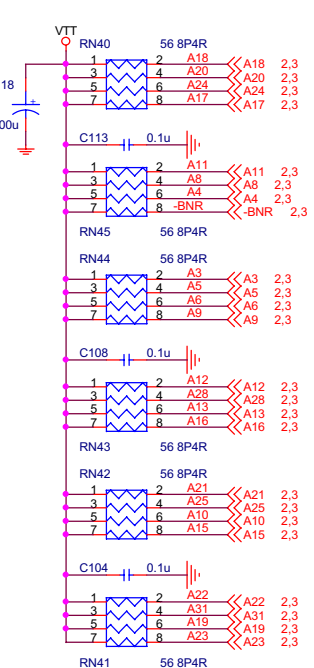
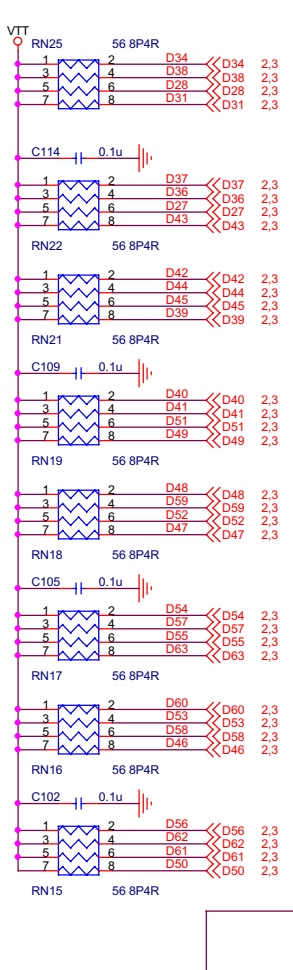
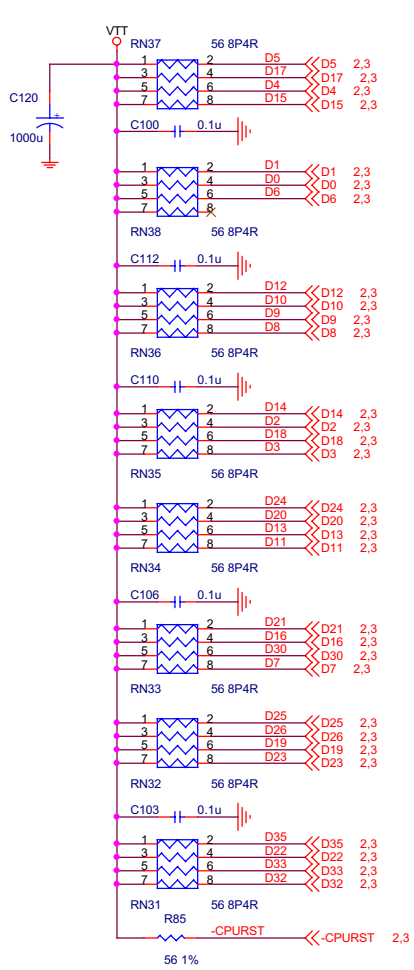


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AC'97 CODEC & AUDIO/GAME PORT		
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ARM SLOT			
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