

JET WAY INFORMATION

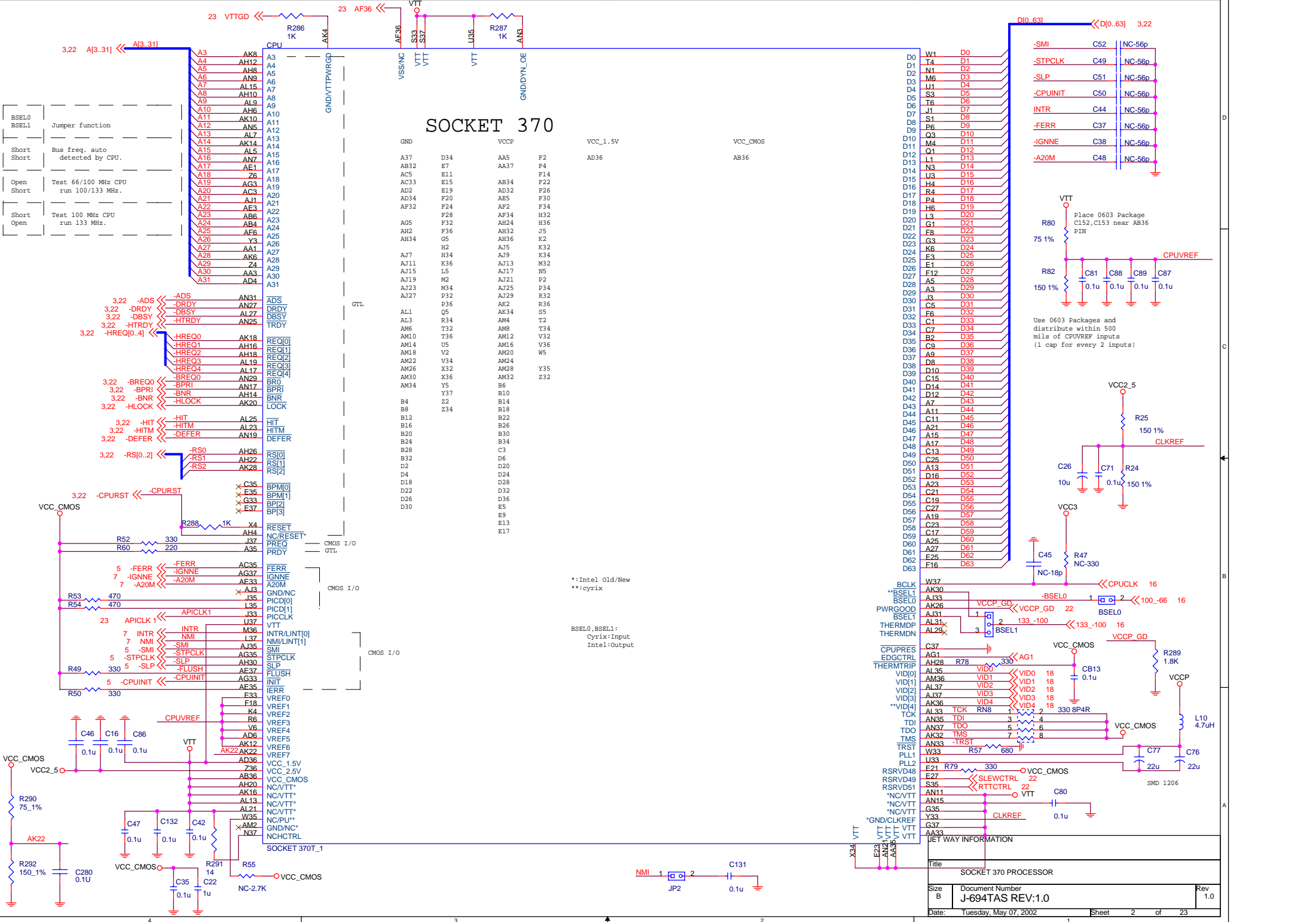
MODEL: 694TAS REV: 1.0

(SOCKET 370+VT82C694T+VT82C686B+AGP 4X+1 AMR)

TITLE	SHEET
COVER SHEET	1
SOCKET 370 PROCESSOR	2
NORTH BRIDGE (VT82C694T)	3,4
SOUTH BRIDGE (VT82C686B)	5,6
USB2,3 & FREQUENCY RATIO	7
SDRAM	8,9
PCI SLOTS	10,11,12
AGP SLOT	13
ISA PULL UP RESISTANCE	14
IDE & PANEL	15
CLOCK SYNTHESIZER	16
BYPASS CAPACITORS	17
DC-DC CONVERTER	18
PRINTER / COM PORT	17
AUDIO CODEC & AUDIO PORT & JOSTICK PORT	20
AMR SLOT	21
ATX POWER CONNECTOR / VTT TERMINATOR / VCC3	22

LINK
 2.SCH
 3.SCH
 4.SCH
 5.SCH
 6.SCH
 7.SCH
 8.SCH
 9.SCH
 10.SCH
 11.SCH
 12.SCH
 13.SCH
 14.SCH
 15.SCH
 16.SCH
 17.SCH
 18.SCH
 19.SCH
 20.SCH
 21.SCH
 22.SCH

JETWAY INFORMATION			
Title			
COVER SHEET			
Size	Document Number		Rev
	J-694TAS REV:1.0		0.1
Date:	Tuesday, May 07, 2002	Sheet	1 of 23



SOCKET 370

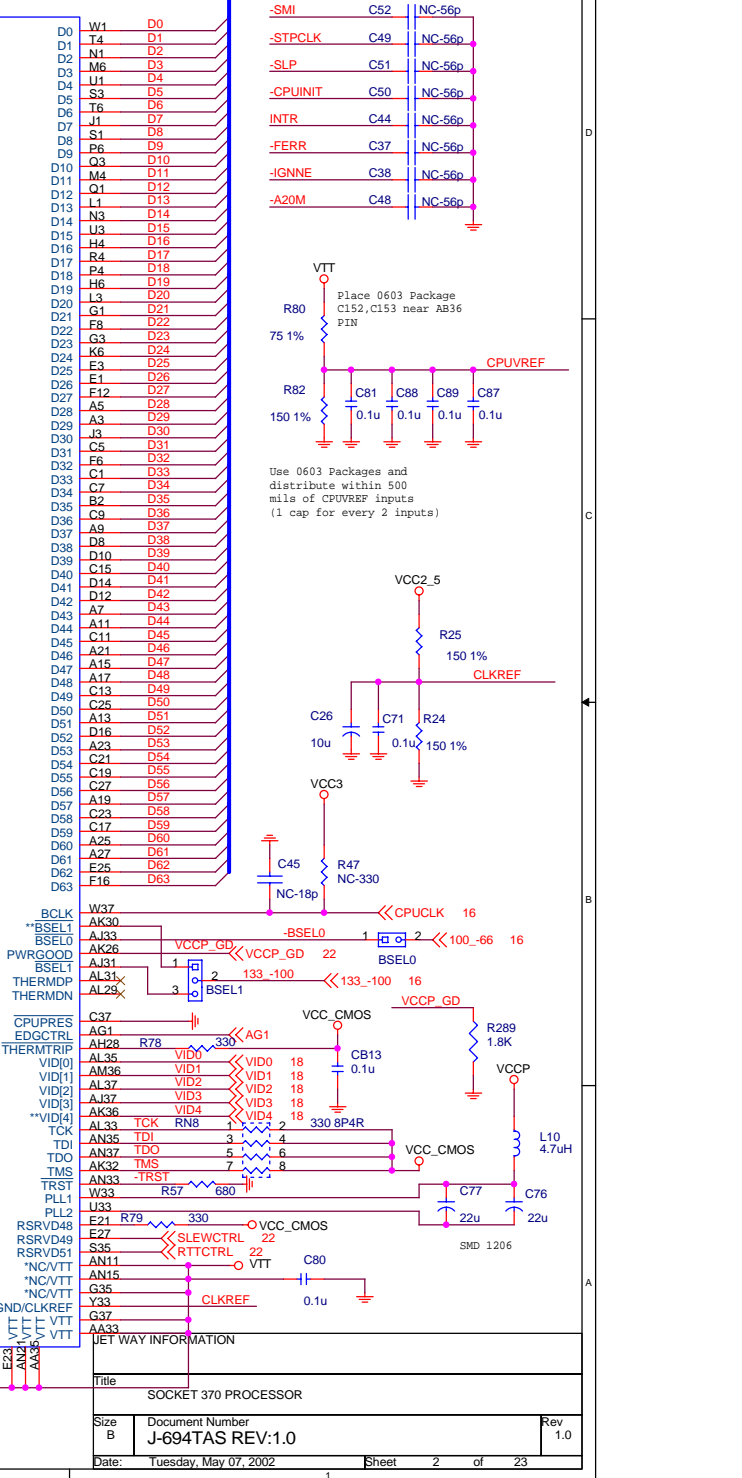
BSEL0	BSEL1	Jumper function
Short	Short	Bus freq. auto detected by CPU.
Open	Short	Test 66/100 MHz CPU run 100/133 MHz.
Short	Open	Test 100 MHz CPU run 133 MHz.

A3	AK8	A3	AK8
A4	AH12	A4	AH12
A5	AH8	A5	AH8
A6	AN8	A6	AN8
A7	AL15	A7	AL15
A8	AH10	A8	AH10
A9	AL9	A9	AL9
A10	AH6	A10	AH6
A11	AK10	A11	AK10
A12	AN5	A12	AN5
A13	AK3	A13	AK3
A14	AK14	A14	AK14
A15	AL5	A15	AL5
A16	AN7	A16	AN7
A17	AE1	A17	AE1
A18	Z6	A18	Z6
A19	AG3	A19	AG3
A20	AC3	A20	AC3
A21	AL1	A21	AL1
A22	AE3	A22	AE3
A23	AB6	A23	AB6
A24	AB4	A24	AB4
A25	AF6	A25	AF6
A26	Y3	A26	Y3
A27	AK6	A27	AK6
A28	Z4	A28	Z4
A29	AA3	A29	AA3
A30	AA3	A30	AA3
A31	AD4	A31	AD4

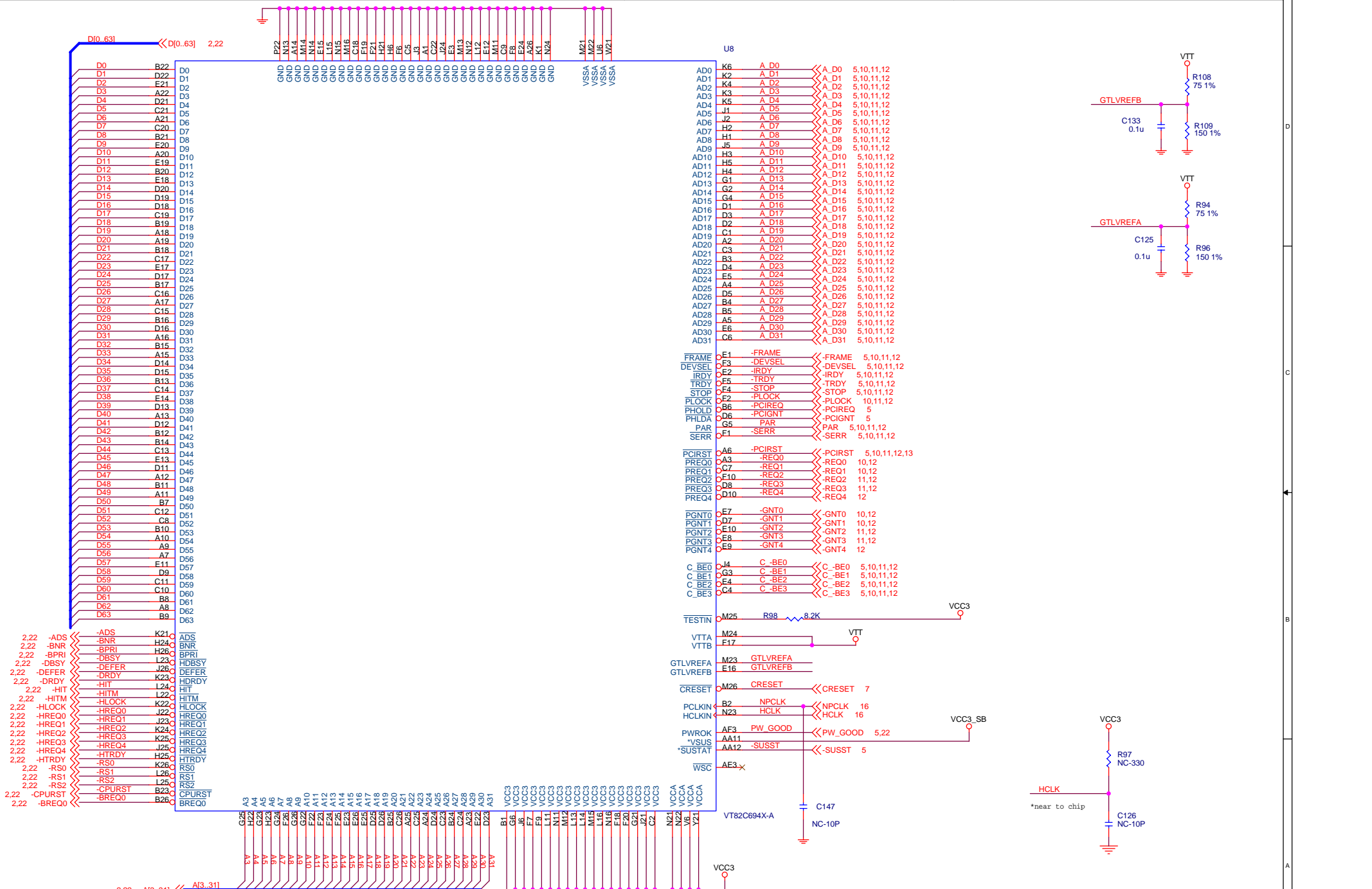
GND	VCCP	VCC_1.5V	VCC_CMOS
A37	D34	AA5	F2
AB32	E7	AA37	F4
AC5	E11	AB34	F14
AC33	E15	AC33	F22
AD2	E19	AD2	F26
AD34	F20	AD5	F30
AF32	F24	AF2	F34
A22	F28	AF34	H32
A23	F32	AH24	H36
AH2	F36	AH32	J5
AH34	G5	AH36	K2
H2	A25	K32	
AJ7	H34	AJ39	K34
AJ11	K36	AJ13	M32
AJ15	L5	AJ17	N5
AJ19	M2	AJ21	P2
AJ23	M34	AJ25	P34
AJ27	P32	AJ29	R32
P36	AK2	R36	
AL1	Q5	AK34	S5
AL3	R34	AM4	T2
AM6	T32	AM8	T34
AM10	T36	AM12	V32
AM14	U5	AM16	V36
AM18	V2	AM20	W5
AM22	Y34	AM24	
AM26	X32	AM28	Y35
AM30	X36	AM32	Z32
AM34	Y6	B6	
B4	Y37	B10	
B8	Z2	B14	
B12	Z34	B18	
B16	B22	B26	
B20	B30	B34	
B24	B34	C3	
B28	C6	D6	
B32	D2	D20	
D4	D4	D24	
D18	D28	D32	
D22	D36	D36	
D26	D36	D36	
D30	E5	E9	
E13	E13	E17	

ADS	AK37	ADS	AK37
DRDY	AN27	DRDY	AN27
DBSY	AL27	DBSY	AL27
HTRDY	AN25	HTRDY	AN25
HREQ[0..4]	AK18	REQ[0]	AK18
HREQ[1]	AH16	REQ[1]	AH16
HREQ[2]	AH18	REQ[2]	AH18
HREQ[3]	AL19	REQ[3]	AL19
HREQ[4]	AL17	REQ[4]	AL17
BREQ0	AN29	REQ[4]	AN29
BPRI	AM17	REQ[4]	AM17
BNR	AH14	REQ[4]	AH14
HLOCK	AK20	REQ[4]	AK20
HIT	AL25	HIT	AL25
HITM	AL23	HITM	AL23
DEFER	AN19	DEFER	AN19
RS[0..2]	AH26	RS[0]	AH26
RS[1]	AH22	RS[1]	AH22
RS[2]	AK28	RS[2]	AK28

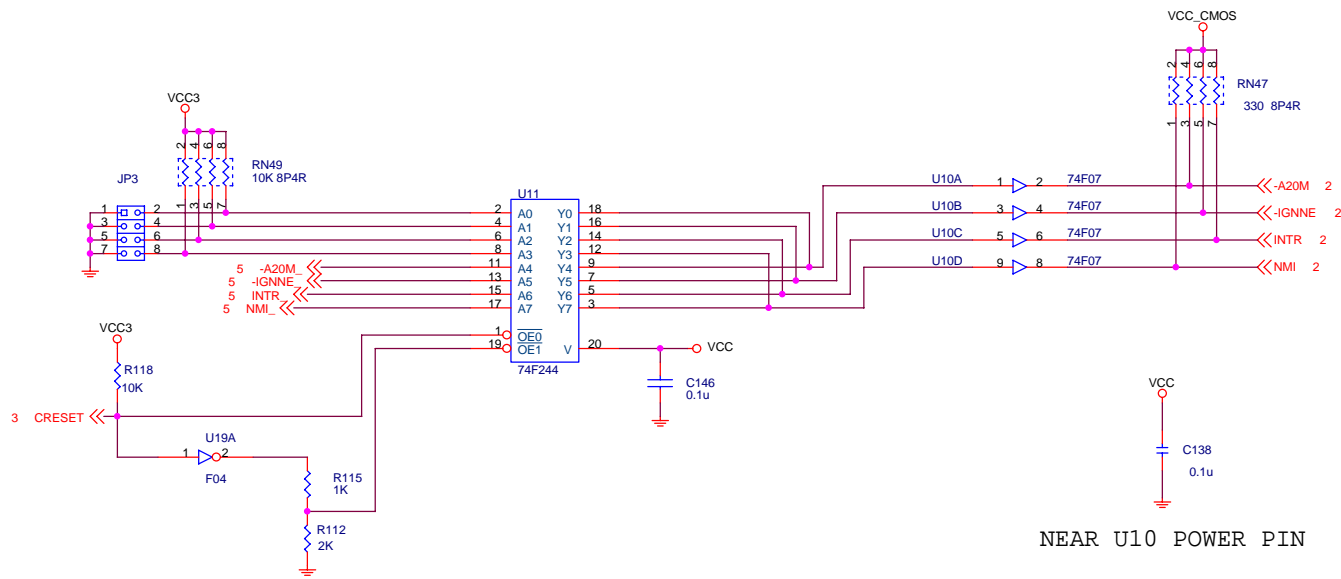
C35	BPM[0]	BPM[0]	C35
E35	BPM[1]	BPM[1]	E35
G33	BPM[2]	BPM[2]	G33
E37	BPM[3]	BPM[3]	E37
X4	RESET	RESET	X4
A44	NC/RESET*	NC/RESET*	A44
J37	PREQ	PREQ	J37
A35	PRDY	PRDY	A35
AC35	FERR	FERR	AC35
AG37	IGNNE	IGNNE	AG37
AE33	IGNNE	IGNNE	AE33
AJ3	A20M	A20M	AJ3
L35	GND/NC	GND/NC	L35
J35	PICD[0]	PICD[0]	J35
J33	PICD[1]	PICD[1]	J33
U37	PICCLK	PICCLK	U37
M36	VTT	VTT	M36
L37	INTR/LINT[0]	INTR/LINT[0]	L37
AJ35	NMI	NMI	AJ35
AG35	SMI	SMI	AG35
AH30	STPCLK	STPCLK	AH30
AE37	FLUSH	FLUSH	AE37
AG33	INIT	INIT	AG33
AE35	IERR	IERR	AE35
E33	VREF0	VREF0	E33
F18	VREF1	VREF1	F18
K4	VREF2	VREF2	K4
R6	VREF3	VREF3	R6
V6	VREF4	VREF4	V6
AD6	VREF5	VREF5	AD6
AK12	VREF6	VREF6	AK12
AK22	VREF7	VREF7	AK22
AD36	VCC_1.5V	VCC_1.5V	AD36
Z36	VCC_2.5V	VCC_2.5V	Z36
AB36	VCC_CMOS	VCC_CMOS	AB36
AH20	NC/VTT*	NC/VTT*	AH20
AK16	NC/VTT*	NC/VTT*	AK16
AL13	NC/VTT*	NC/VTT*	AL13
AL21	NC/VTT*	NC/VTT*	AL21
W35	NC/VTT*	NC/VTT*	W35
W35	NC/PU*	NC/PU*	W35
N37	GND/NC*	GND/NC*	N37
N37	NCHCTRL	NCHCTRL	N37



Title	SOCKET 370 PROCESSOR	
Size	Document Number	Rev
B	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 2 of 23

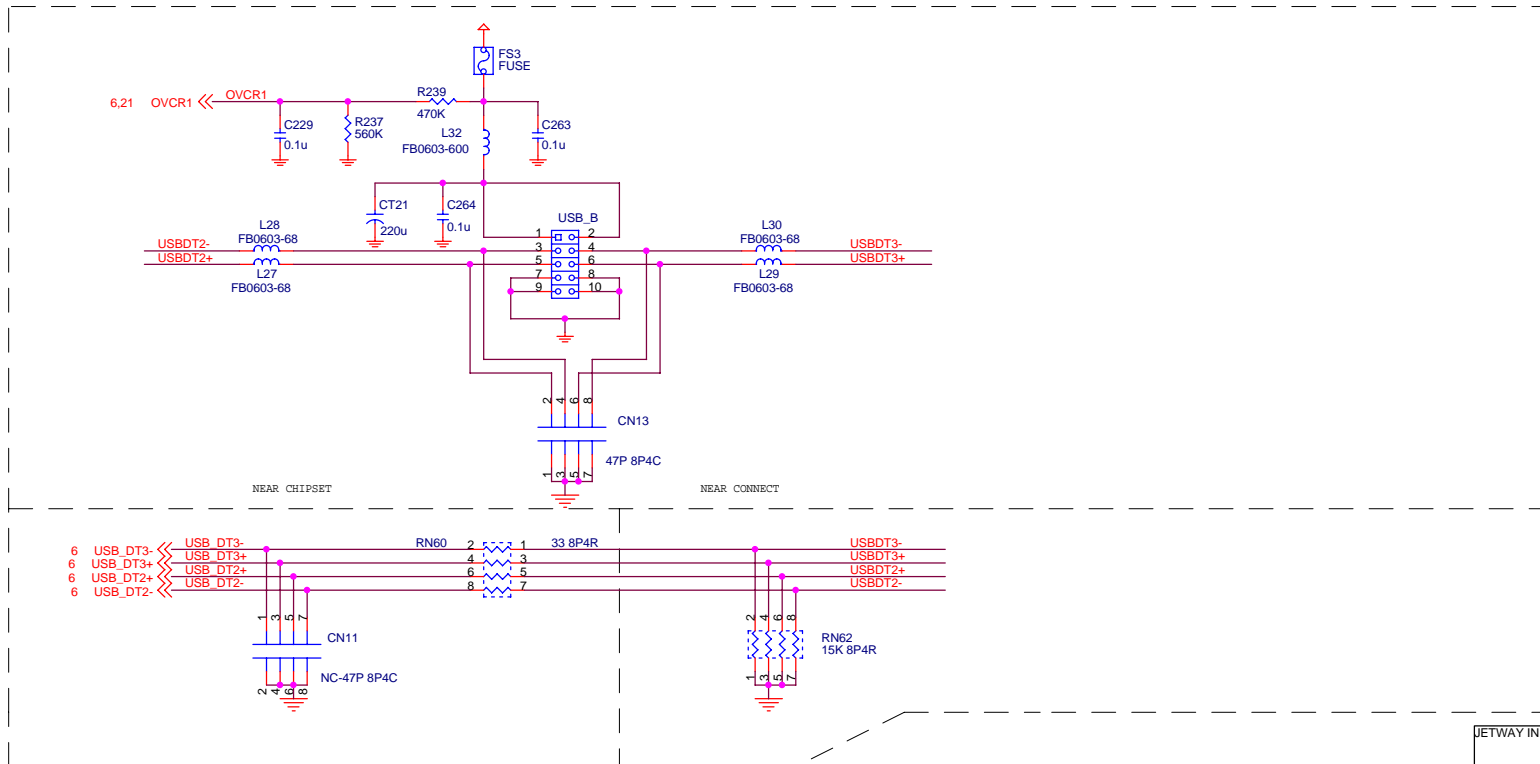


JETWAY INFORMATION		
Title	NORTH BRIDGE VT82C694X-A	
Size	Document Number	Rev
C	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 3 of 23

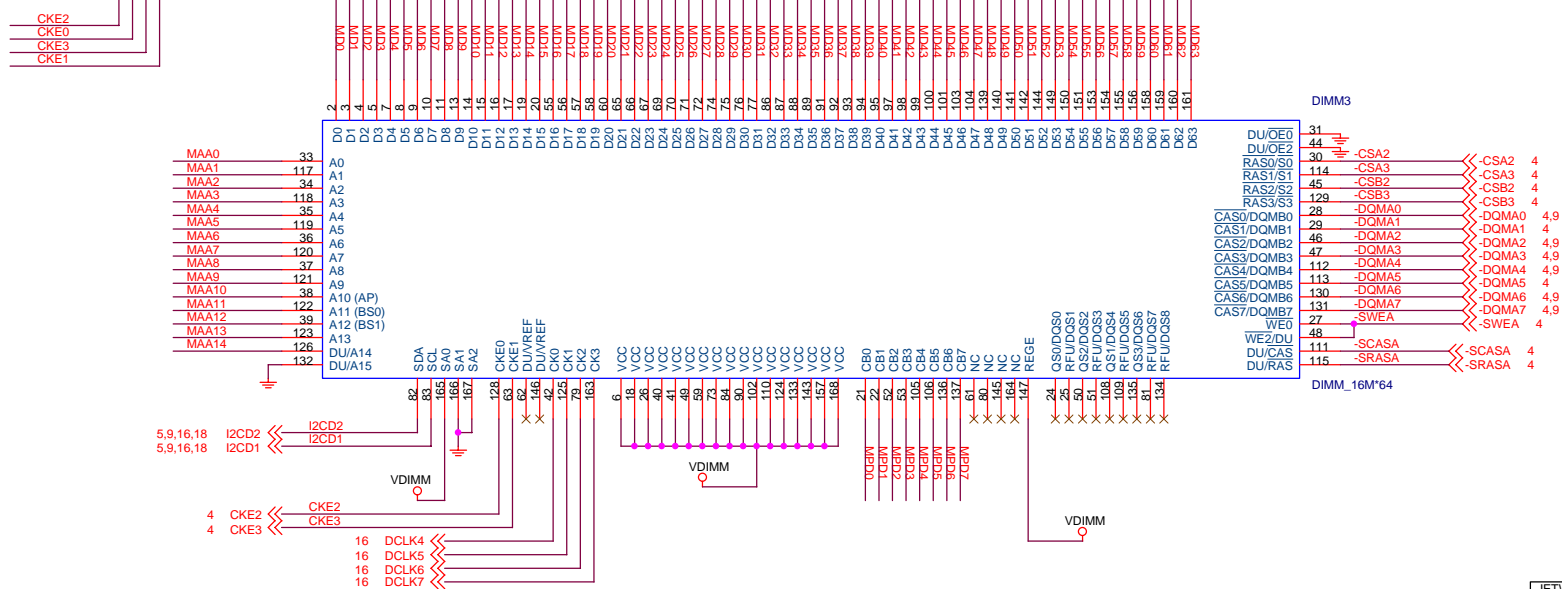
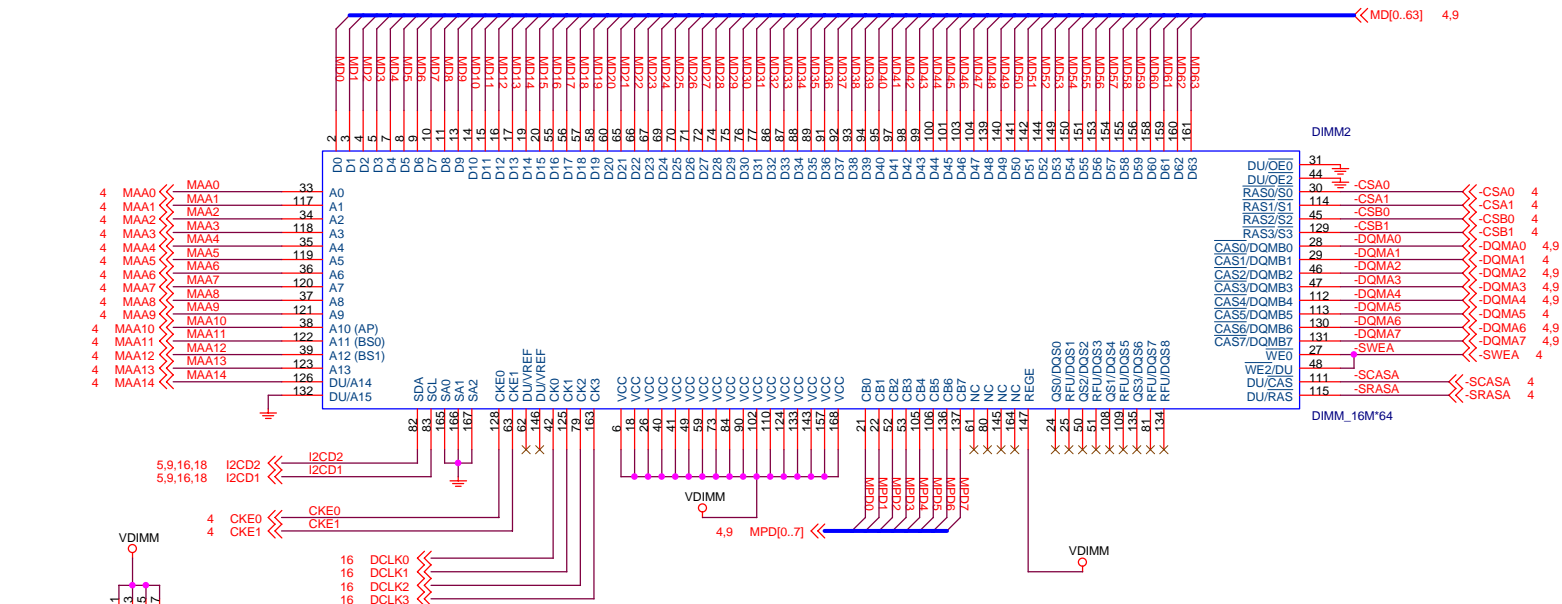


Freq/Ratio	1	2	3	4
3	ON	OFF	ON	ON
3.5	ON	OFF	OFF	ON
4	OFF	ON	ON	ON
4.5	OFF	ON	OFF	ON
5	OFF	OFF	ON	ON
5.5	OFF	OFF	OFF	ON
6	ON	ON	ON	OFF
6.5	ON	ON	OFF	OFF
7	ON	OFF	ON	OFF
7.5	ON	OFF	OFF	OFF
8	OFF	ON	ON	OFF
8.5	ON	OFF	ON	ON
9	ON	OFF	OFF	ON

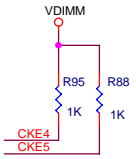
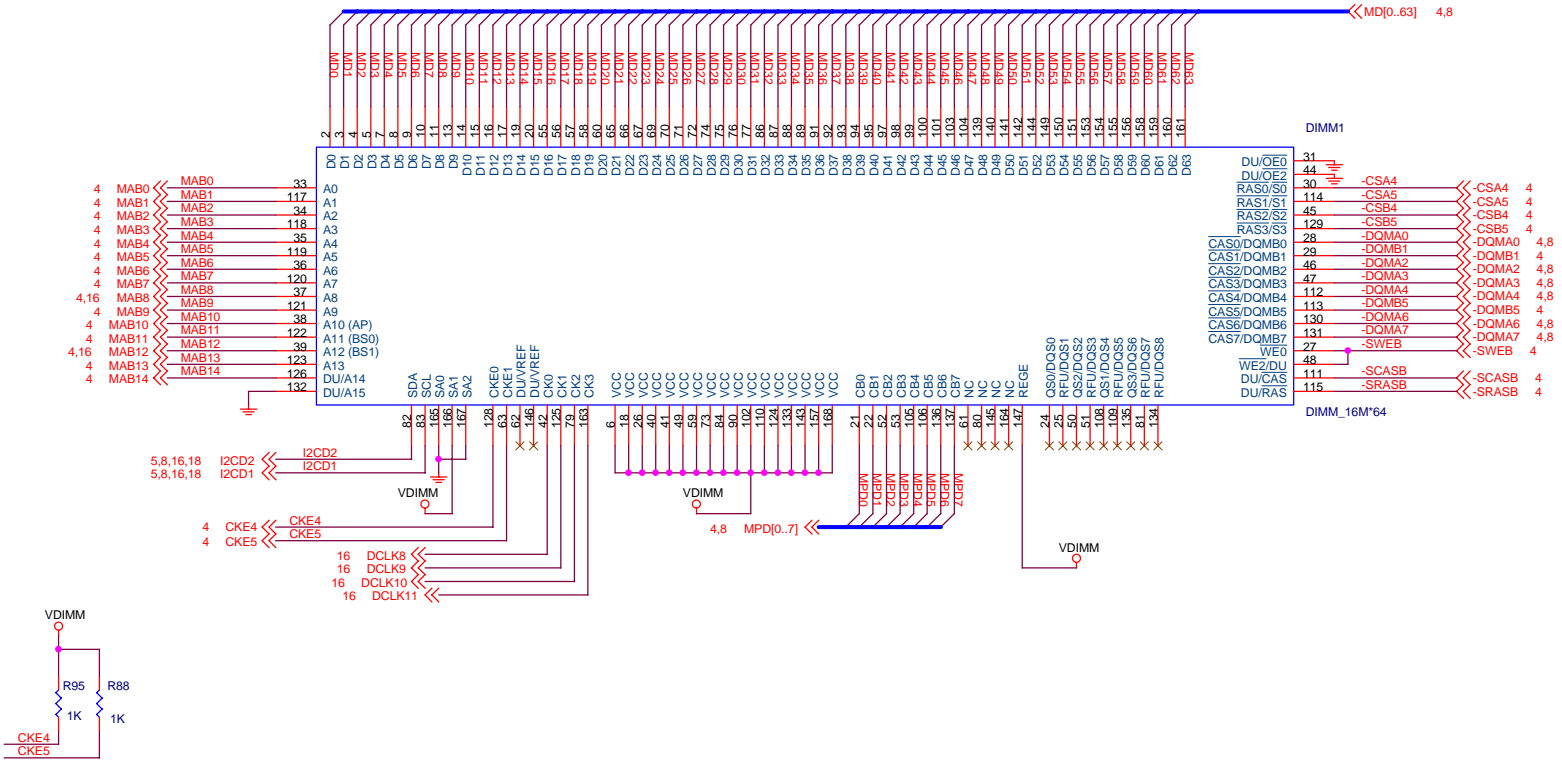
NEAR U10 POWER PIN



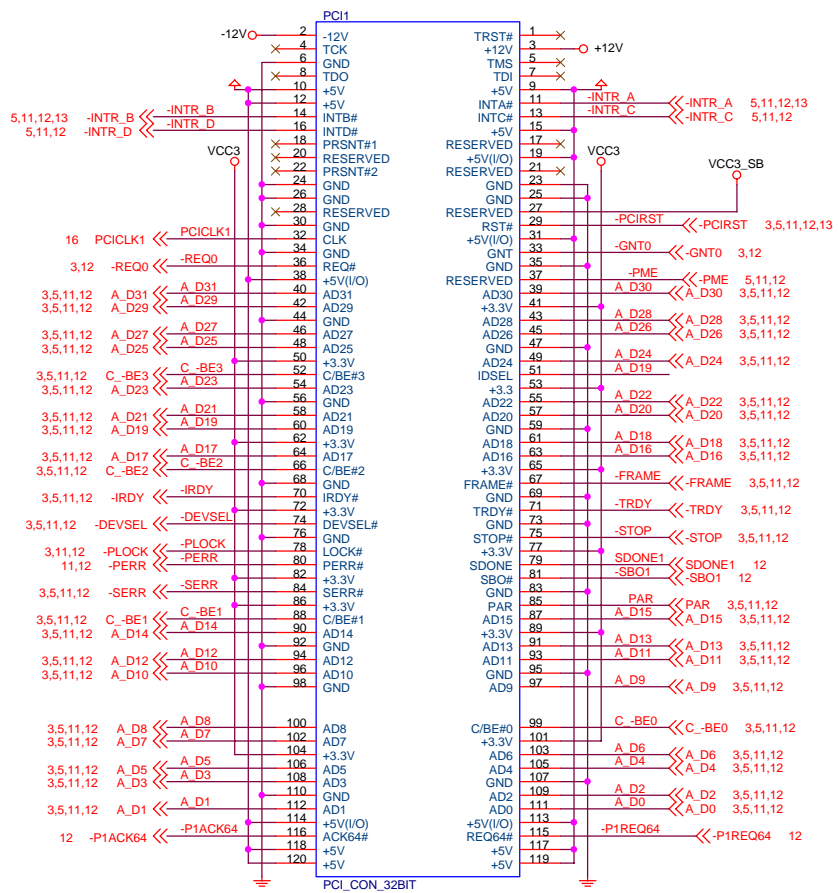
JETWAY INFORMATION		
Title	USB/FREQ. RATIO	
Size	Document Number	Rev
C	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 7 of 23



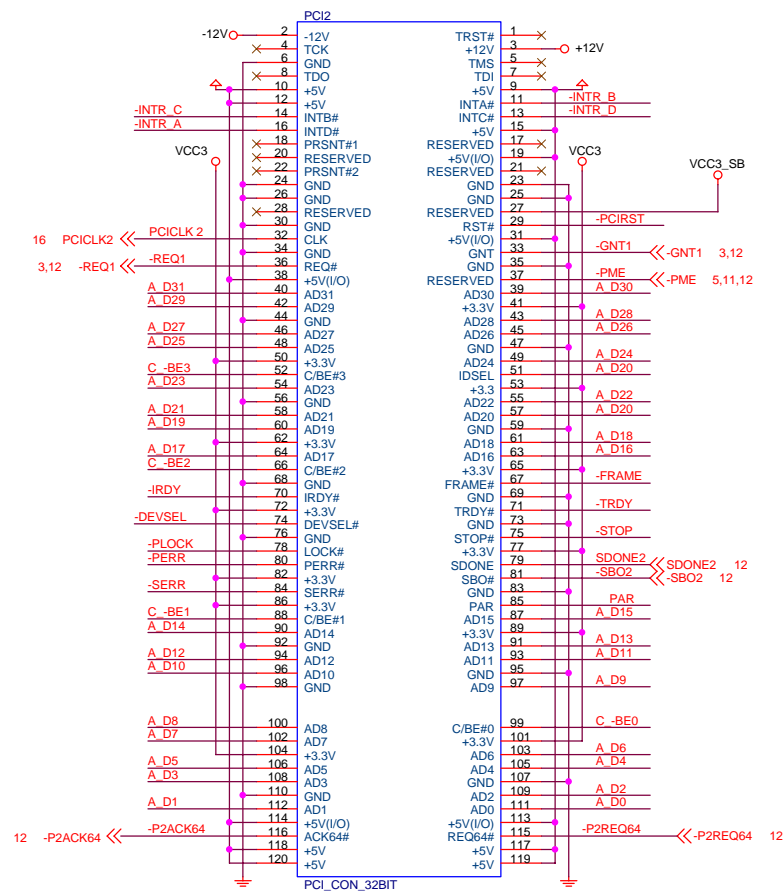
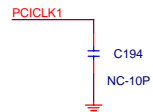
JETWAY INFORMATION			
Title	SDRAM		
Size	Document Number	Rev	
C	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	8 of 23



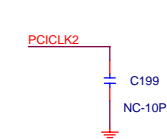
JETWAY INFORMATION			
Title	SDRAM		
Size C	Document Number	Rev	
C	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	9 of 23



AD: 19
A, B, C, D

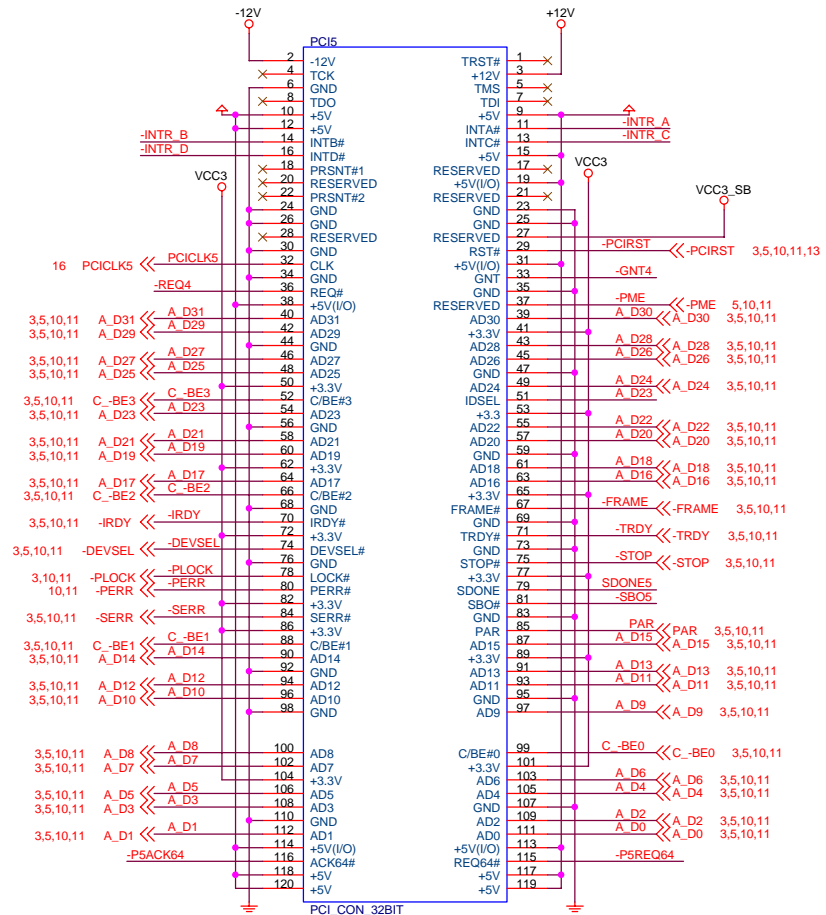
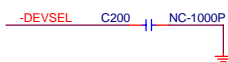
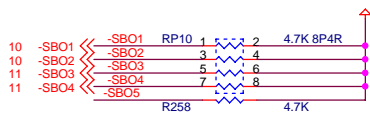
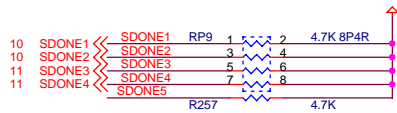
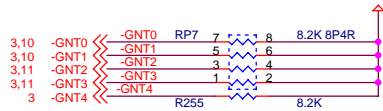
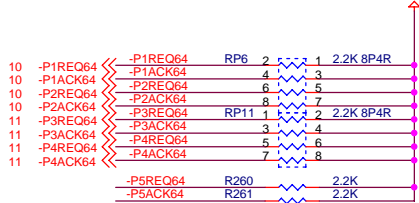
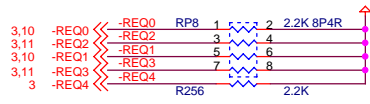
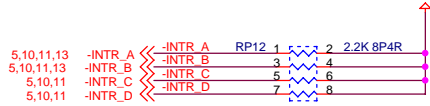
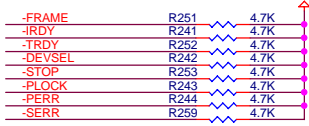


AD: 20
B, C, D, A

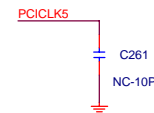


JETWAY INFORMATION

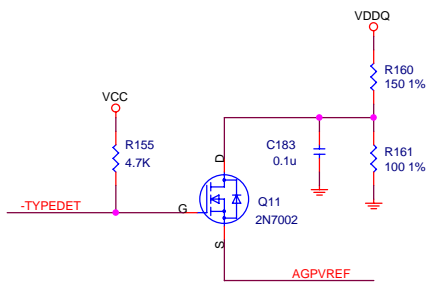
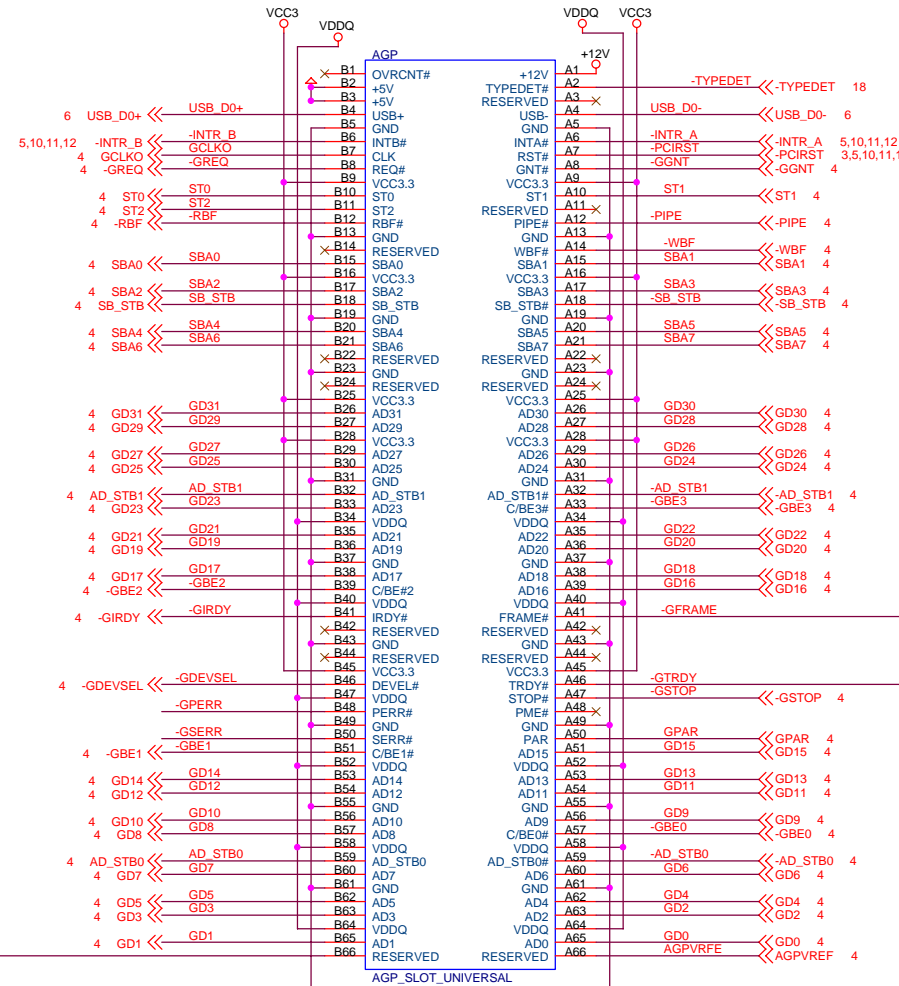
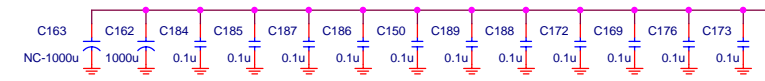
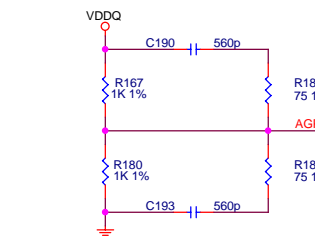
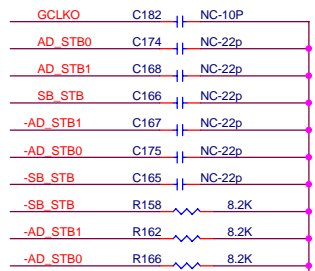
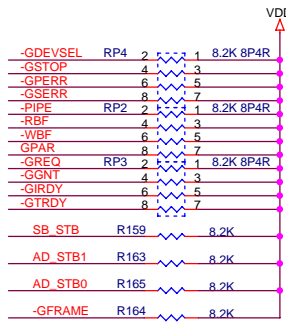
Title	PCI SLOT		
Size	Document Number	Rev	
	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	10 of 23



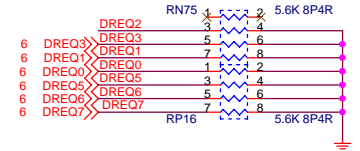
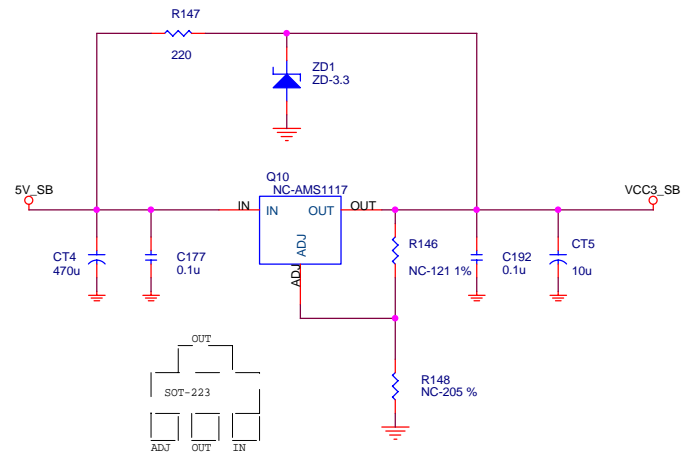
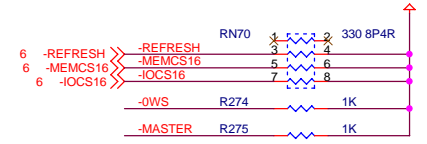
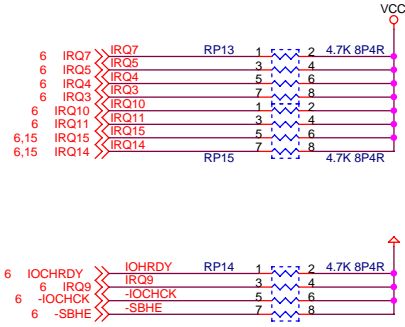
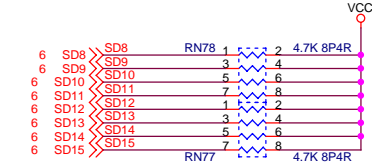
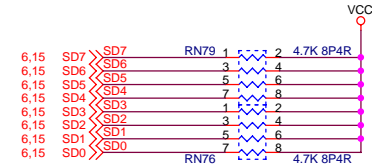
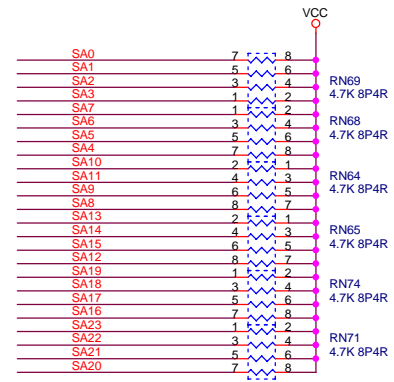
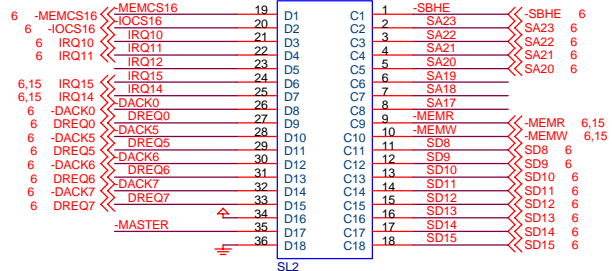
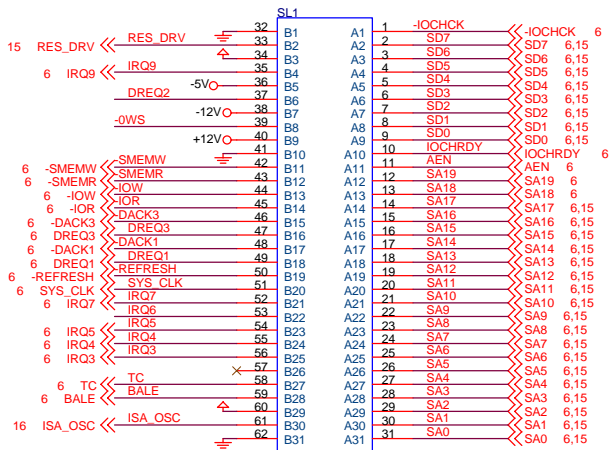
AD: 23
A, B, C, D



JETWAY INFORMATIN			
Title	PCI SLOT		
Size	Document Number	Rev	
C	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	12 of 23

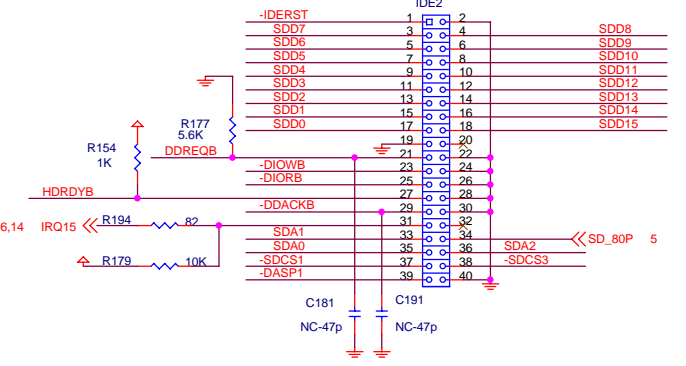
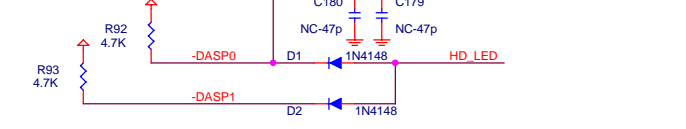
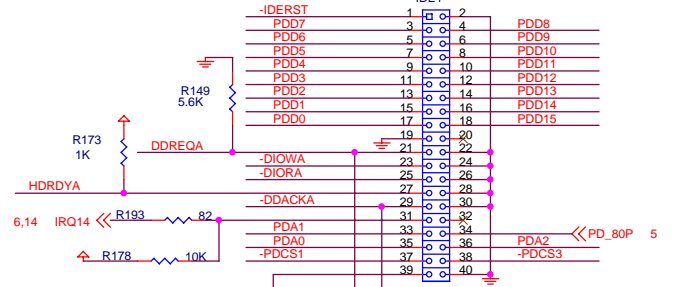
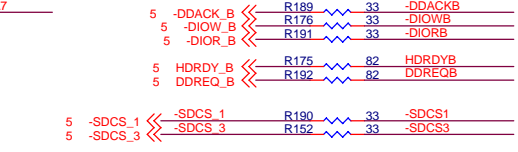
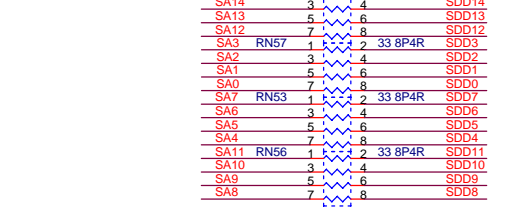
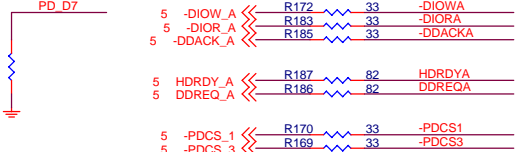
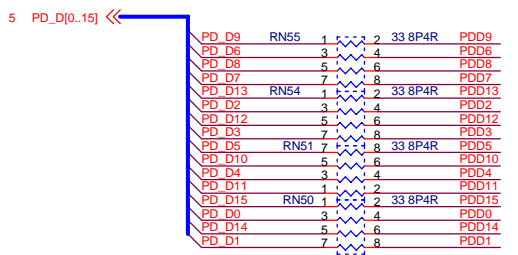
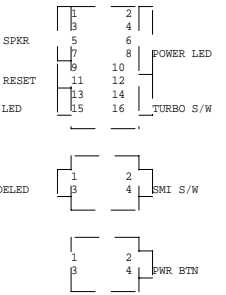
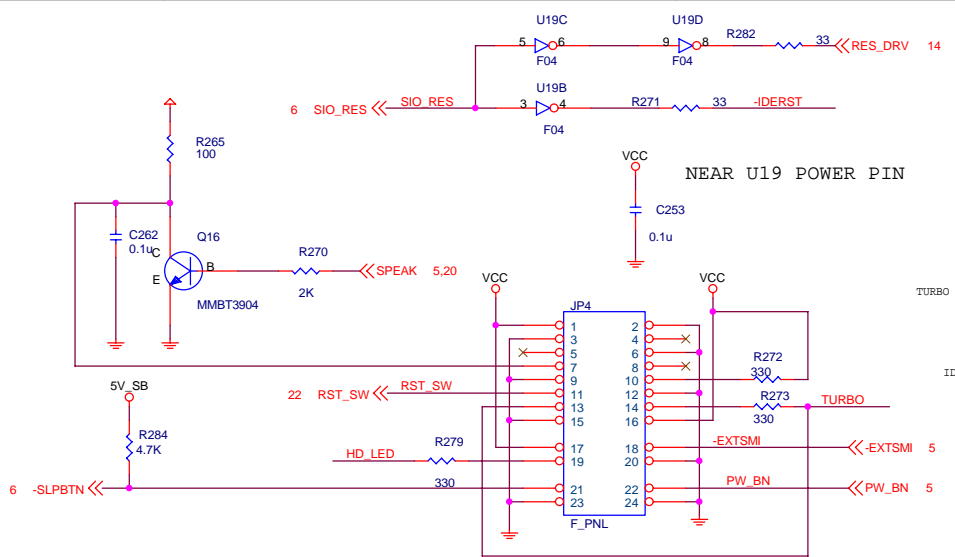
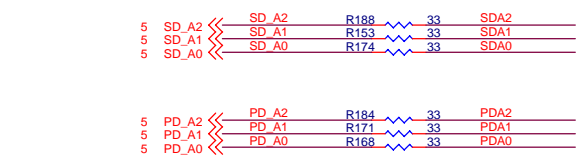
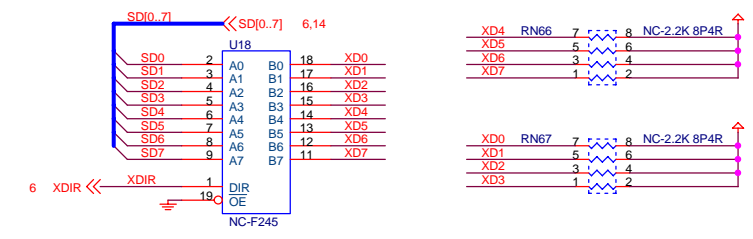
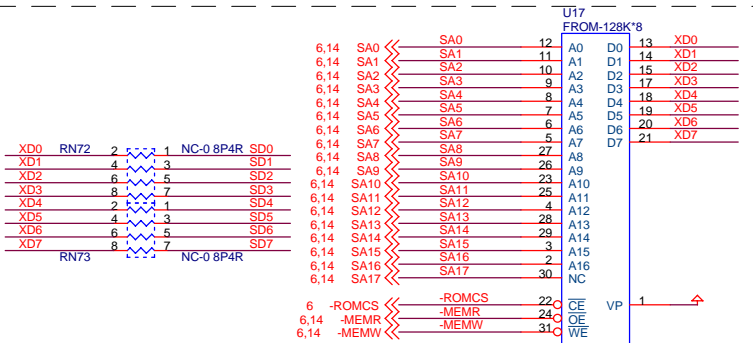
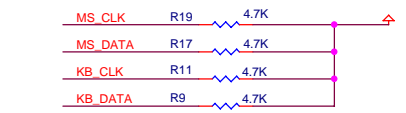
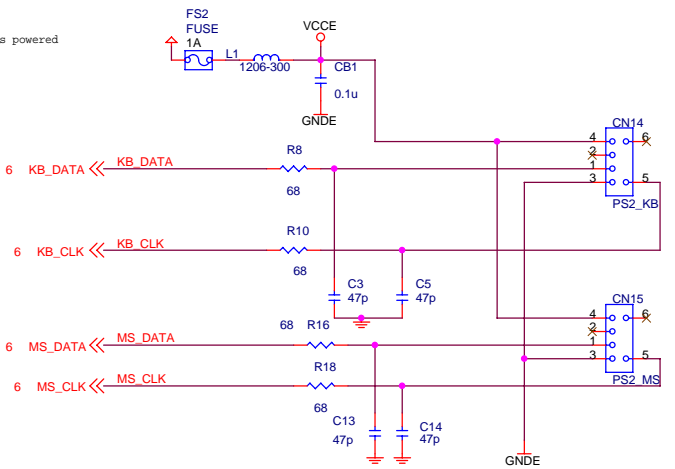


JETWAY INFORMATION			
Title	AGP 4X SLOT		
Size	Document Number	Rev	
C	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	13 of 23

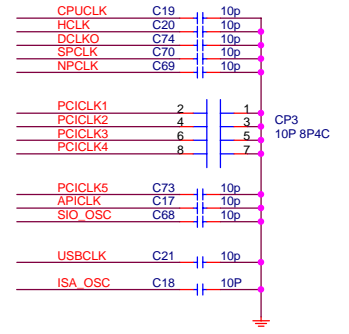
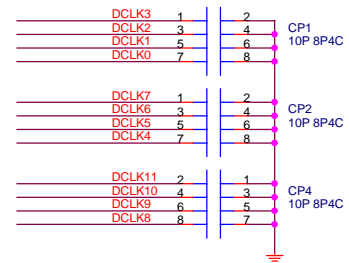
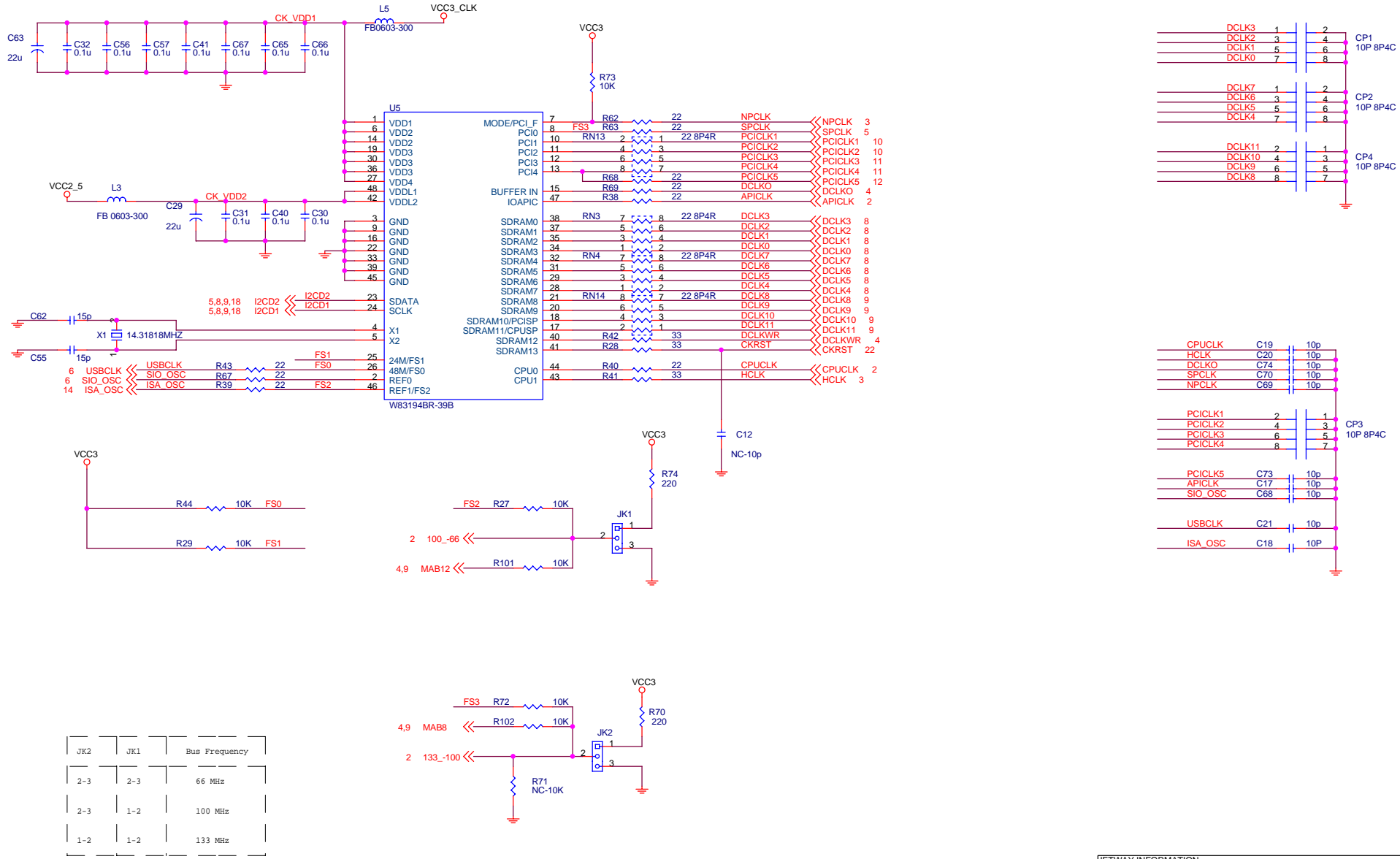


JETWAY INFORMATION			
Title	ISA PULL UP RESISTANCE		
Size	Document Number	Rev	
C	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	14 of 23

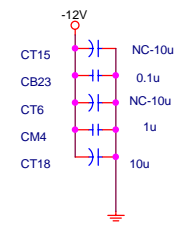
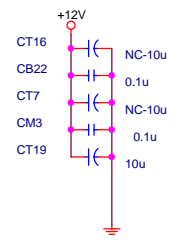
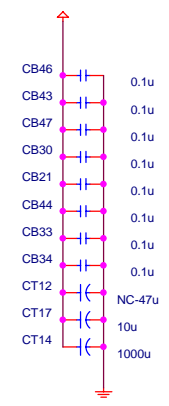
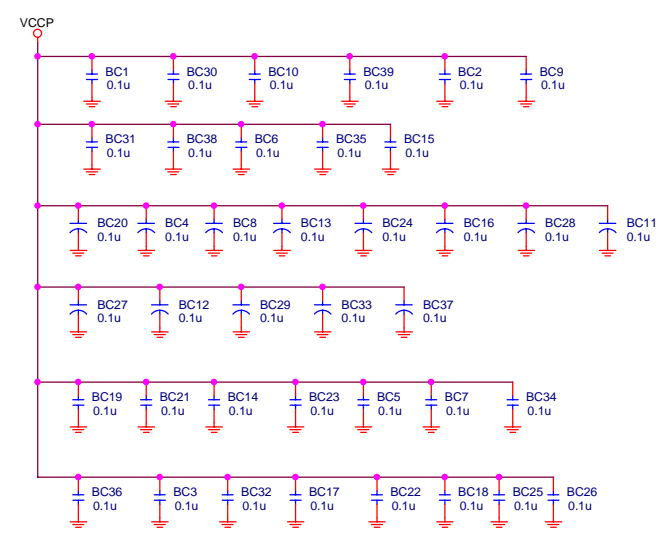
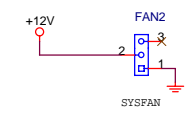
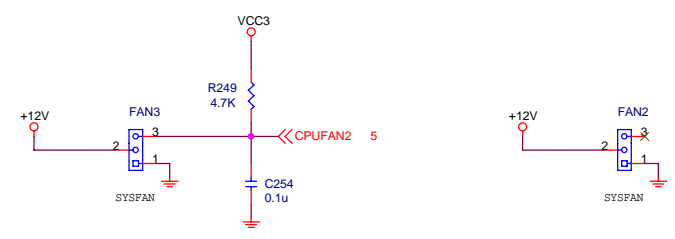
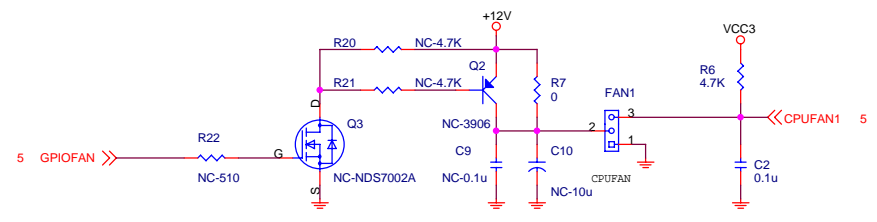
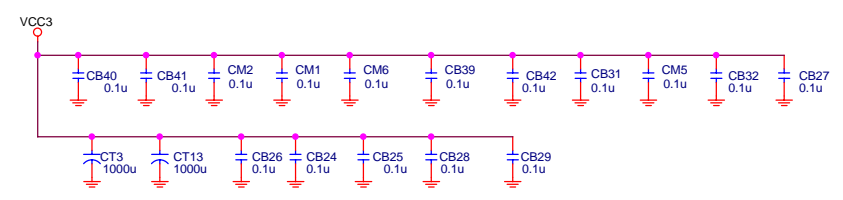
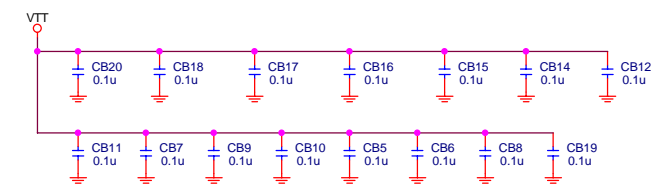
NOTE: F125 is powered by VCC3_SB



JETWAY INFORMATION		
Title	PERIPHERAL	
Size	Document Number	Rev
C	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 15 of 23

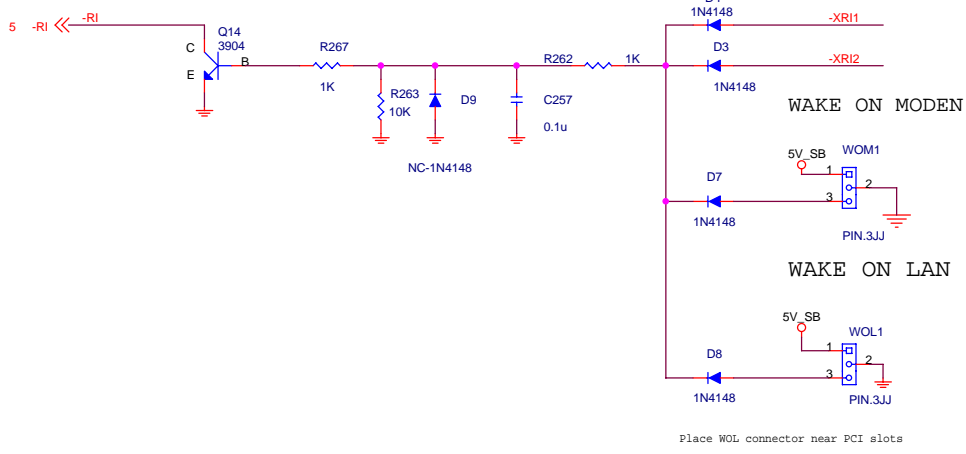


JK2	JK1	Bus Frequency
2-3	2-3	66 MHz
2-3	1-2	100 MHz
1-2	1-2	133 MHz



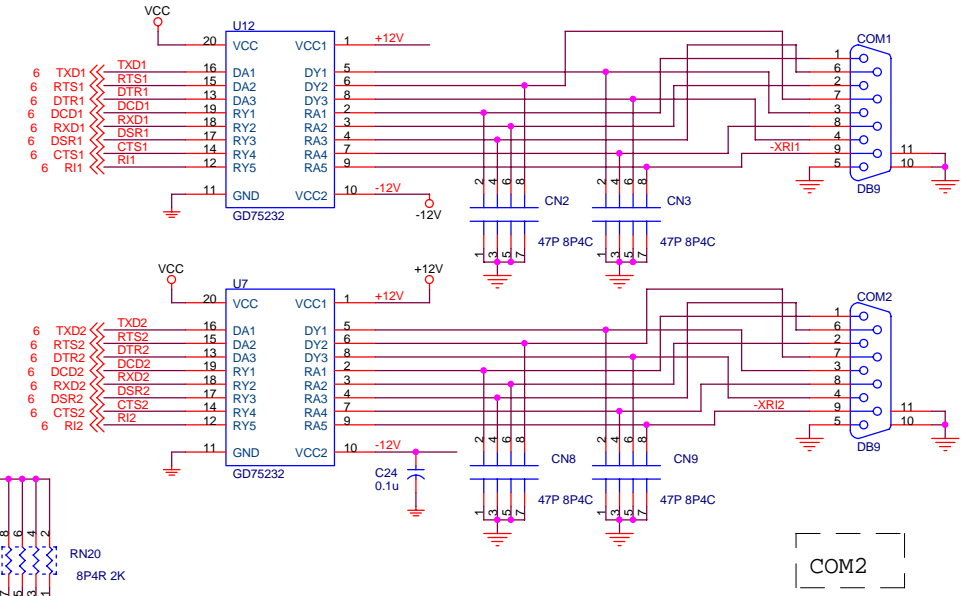
JETWAY INFORMATION		
Title	POWER CONNECTOR / BYPASS CAPACITOR	
Size	Document Number	Rev
C	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 17 of 23

RING IN

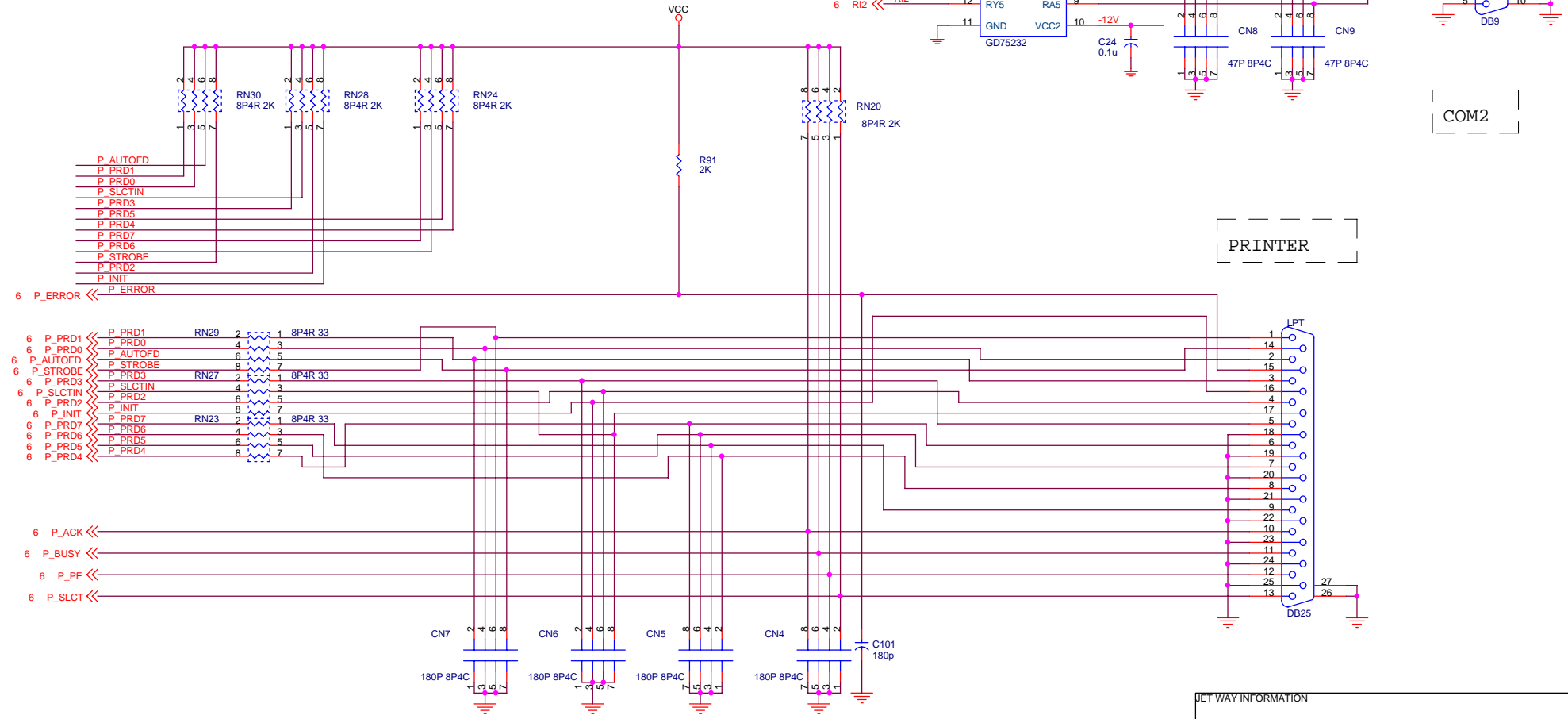


COM1 ,COM2

with Bipolar drivers and receivers

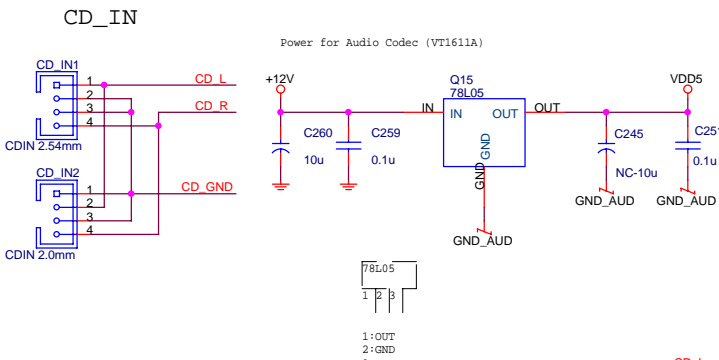


PRINTER

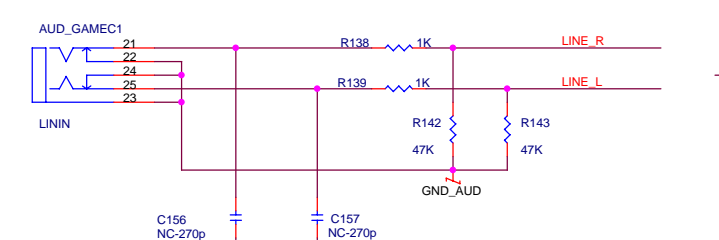


JET WAY INFORMATION		
Title	PRINTER / COM PORT	
Size	Document Number	Rev
	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 19 of 23

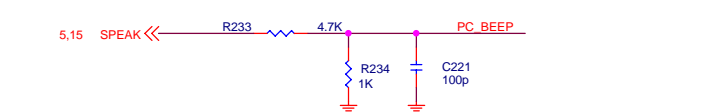
CD_IN



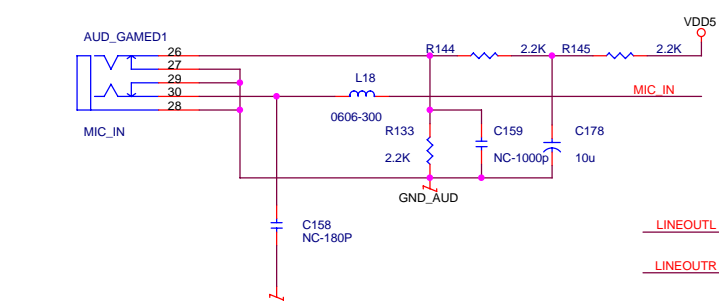
LINE_IN



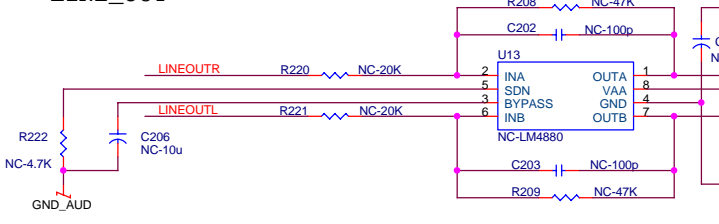
SPEAK_IN



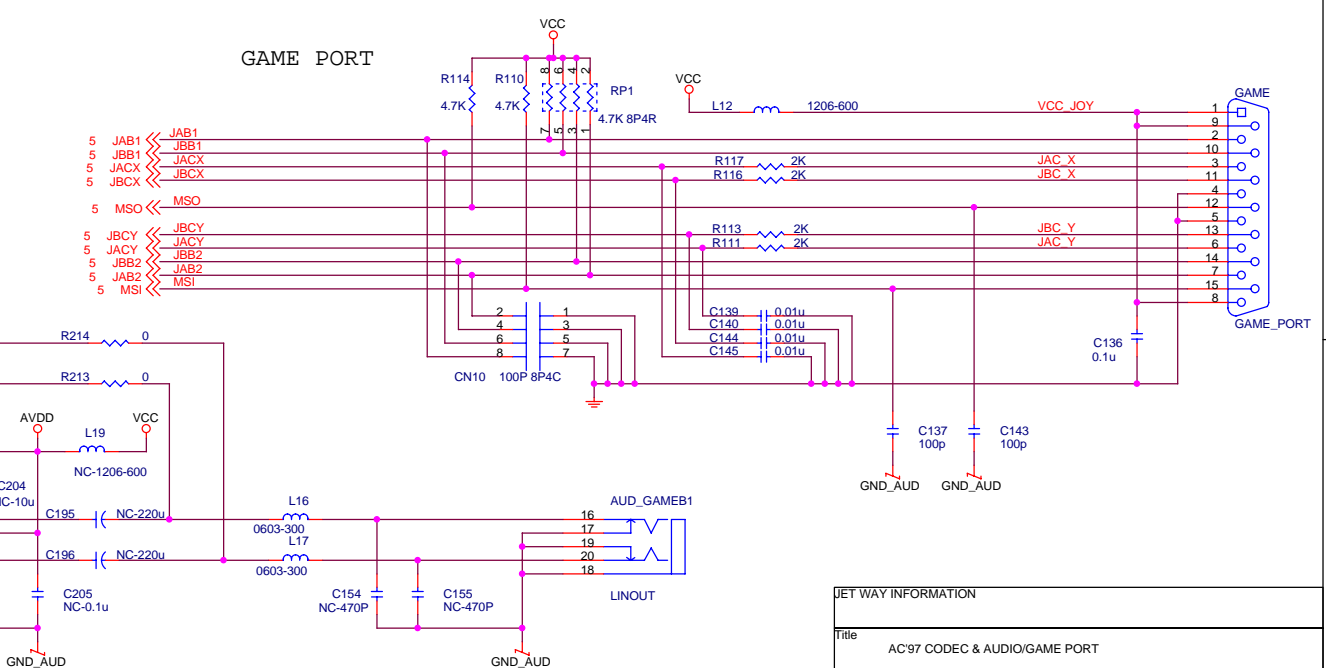
MIC_IN



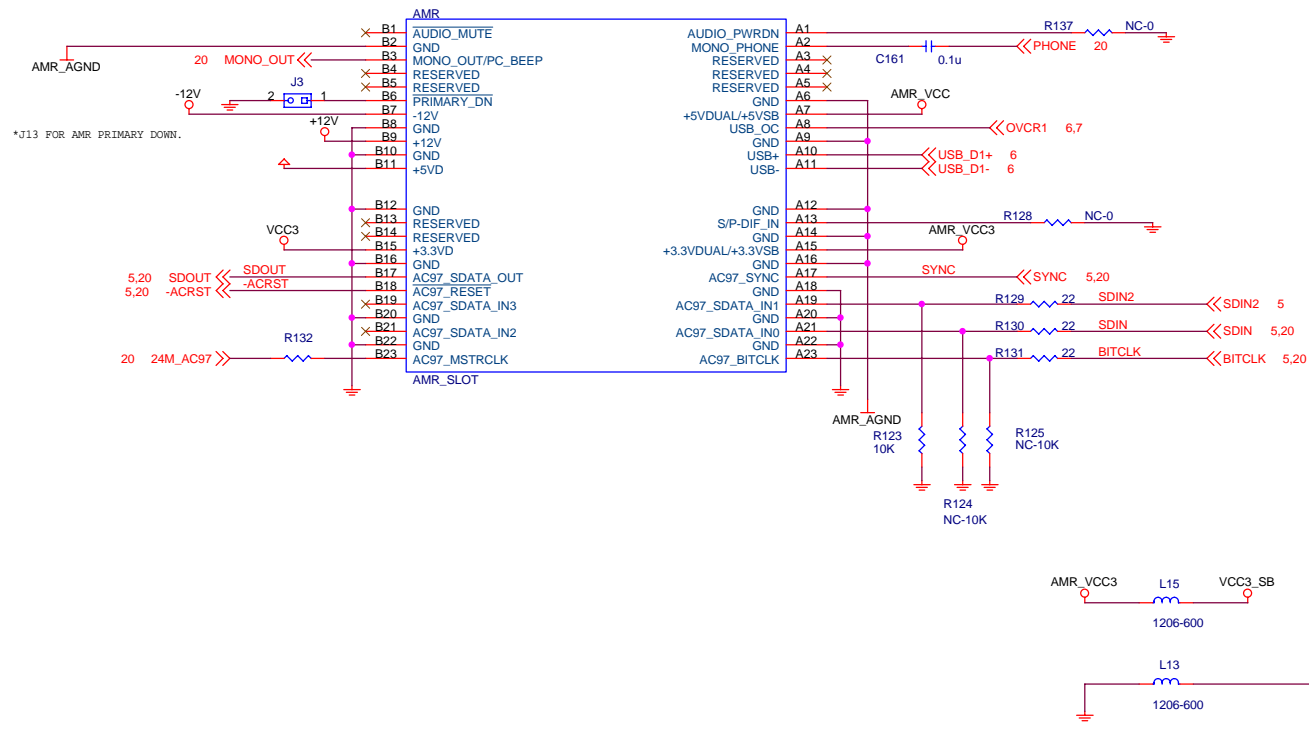
LINE_OUT



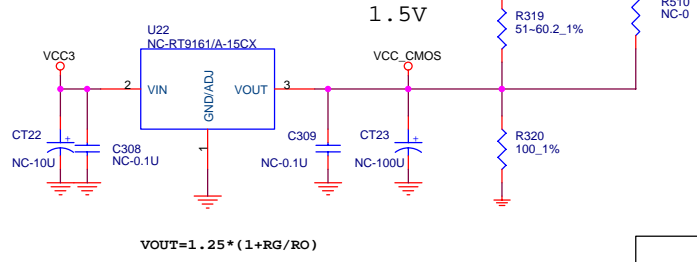
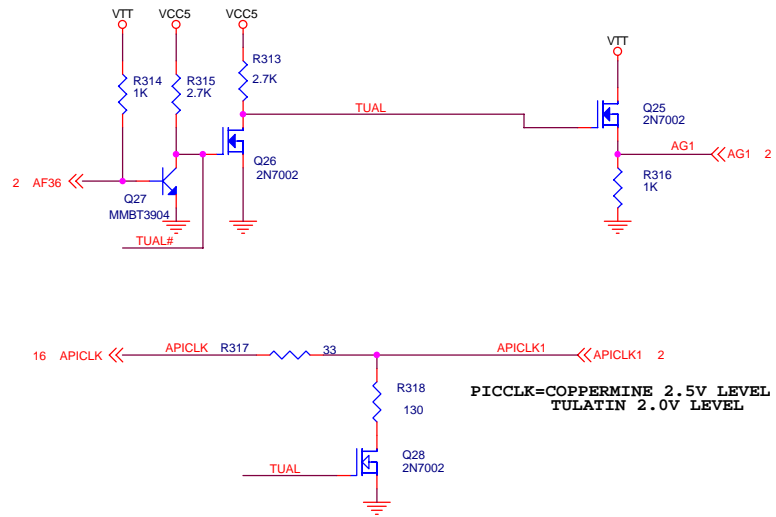
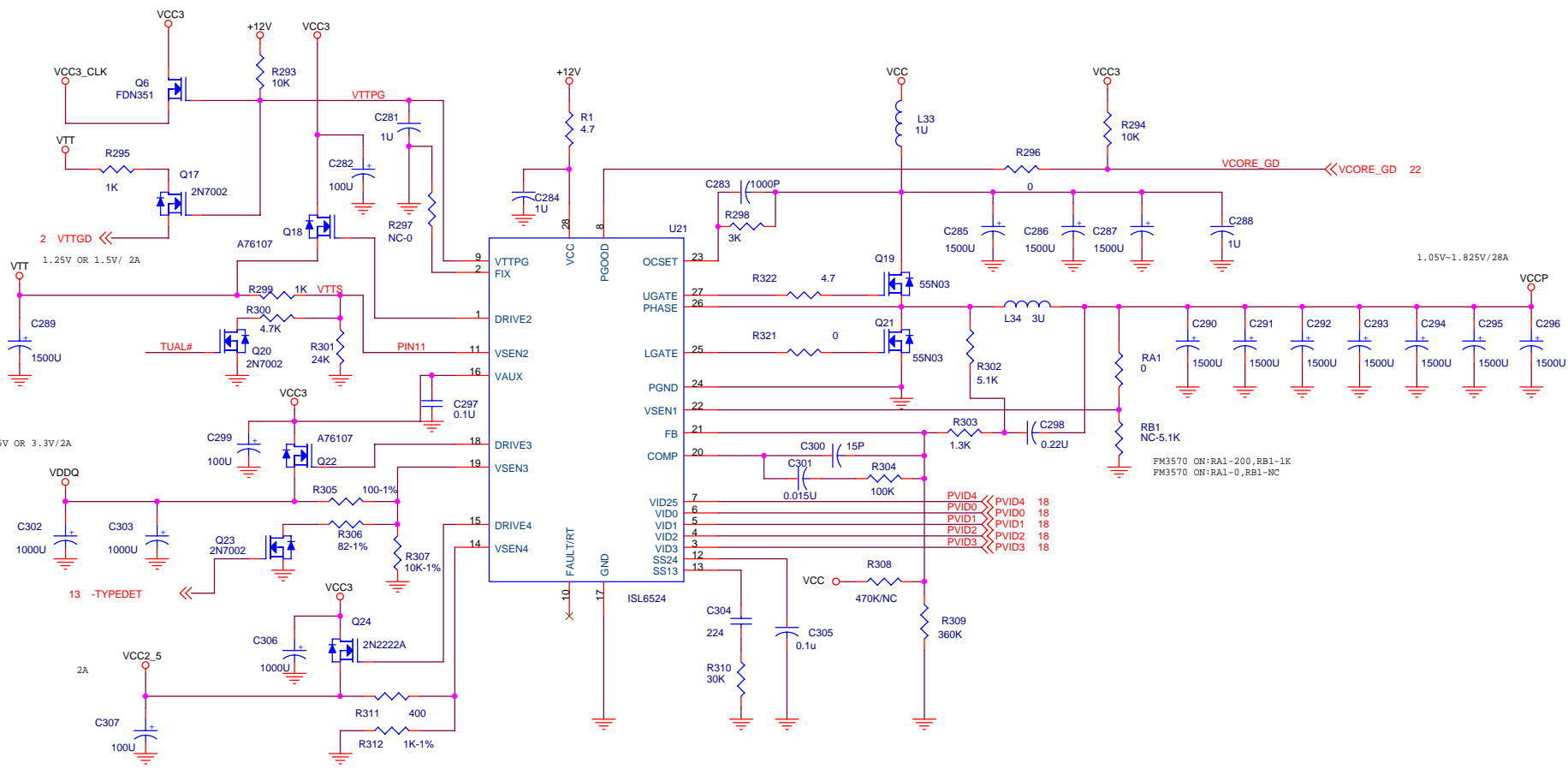
GAME PORT



JET WAY INFORMATION		
Title	AC'97 CODEC & AUDIO/GAME PORT	
Size	Document Number	Rev
C	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 20 of 23



JETWAY INFORMATION			
Title	ARM SLOT		
Size	Document Number	Rev	
C	J-694TAS REV:1.0	1.0	
Date:	Tuesday, May 07, 2002	Sheet	21 of 23



PICCLK=COPPERMINE 2.5V LEVEL
TULATIN 2.0V LEVEL

$$VOUT = 1.25 * (1 + RG / RO)$$

JETWAY INFORMATION		
Title	HIP6524	
Size	Document Number	Rev
B	J-694TAS REV:1.0	1.0
Date:	Tuesday, May 07, 2002	Sheet 23 of 23