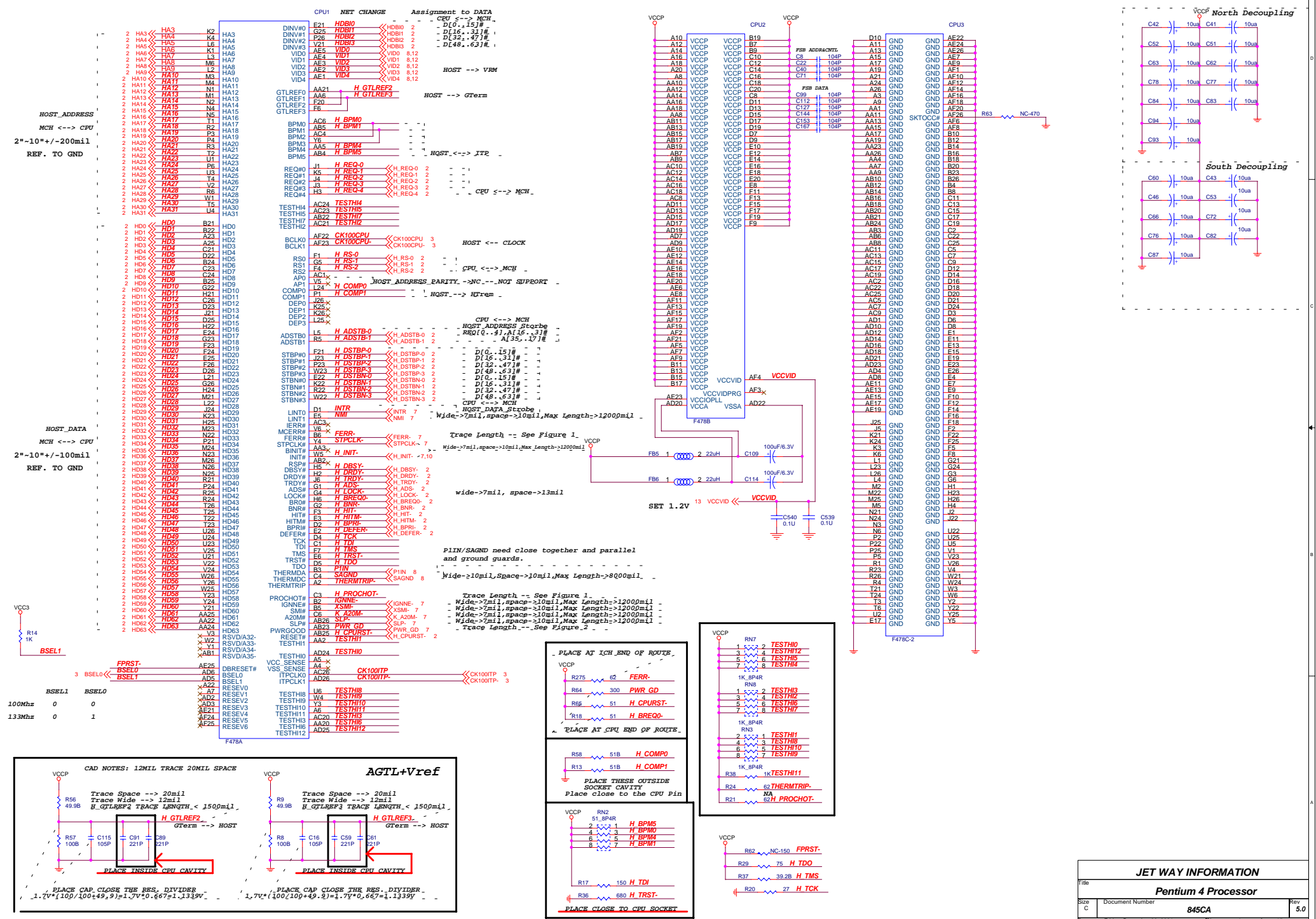
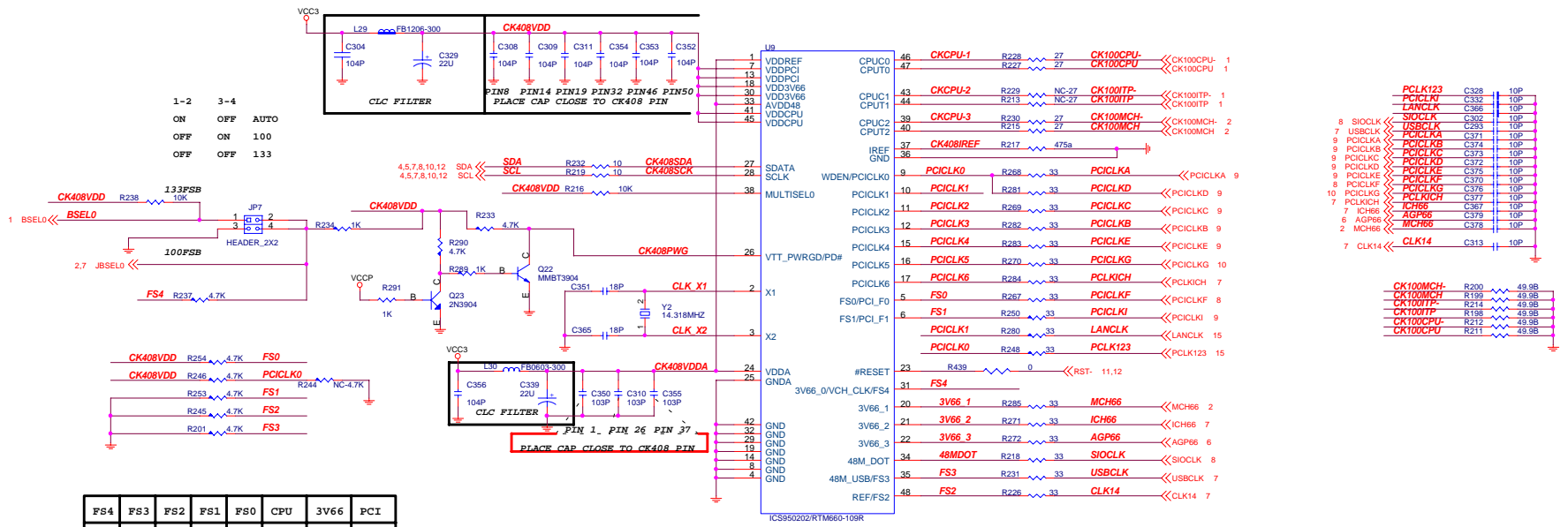


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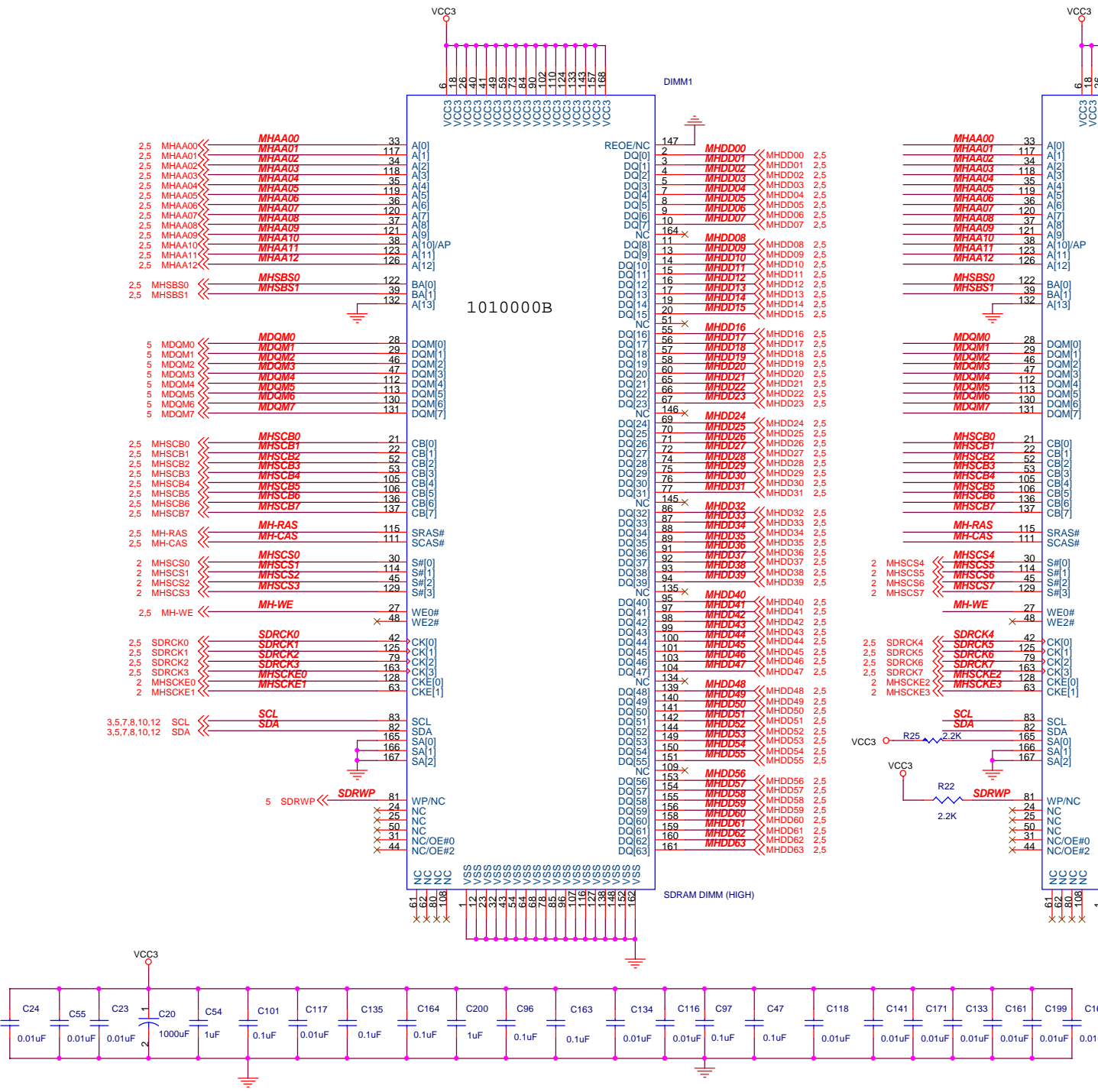
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1-2 3-4
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 OFF ON 100
 OFF OFF 133

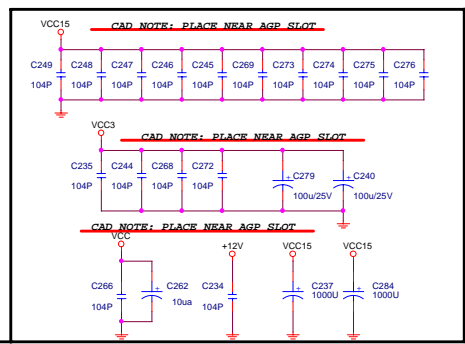
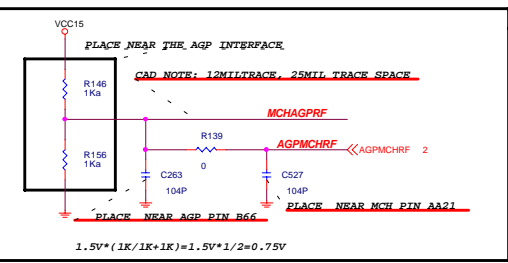
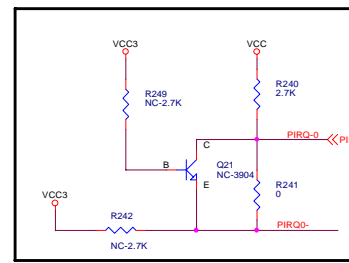
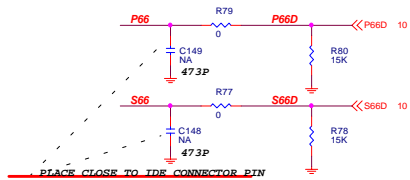
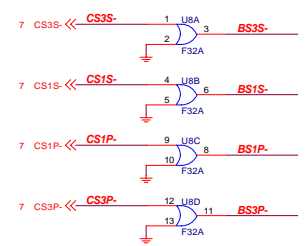
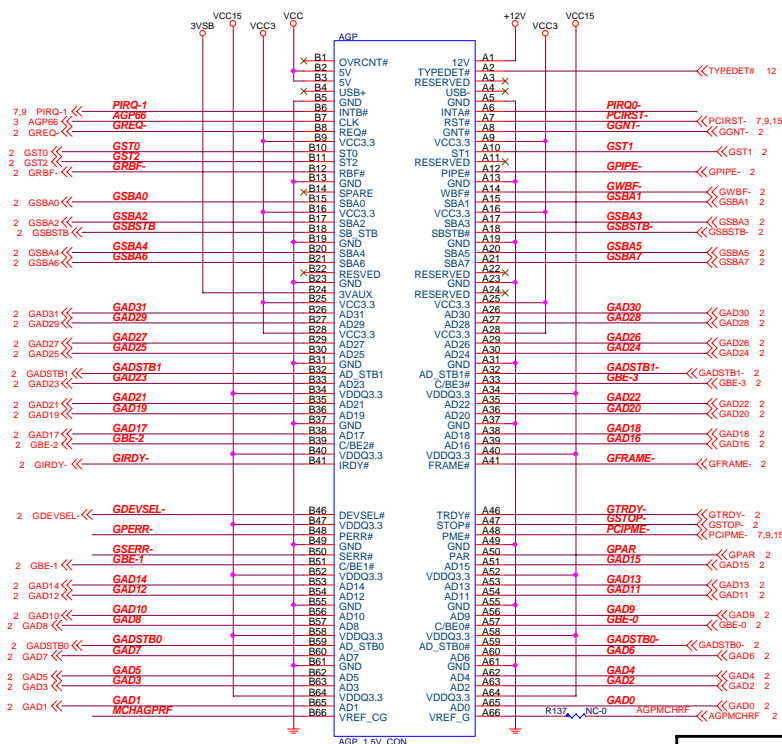
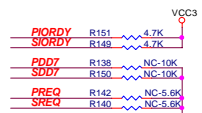
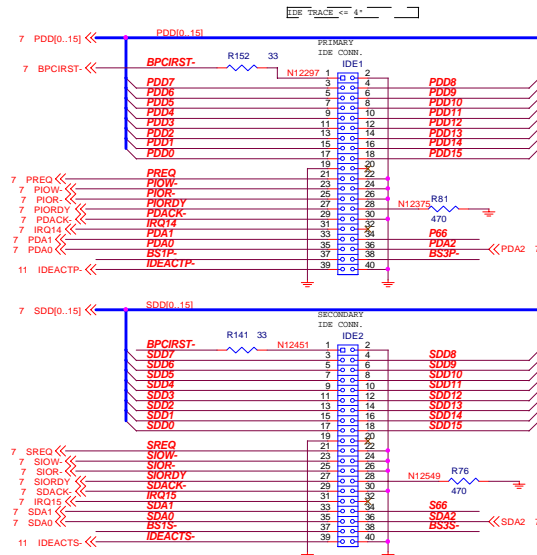


FS4	FS3	FS2	FS1	FS0	CPU	3V66	PCI
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1	0	0	0	1	133	66.6	33.3
1	1	1	1	0	66.6	66.6	33.3
1	1	1	1	1	200	66.6	33.3

PIN9: ICS950202 PULL HI: ENABLE WATCHDOG
 PIN9: RTM660-109R PULL HI: DISABLE WATCHDOG

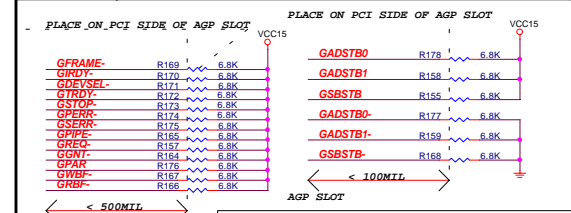


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Signals	Maximum Length	Trace Spacing (0.8 Mil Trace)	Length Mismatch	Relative To	Notes
1X Timing Domain	7.5"	10 Mils	N/A	N/A	NONE
2X/4X Timing Domain Set#1	6"	15 Mils	+ / - 0.25"	GADSTB0 AND GADSTB0- GADSTB0-	Must be same length
2X/4X Timing Domain Set#2	6"	15 Mils	+ / - 0.25"	GADSTB1 AND GADSTB1- GADSTB1-	Must be same length
2X/4X Timing Domain Set#3	6"	15 Mils	+ / - 0.25"	GSBSTB AND GSBSTB- GSBSTB-	Must be same length

IX TIMING DOMAIN SIGNALS	MISCELLANEOUS SIGNALS	2X/4X TIMING DOMAIN
GFRAME-	USBA0+	GAD [0 . 3]
GDEVEL-	USBA0P-	GBE [0 . 3]
GIRDY-	AGPOC-	ROUTE 5 ON 15 FOR STROB SIGNAL (1 : 3)
GTRDY-	PCIPME-	STROBE SIGNAL TO OTHERS 5 ON 20 (1 : 4)
GSTOP-	TYPEDET-	GADSTB0
GPAR	GPERR-	GADSTB0-
GSDY-	GSERR-	GADSTB1
GQNT-	GBE1-	GADSTB1-
GPIPE-	PIRQ0-	GSBA [0 . 7]
GWBFB-	GRBF-	GSBSTB
GMBF-		GSBSTB-
GST [1 . 2]		



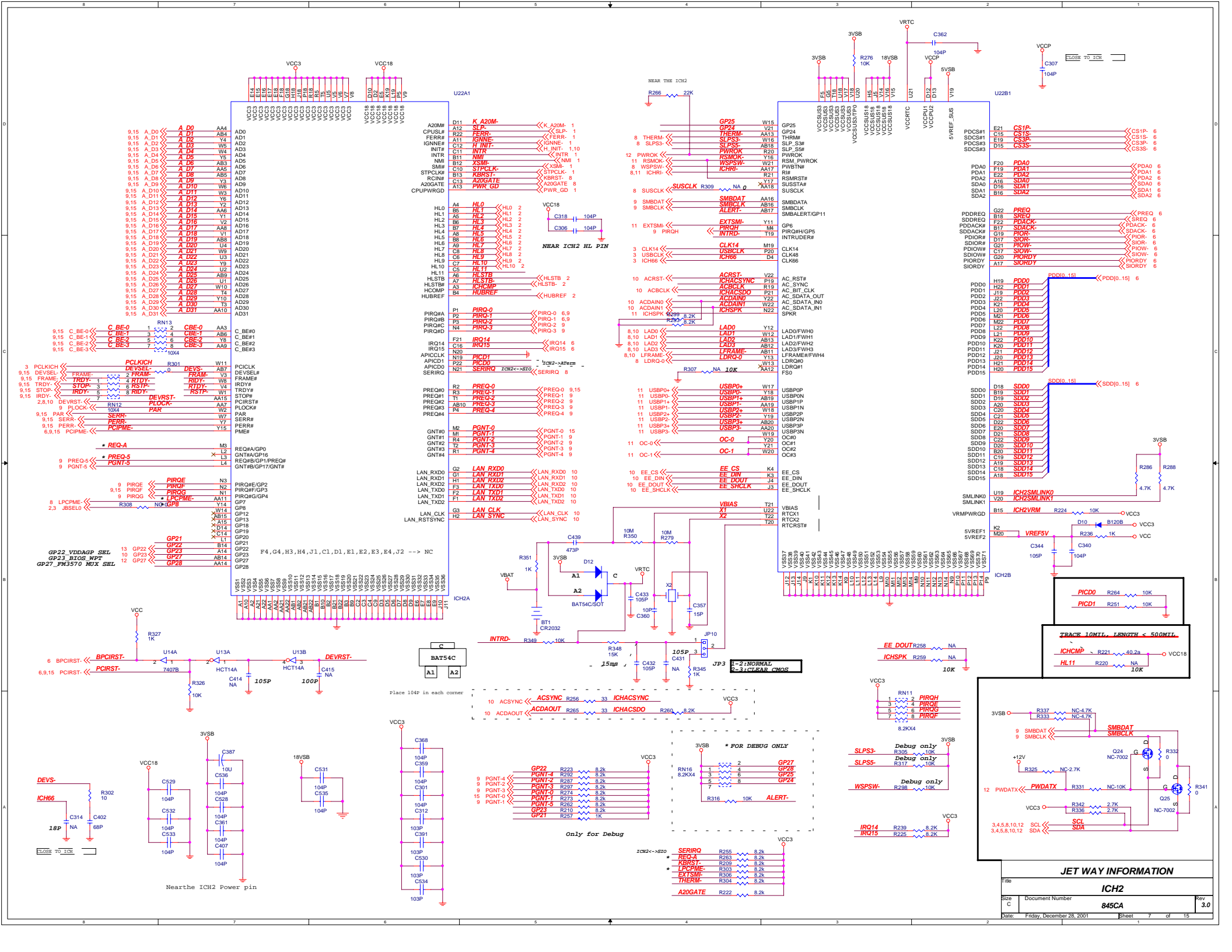
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3.0



9.15 A.D0	A D0	AA4	ADD
9.15 A.D1	A D1	AB4	AD1
9.15 A.D2	A D2	Y4	AD2
9.15 A.D3	A D3	W5	AD3
9.15 A.D4	A D4	Y5	AD4
9.15 A.D5	A D5	Y1	AD5
9.15 A.D6	A D6	AB3	AD6
9.15 A.D7	A D7	AB5	AD7
9.15 A.D8	A D8	Y3	AD8
9.15 A.D9	A D9	W6	AD9
9.15 A.D10	A D10	W3	AD10
9.15 A.D11	A D11	AD11	AD11
9.15 A.D12	A D12	Y2	AD12
9.15 A.D13	A D13	AA6	AD13
9.15 A.D14	A D14	Y4	AD14
9.15 A.D15	A D15	Y1	AD15
9.15 A.D16	A D16	AA8	AD16
9.15 A.D17	A D17	Y1	AD17
9.15 A.D18	A D18	AB8	AD18
9.15 A.D19	A D19	AB9	AD19
9.15 A.D20	A D20	U4	AD20
9.15 A.D21	A D21	U3	AD21
9.15 A.D22	A D22	U2	AD22
9.15 A.D23	A D23	U9	AD23
9.15 A.D24	A D24	U2	AD24
9.15 A.D25	A D25	U2	AD25
9.15 A.D26	A D26	W10	AD26
9.15 A.D27	A D27	T4	AD27
9.15 A.D28	A D28	Y10	AD28
9.15 A.D29	A D29	T9	AD29
9.15 A.D30	A D30	T9	AD30
9.15 A.D31	A D31	AA10	AD31

9.15 C.BE-0	C BE-0	1	RN13	C.BE-0	AA3	C.BE-0
9.15 C.BE-1	C BE-1	3	4	C.BE-1	AB6	C.BE-1
9.15 C.BE-2	C BE-2	5	6	C.BE-2	Y8	C.BE-2
9.15 C.BE-3	C BE-3	7	8	C.BE-3	AA9	C.BE-3

3	PCLKICH	PCLKICH	R301	W11	PCCLK
9.15	DEVSEL	DEVSEL	0	DEVSEL	AB7
9.15	FRAME	FRAME	2	FRAME	W3
9.15	TRDY	TRDY	1	TRDY	W6
9.15	STOP	STOP	3	TRDY	VA
9.15	IRDY	IRDY	5	IRDY	W1
9.15	IRDY	IRDY	8	IRDY	W1
2.8,10	DEVST	DEVST	10X4	AA15	STOP
9	PLOCK	PLOCK	RN12	AA7	PLOCK#
9.15	PAR	SERR	W7	PAR	SERR#
9.15	PERR	PERR	Y7	PERR#	PERR#
6.9,15	PCPME	PCPME	Y15	PME#	PME#

9	PREQ-0	PREQ-0	M3	REQ#A/GP9
9	PREQ-5	PREQ-5	X L2	GNT#B/GP16
9	PNT-5	PNT-5	L4	REQ#B/GP1/PREQ#
9	PNT-5	PNT-5	L4	GNT#B/GP1/GNT#

9	PIROE	PIROE	N3	PIROE/GP2
9.15	PIROF	PIROF	N2	PIROF/GP3
9	PIROG	PIROG	N1	PIROG/GP4
9	PIROH	PIROH	AA11	PIROH/GP5
2.3	JBSSEL0	JBSSEL0	R308	NO-IGPB

13	GP22	GP22	B14	GP21
10	GP23	GP23	A14	GP22
12	GP27	GP27	AB14	GP23
12	GP27	GP27	AA14	GP24
12	GP27	GP27	AA14	GP28

6	BPCIRST	BPCIRST	U14A	U13A	U13B	DEVIRST
6.9,15	PCIRST	PCIRST	U14A	U13A	U13B	DEVIRST

A20M#	D11	K A20M-	<<K A20M- 1
CPUSL#	A12	SLP	<<SLP- 1
IGNNE#	R22	FERR	<<FERR- 1
INTR#	C12	H INIT	<<IGNNE- 1
INTR	C11	INTR	<<INTR 1
NMI	B12	XSMI	<<NMI 1
SMI#	C13	ADSGATE	<<SMI# 1
STPCLK#	B13	ADSGATE	<<STPCLK- 1
RCM#	A20	ADSGATE	<<RCMST- 6
ADSGATE	A13	PWR GD	<<ADSGATE 8
CPUPWRGD	A13	PWR GD	<<PWR GD 1

A4	HL0	<<HL0 2
B5	HL1	<<HL1 2
H4	HL2	<<HL2 2
B7	HL4	<<HL4 2
A8	HL5	<<HL5 2
H5	HL6	<<HL6 2
H7	HL7	<<HL7 2
C8	HL8	<<HL8 2
H8	HL9	<<HL9 2
C7	HL10	<<HL10 2
C5	HL11	<<HL11 2
H11	HL12	<<HL12 2
H12	HL13	<<HL13 2
H13	HL14	<<HL14 2
H14	HL15	<<HL15 2
H15	HL16	<<HL16 2
H16	HL17	<<HL17 2
H17	HL18	<<HL18 2
H18	HL19	<<HL19 2
H19	HL20	<<HL20 2
H20	HL21	<<HL21 2
H21	HL22	<<HL22 2
H22	HL23	<<HL23 2
H23	HL24	<<HL24 2
H24	HL25	<<HL25 2
H25	HL26	<<HL26 2
H26	HL27	<<HL27 2
H27	HL28	<<HL28 2
H28	HL29	<<HL29 2
H29	HL30	<<HL30 2
H30	HL31	<<HL31 2

P1	PIRO-0	<<PIRO-0 6.9
P2	PIRO-1	<<PIRO-1 6.9
P3	PIRO-2	<<PIRO-2 6.9
P4	PIRO-3	<<PIRO-3 9
F21	IRO14	<<IRO14 6
C16	IRQ15	<<IRQ15 6
N20	IRQ15	<<IRQ15 6
N19	ICD1	<<ICD1 2
N21	SERRIO	<<SERRIO 8
R2	PREQ-0	<<PREQ-0 9.15
R3	PREQ-1	<<PREQ-1 9
AB10	PREQ-2	<<PREQ-2 9
PA	PREQ-3	<<PREQ-3 9
PREQ4	PREQ-4	<<PREQ-4 9

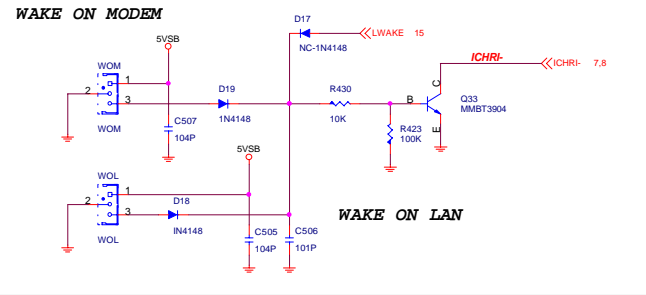
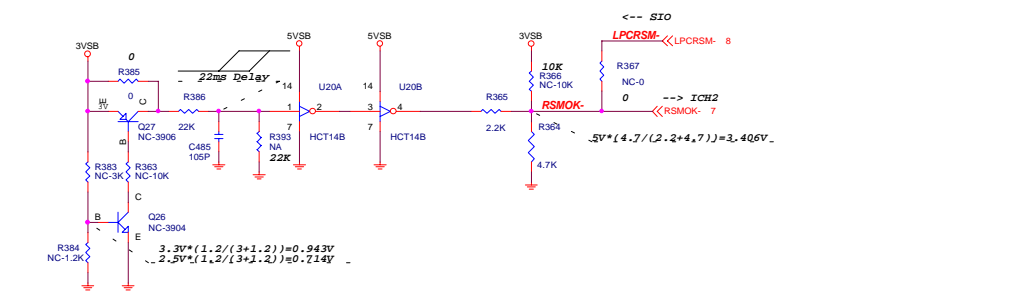
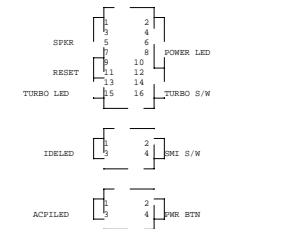
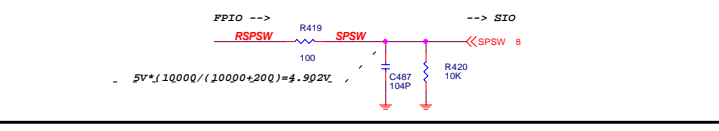
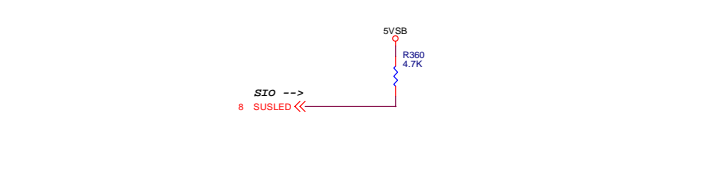
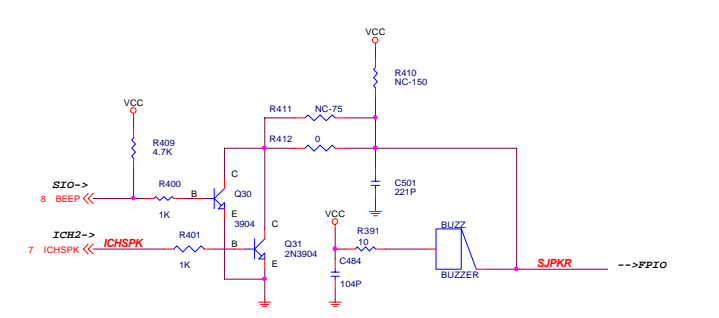
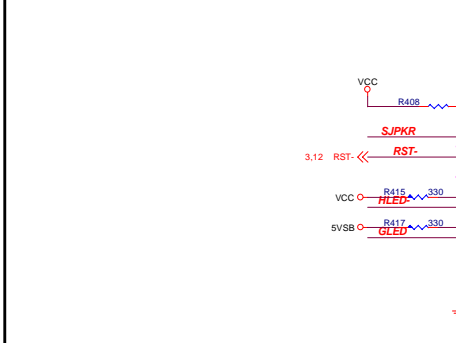
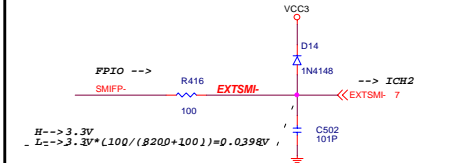
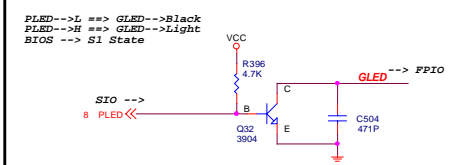
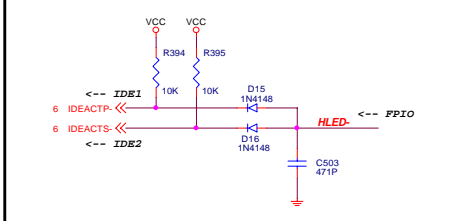
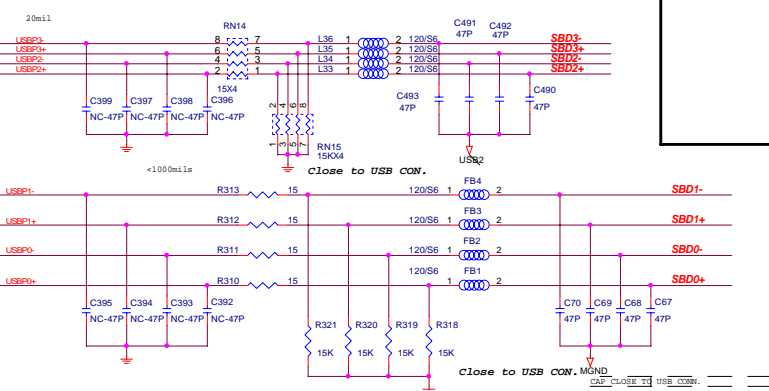
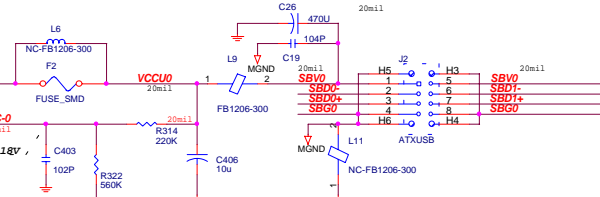
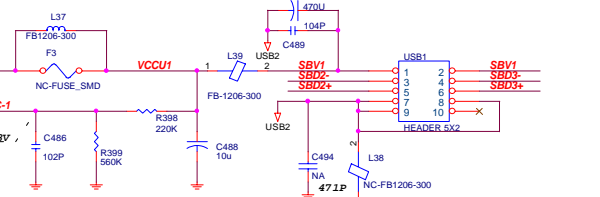
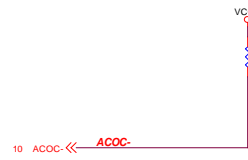
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M1	PNT-1	<<PNT-1 9
GNT#1	PNT-2	<<PNT-2 9
GNT#2	PNT-3	<<PNT-3 9
GNT#3	PNT-4	<<PNT-4 9
GNT#4	PNT-5	<<PNT-5 9

G1	LAN_RXD0	<<LAN_RXD0 10
G2	LAN_RXD1	<<LAN_RXD1 10
G3	LAN_RXD2	<<LAN_RXD2 10
F1	LAN_TXD0	<<LAN_TXD0 10
F2	LAN_TXD1	<<LAN_TXD1 10
F3	LAN_TXD2	<<LAN_TXD2 10
H2	LAN_CLK	<<LAN_CLK 10
H3	LAN_SYNC	<<LAN_SYNC 10

10	ACSYNC	ACSYNC	R256	33	ICHASYN	VCC3
10	ACDAOUT	ACDAOUT	R265	33	ICHACSDO	R266 8.2K

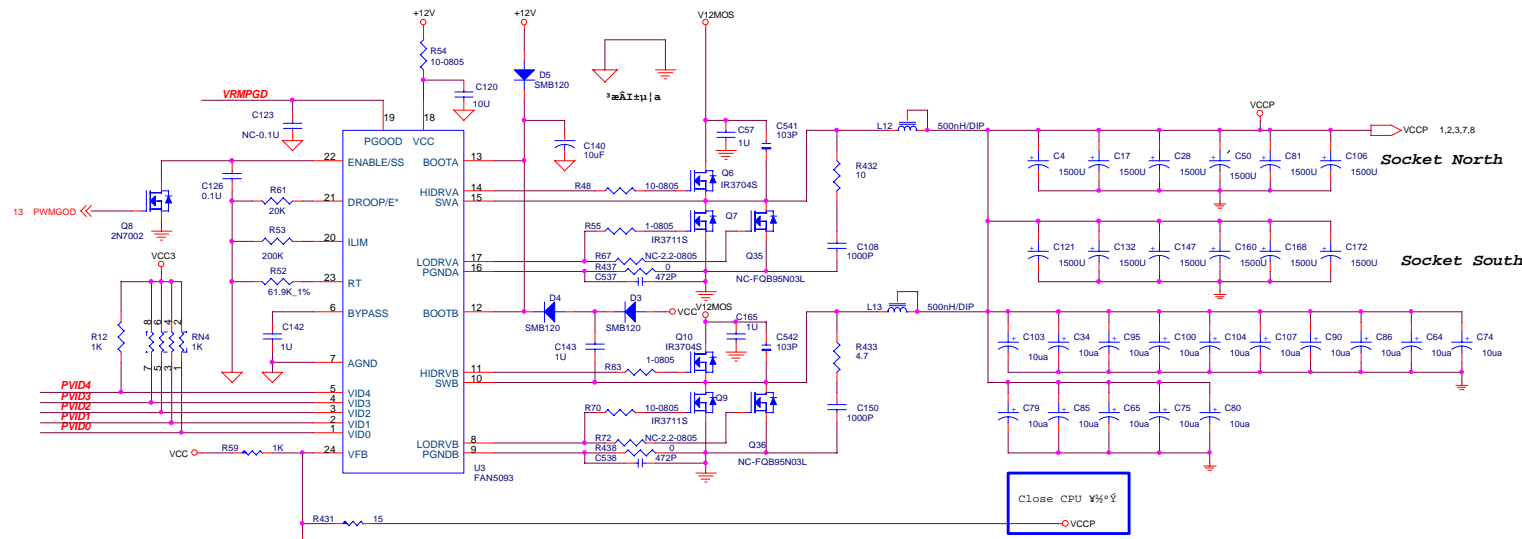
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9	PNT-2	GP22	R287	8.2K
9	PNT-3	GP22	R297	8.2K
15	PNT-0	GP22	R274	8.2K
9	PNT-1	GP22	R273	8.2K
9	PNT-5	GP22	R262	8.2K
9	PNT-1	GP22	R210	8.2K
9	PNT-1	GP22	R257	1K

8	THERM	GP25	W15
12	PWROK	GP24	W21
8,11	ICHRI	GP24	W16
8	SUSCLK	GP24	W16
8	SMBDAT	GP24	W16
9	SMBCLK	GP24	W16
11	EXTSMI	GP24	W16
3	CLK14	GP24	W16
3	USCLK	GP24	W16
3	ICH96	GP24	W16
10	ACRST	GP24	W16
10	ACBCLK	GP24	W16
10	ACDAIN0	GP24	W16
10	ACDAOUT	GP24	W16
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10	ACDAOUT1	GP24	W16
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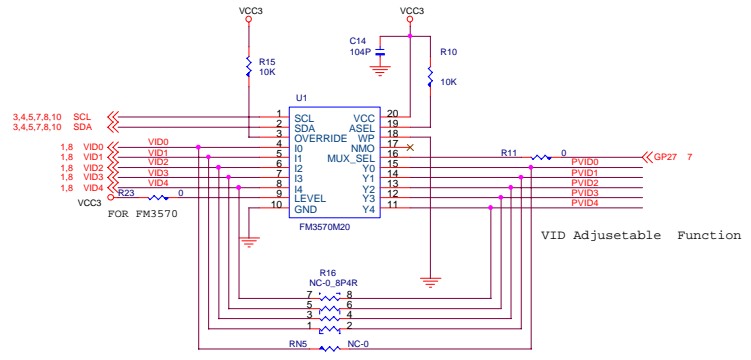
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1	1	0	1	1	1.175
1	1	0	0	1	1.200
1	1	0	0	0	1.225
1	0	1	1	0	1.250
1	0	1	1	1	1.275
1	0	1	0	0	1.300
1	0	1	0	1	1.325
1	0	0	1	0	1.350
1	0	0	1	1	1.375
1	0	0	0	1	1.400
1	0	0	0	0	1.425
0	1	1	1	1	1.450
0	1	1	1	0	1.475
0	1	1	0	1	1.500
0	1	1	0	0	1.525
0	1	0	1	1	1.550
0	1	0	1	0	1.575
0	1	0	0	1	1.600
0	1	0	0	0	1.625
0	0	1	1	1	1.650
0	0	1	1	0	1.675
0	0	1	0	1	1.700
0	0	1	0	0	1.725
0	0	0	1	1	1.750
0	0	0	1	0	1.775
0	0	0	0	1	1.800
0	0	0	0	0	1.825
0	0	0	0	0	1.850



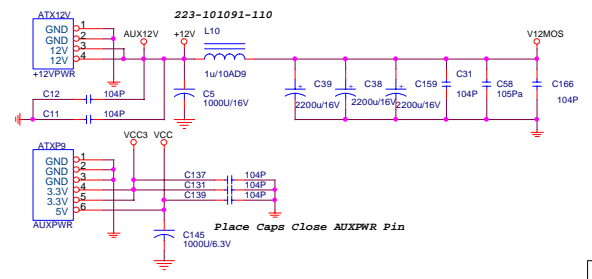
Close CPU V_{CC}

Default VCCP = 1.725V
 VID[4..0]=1,1,1,1,VCCP->0.000V
 VID[4..0]=0,0,0,0,VCCP->1.850V

Place Caps Close +12V_{PWR} Pin
 Place Caps Close ATX_{POWER} Pin

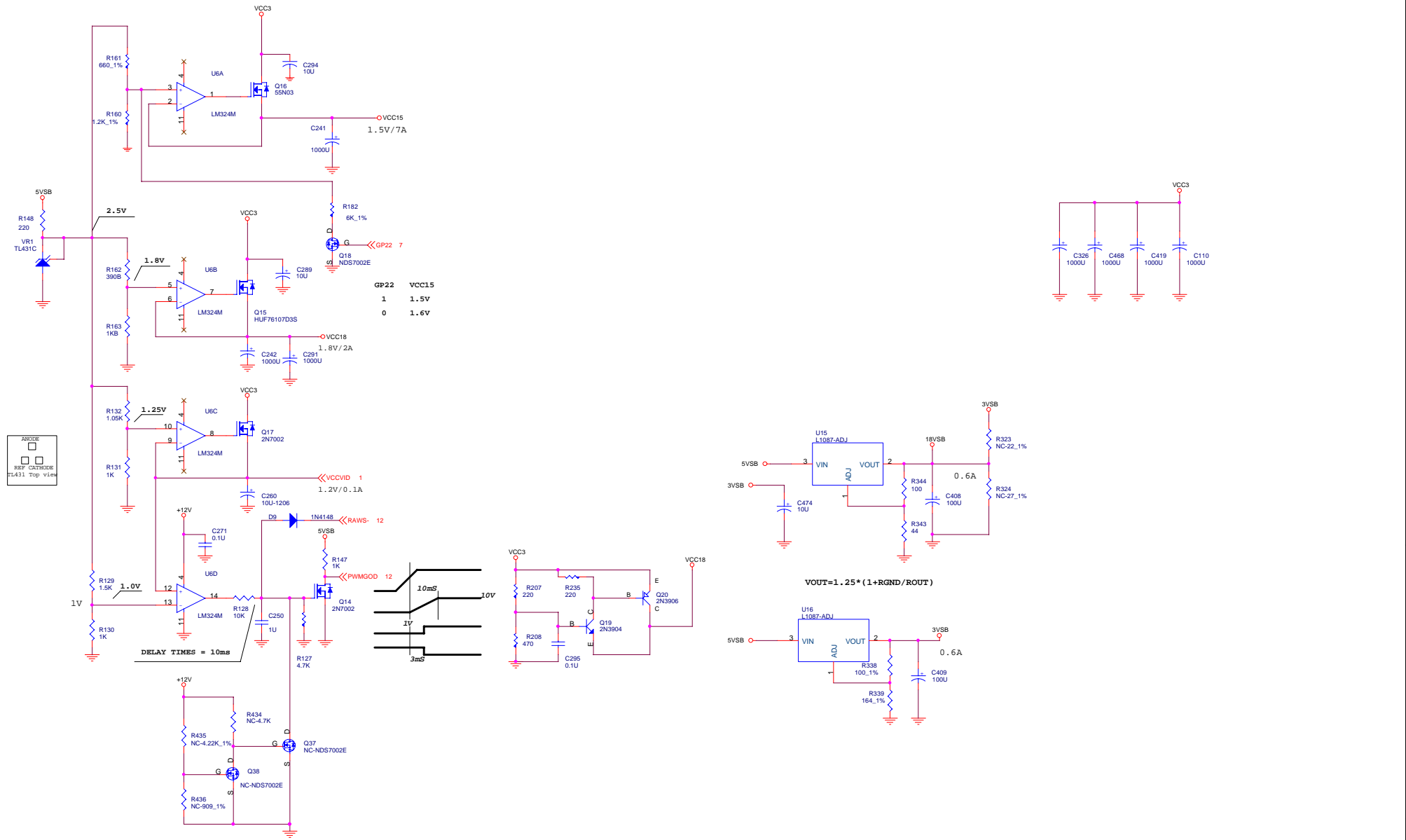


TO PWM REGULATOR and I/O WHM



12VATXPOWER SPEC. Ver. 1.0

JET WAY INFORMATION		
HIP6303/ATX12V		
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ANODE
REF CATHODE
TL431 Top view

