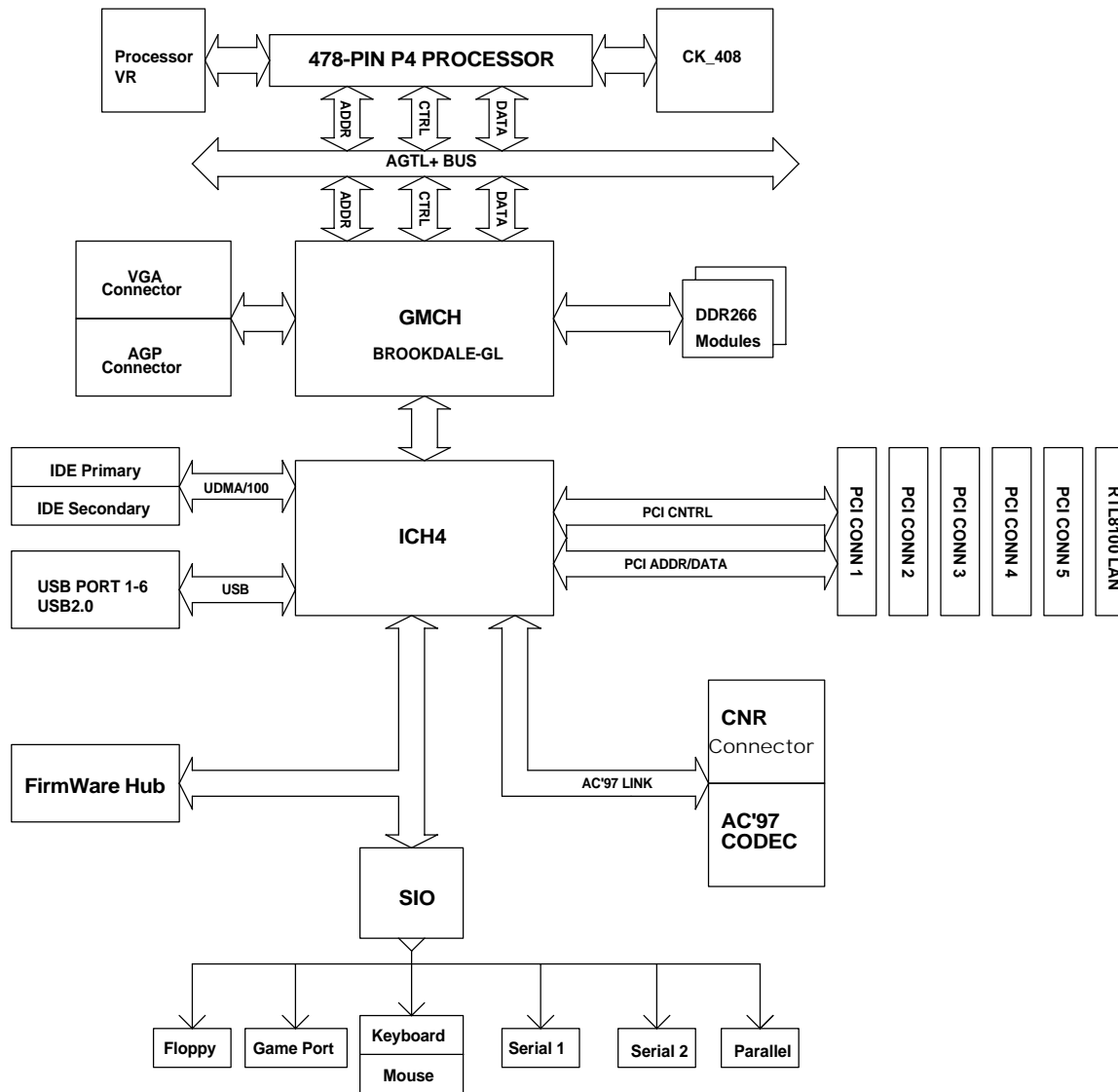


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**845LDA**

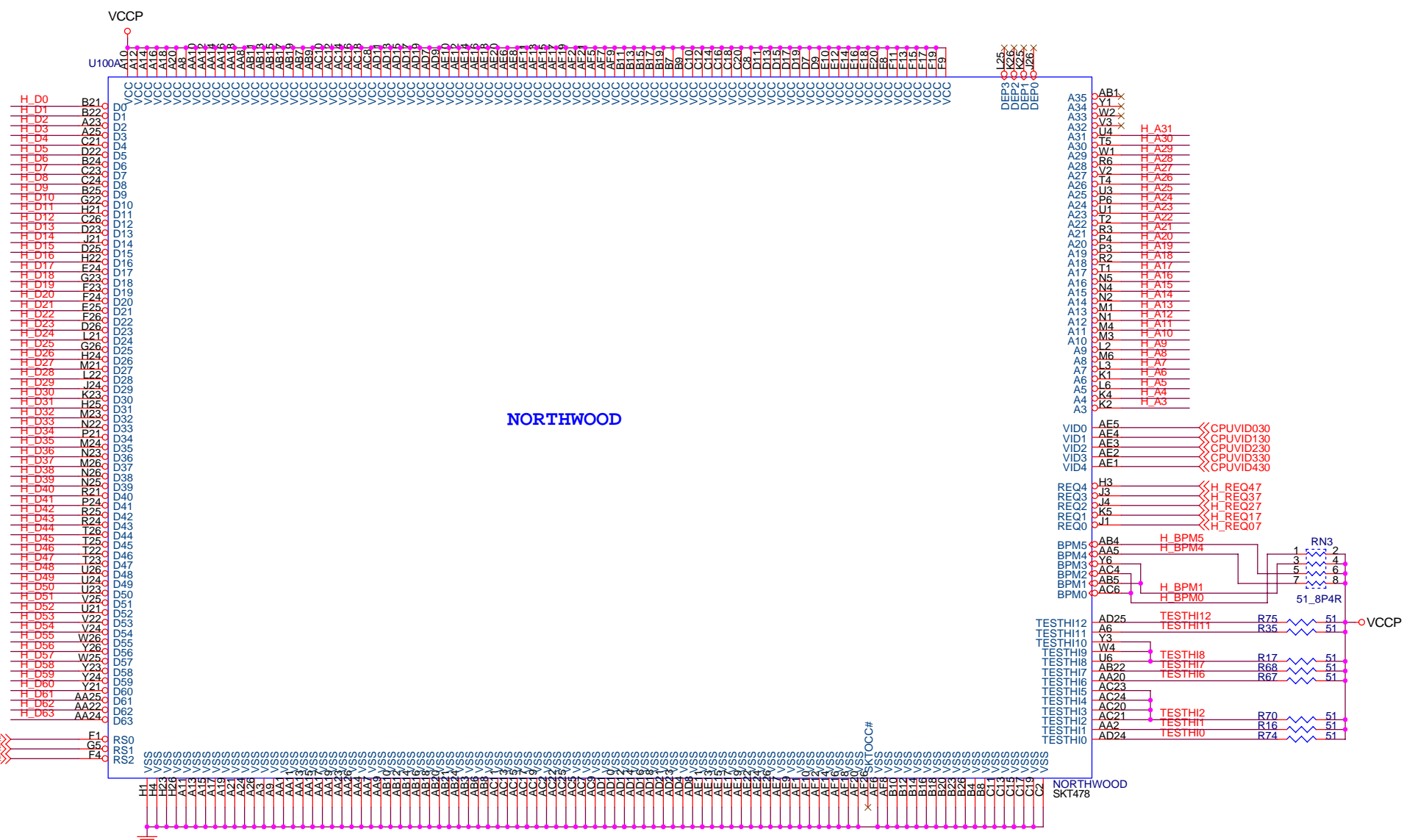
PAGE01 COVER SHEET  
PAGE02 BLOCK DIAGRAM  
PAGE03 SOCKET 478 PROCESSOR: PART1  
PAGE04 SOCKET 478 PROCESSOR: PART2  
PAGE05 P4 CAPACITORS  
PAGE06 CLOCK SYNTHESIZER  
PAGE07 GMCH:PART1  
PAGE08 GMCH:PART2  
PAGE09 GMCH:PART3  
PAGE10 DDR TERMINATOR 1  
PAGE11 DIMM1 & DIMM2  
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PAGE13 ICH4: PART1  
PAGE14 ICH4: PART2  
PAGE15 ICH4: PART3(POWER)  
PAGE16 FIRMWARE HUB (FWH) & IDE  
PAGE17 4X AGP SLOT  
PAGE18 PCI SLOT 1 & 2  
PAGE19 PCI SLOT 3  
PAGE20 SUPER I/O (W83627HF)  
PAGE21 PRT & GAME PORT  
PAGE22 COM PORT & IR/CIR CONNECTOR  
PAGE23 H/W MONITORING  
PAGE24 USB & RJ45 PORT  
PAGE25 CNR SLOT & K/B PORT  
PAGE26 CODEC  
PAGE27 AUDIOI CONNECTOR  
PAGE28 SYSTEM1(FRONT PANEL)  
PAGE29 SYSTEM2(ATX POWER CONNECTOR)  
PAGE30 Processor VR  
PAGE31 VOLTAGE REGULATORS  
PAGE32 RTL 8100BL LAN  
PAGE33 VGA CONNECTOR  
PAGE34 PCI SLOT 4 & 5

<b>JET WAY INFORMATION</b>		
<b>COVER SHEET</b>		
Title		
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# BLOCK DIAGRAM

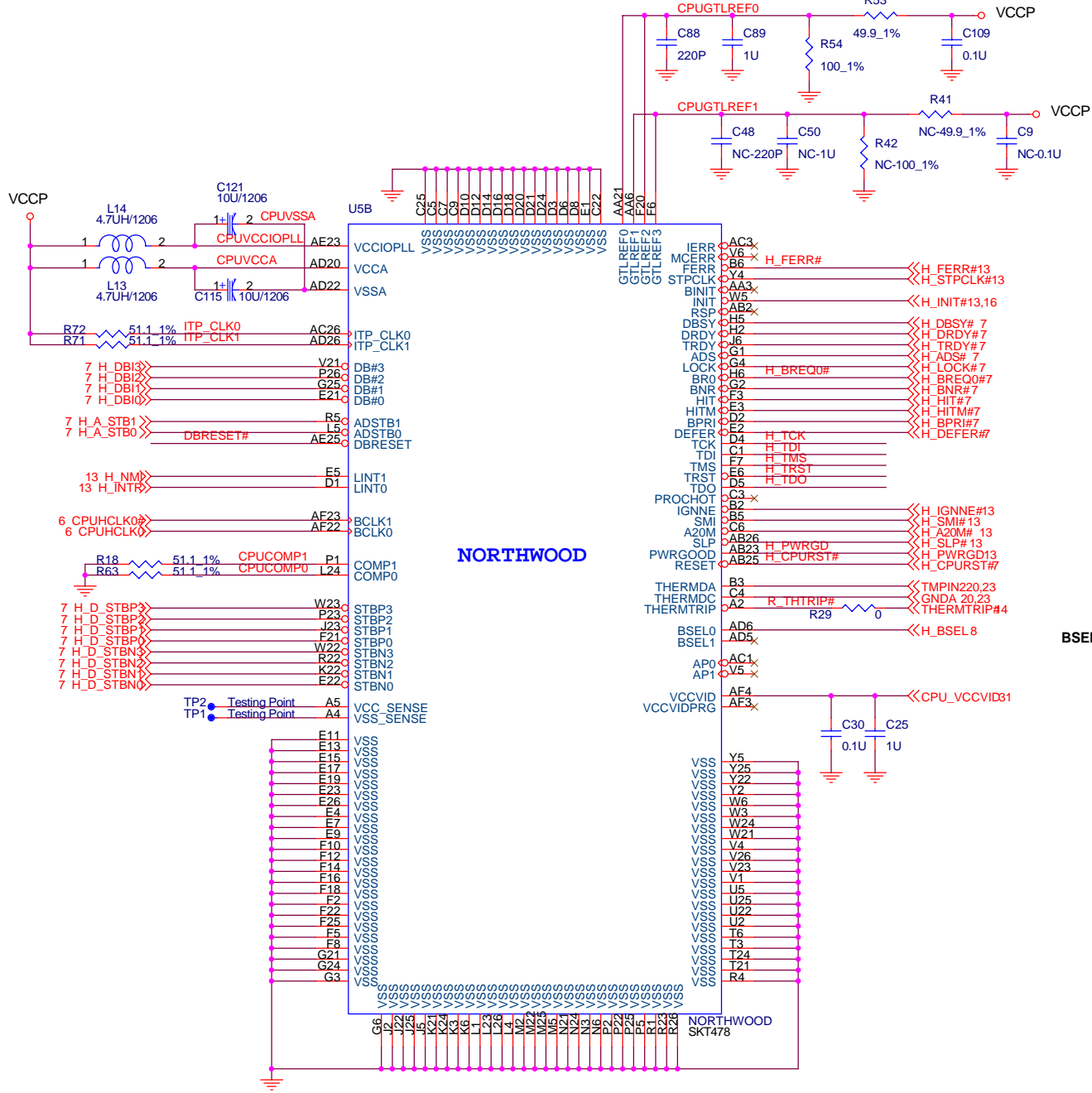


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Title		
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Size	Document Number	Rev
	<b>845LDA</b>	<b>0</b>
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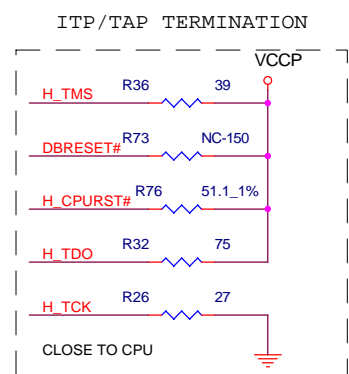
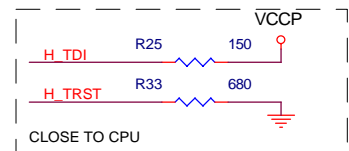
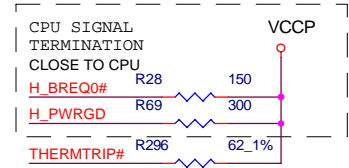


NORTHWOOD

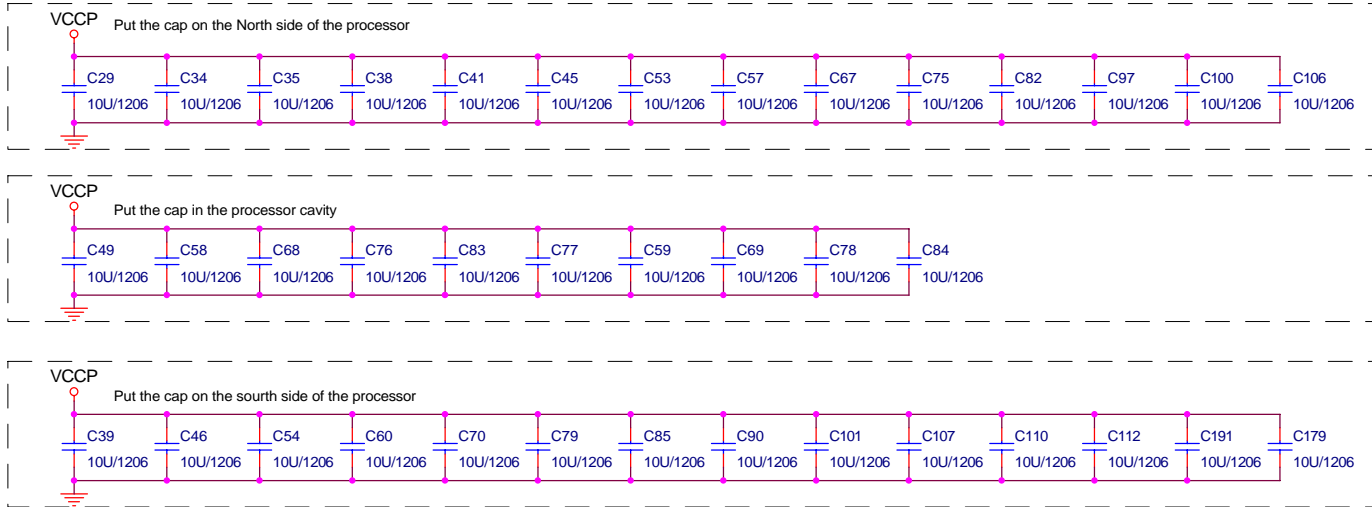
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<b>478-PIN SOCKET : PART 1</b>		
Size	Document Number	Rev
	<b>845LDA</b>	<b>0</b>
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CLOSE TO CPU

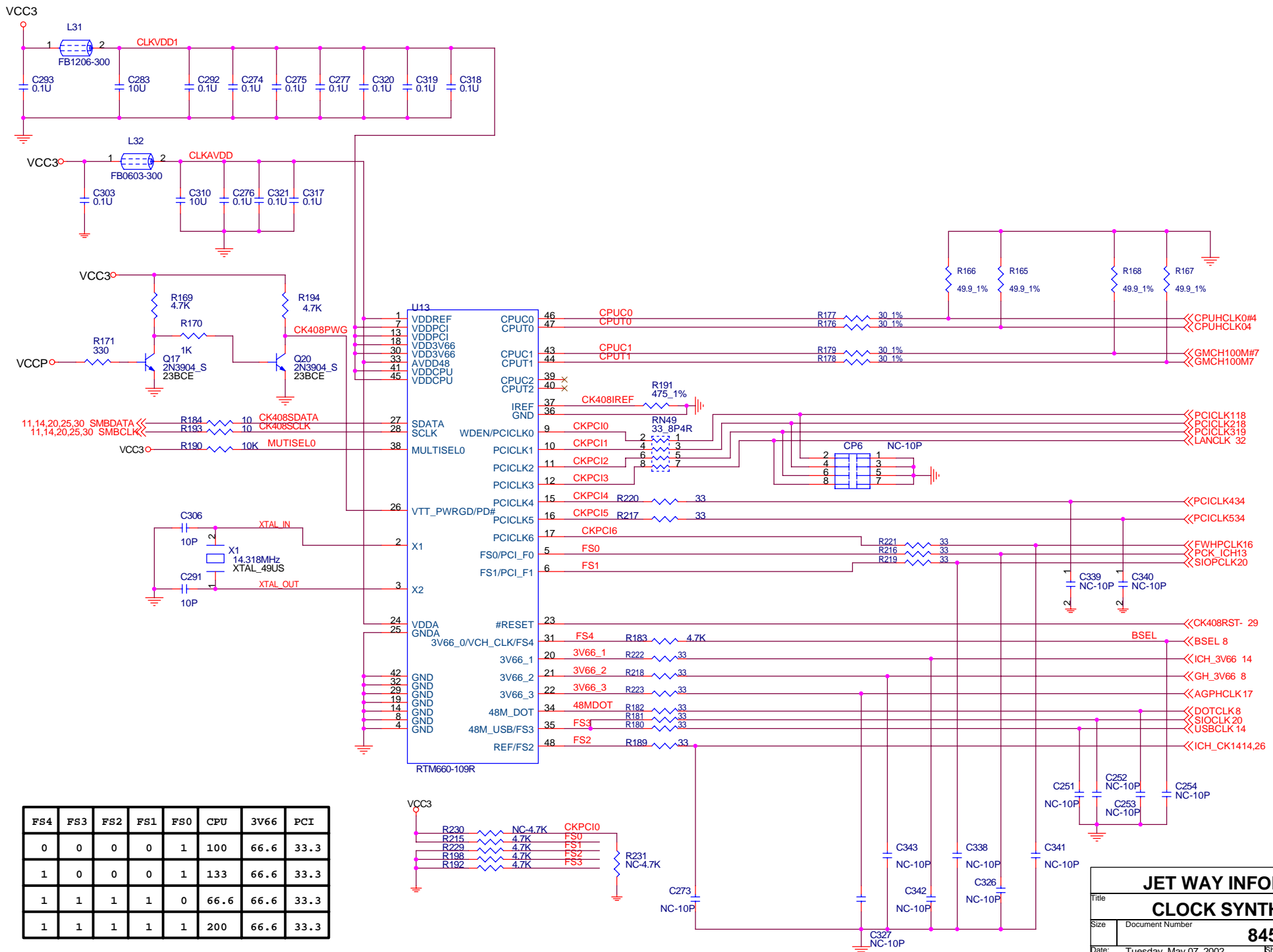


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Title		
<b>478-PIN SOCKET : PART 2</b>		
Size	Document Number	Rev 0
<b>845LDA</b>		
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<b>JET WAY INFORMATION</b>		
Title		
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Size	Document Number	Rev
	<b>845LDA</b>	<b>0</b>
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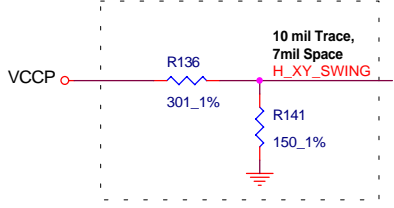
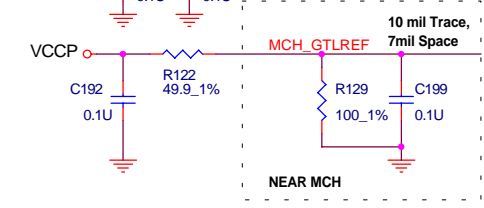
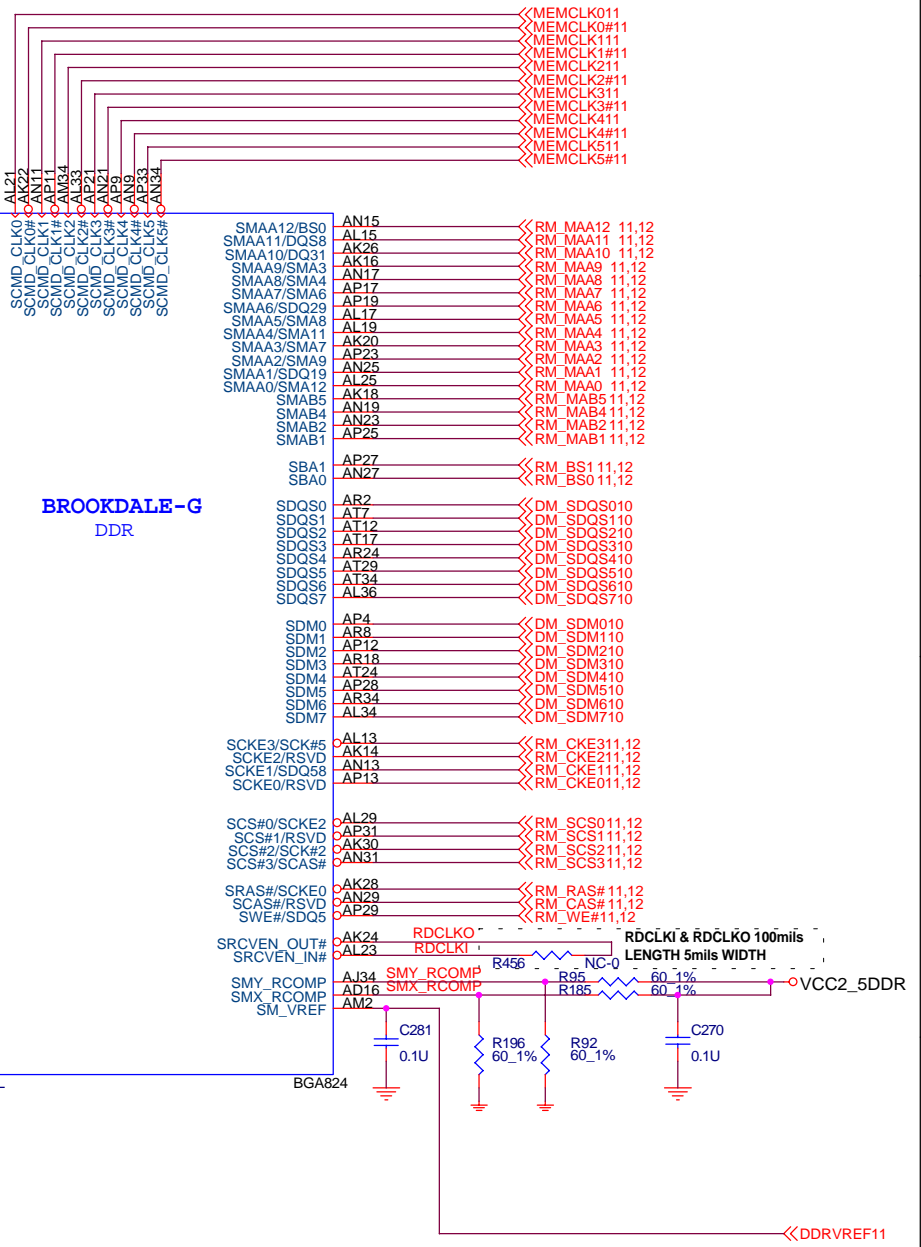
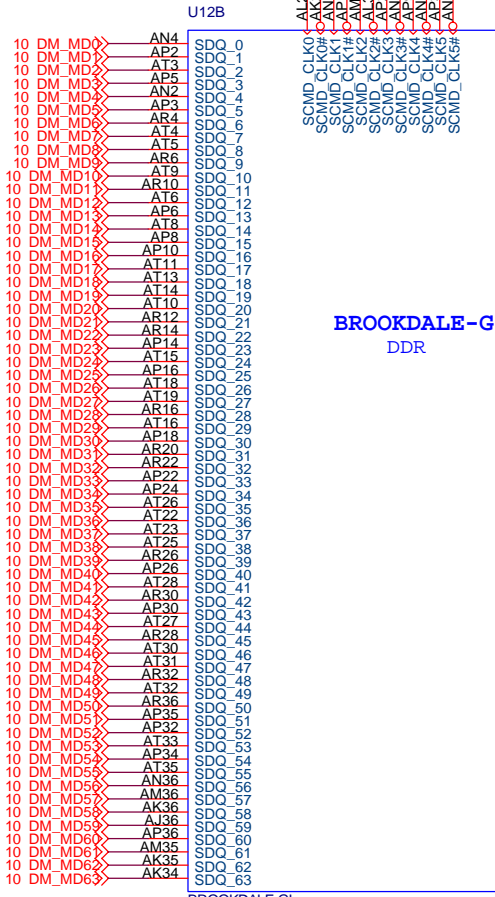
# Clock Synthesizer



FS4	FS3	FS2	FS1	FS0	CPU	3V66	PCI
0	0	0	0	1	100	66.6	33.3
1	0	0	0	1	133	66.6	33.3
1	1	1	1	0	66.6	66.6	33.3
1	1	1	1	1	200	66.6	33.3

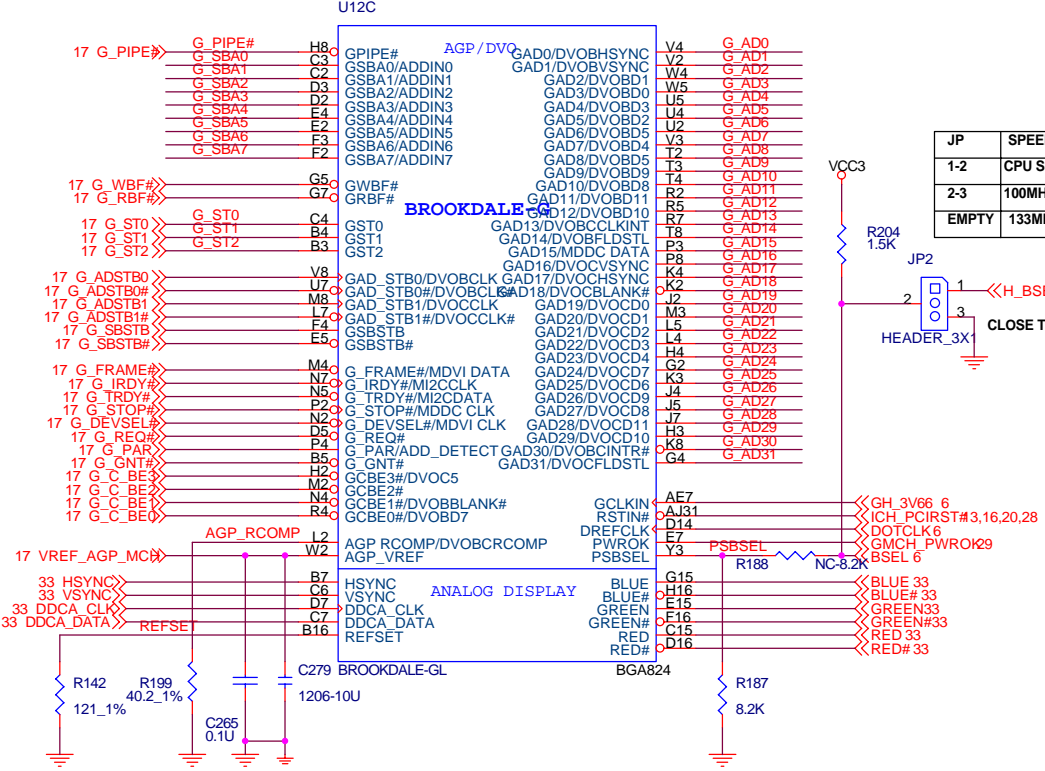
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<b>CLOCK SYNTHESIZER</b>		
Title	<b>845LDA</b>	
Size	Document Number	Rev 0
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H\_D[0..63] << H\_D[0..63] 3  
 H\_A[3..31] << H\_A[3..31] 3

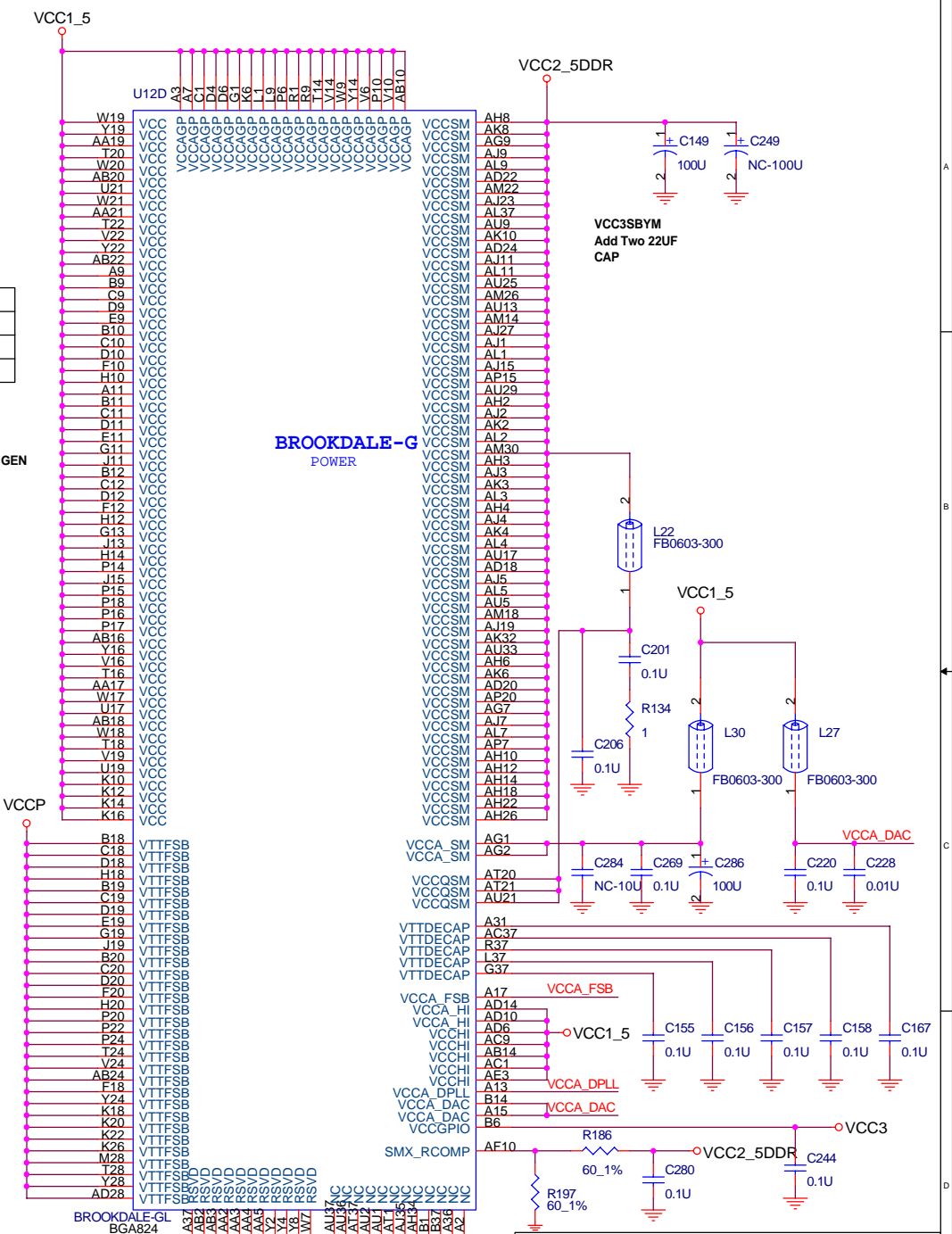
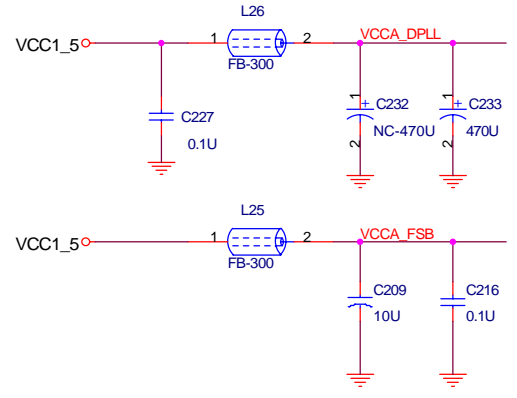
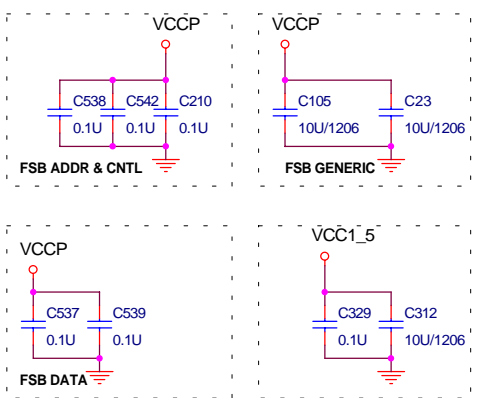
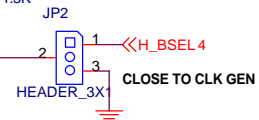


JET WAY INFORMATION			
Title	MCH : PART 1 (HOST INTERFACE)		
Size	Document Number	845LDA	
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17 G\_AD[0..31] >> G\_ADI0..31  
 17 G\_SBA[0..7] >> G\_SBA0..7



JP	SPEED
1-2	CPU SELECT
2-3	100MHz
EMPTY	133MHz



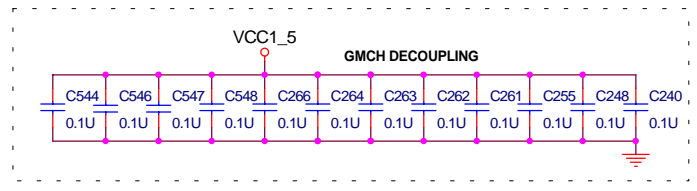
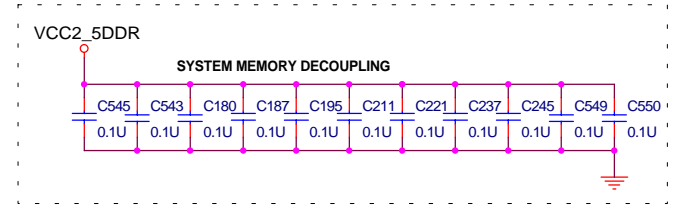
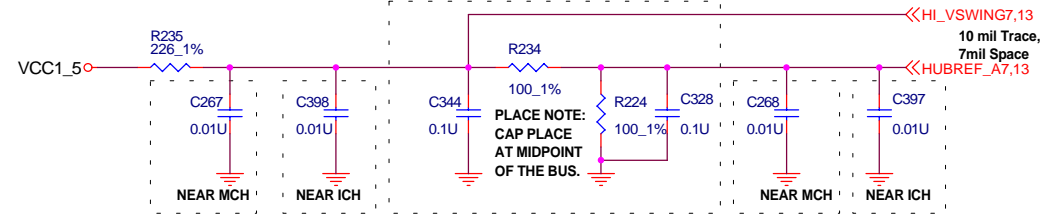
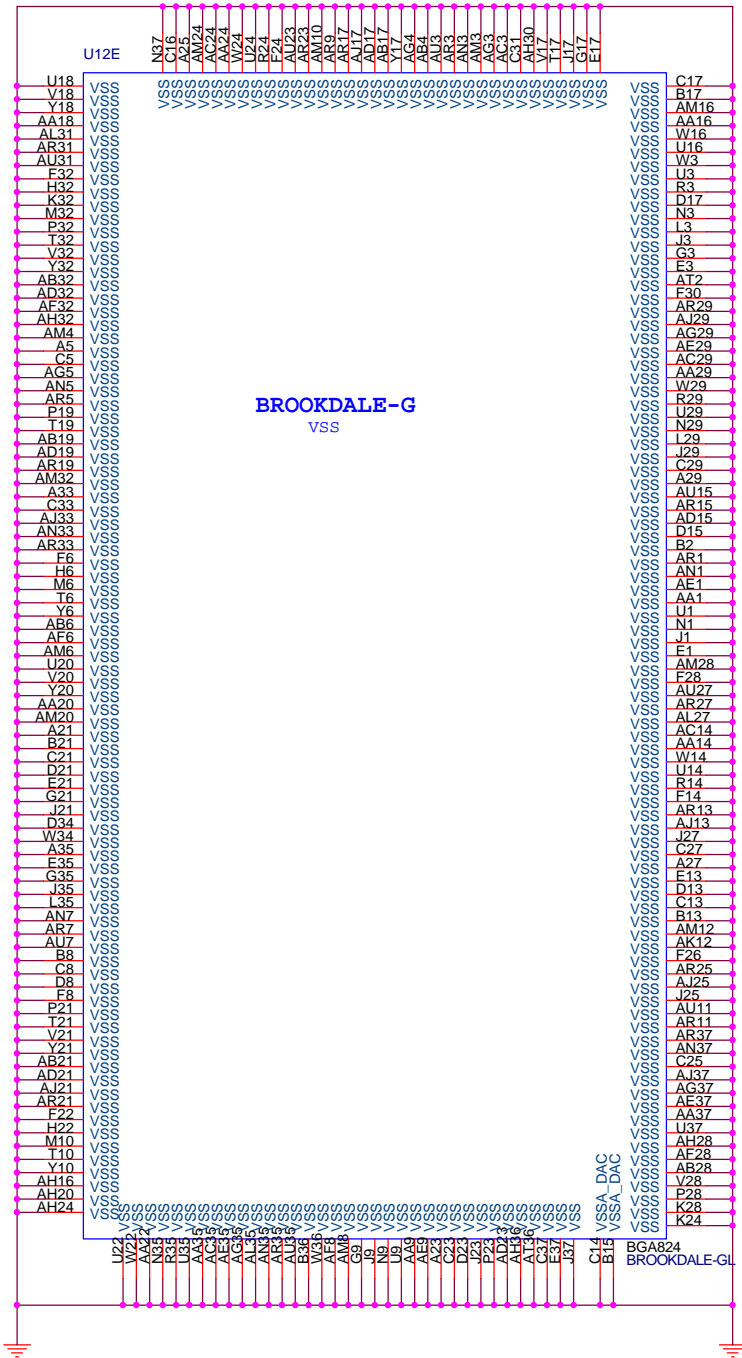
**JET WAY INFORMATION**

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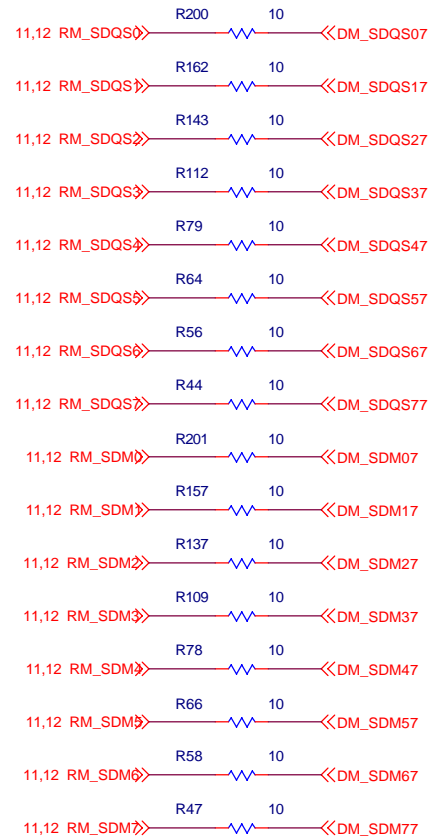
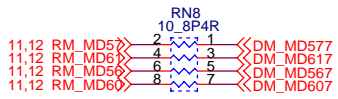
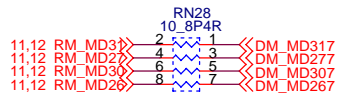
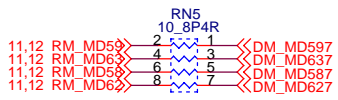
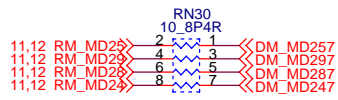
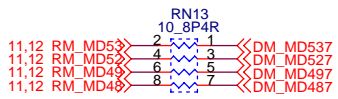
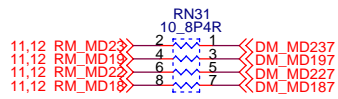
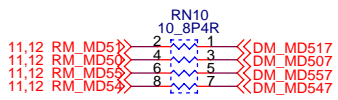
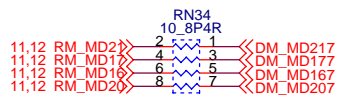
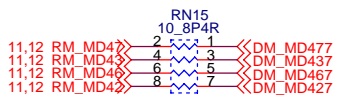
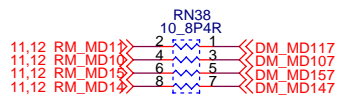
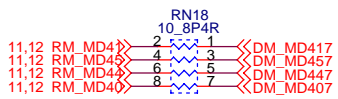
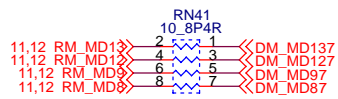
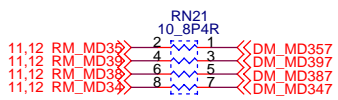
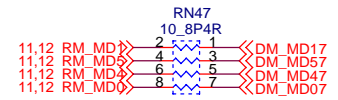
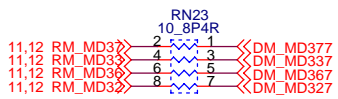
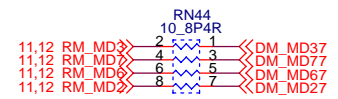
Size: 845LDA

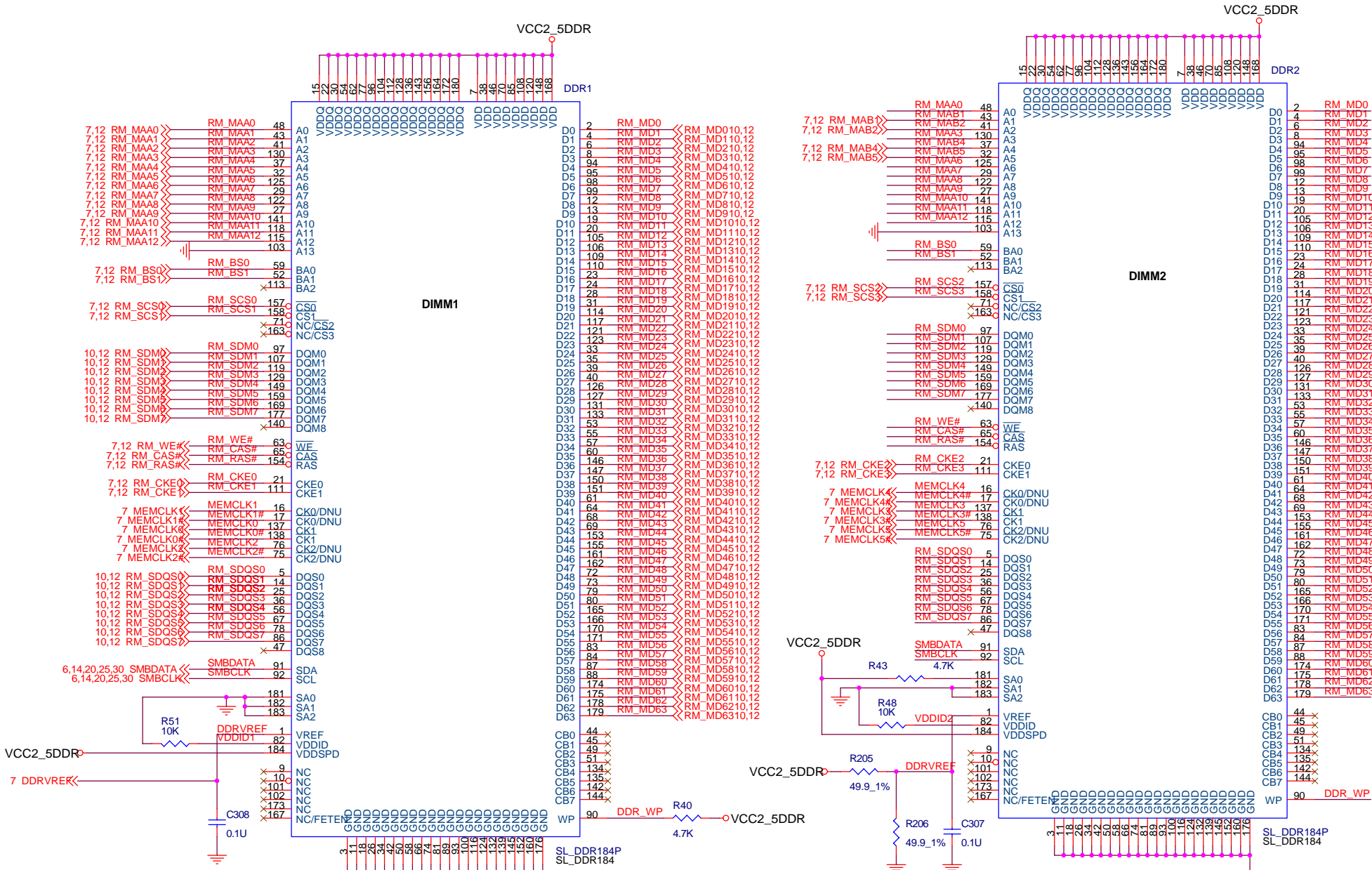
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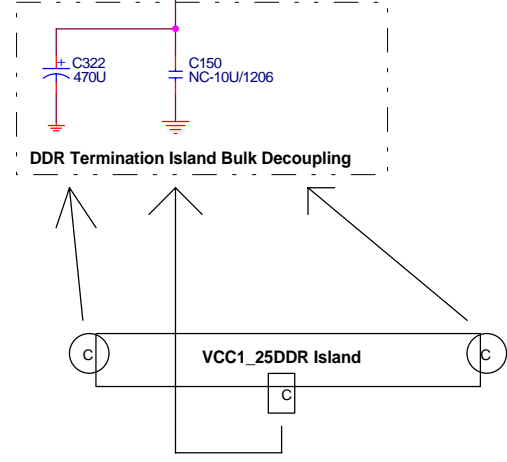
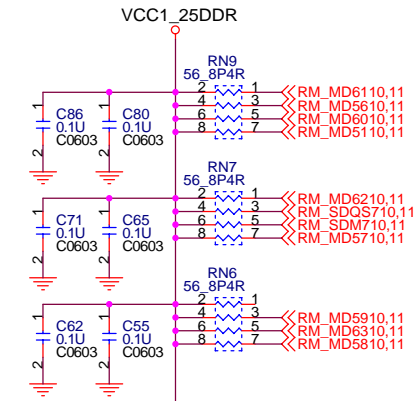
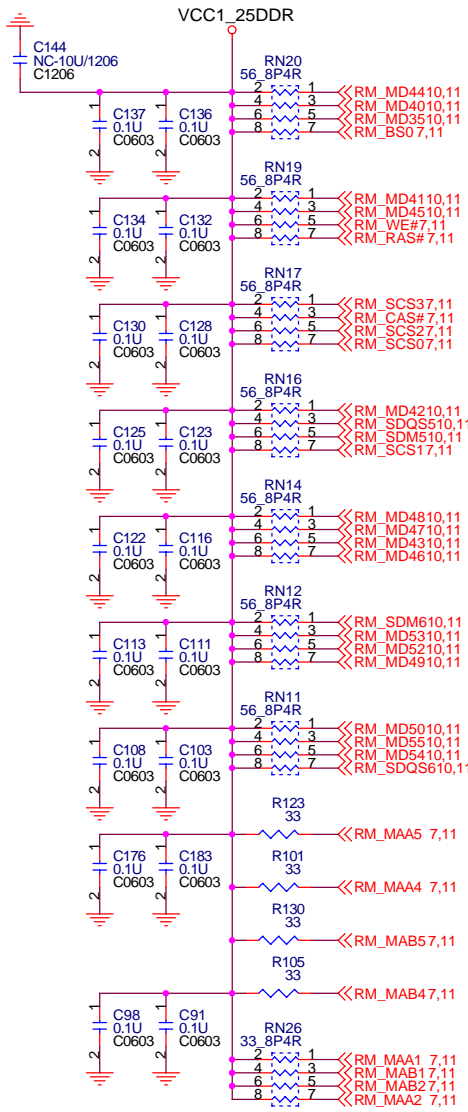
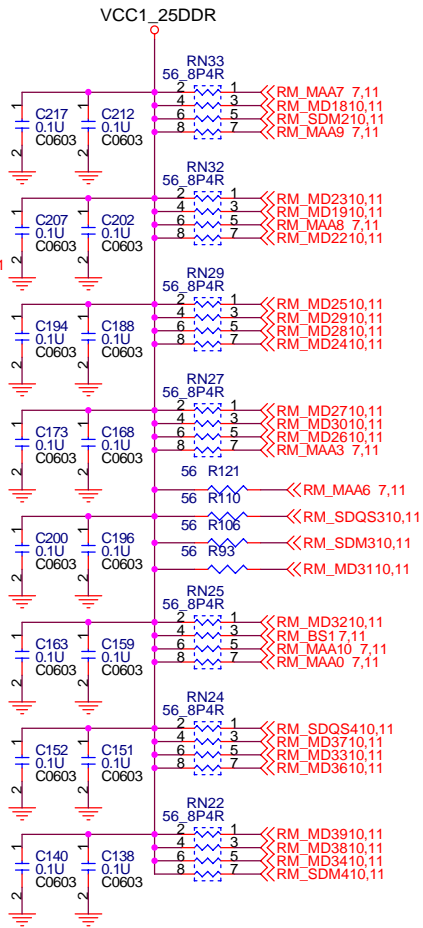
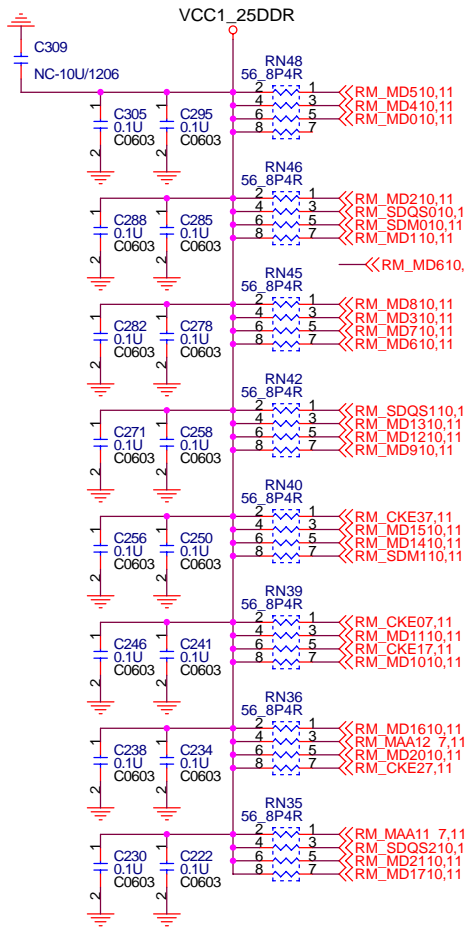


<b>JET WAY INFORMATION</b>			
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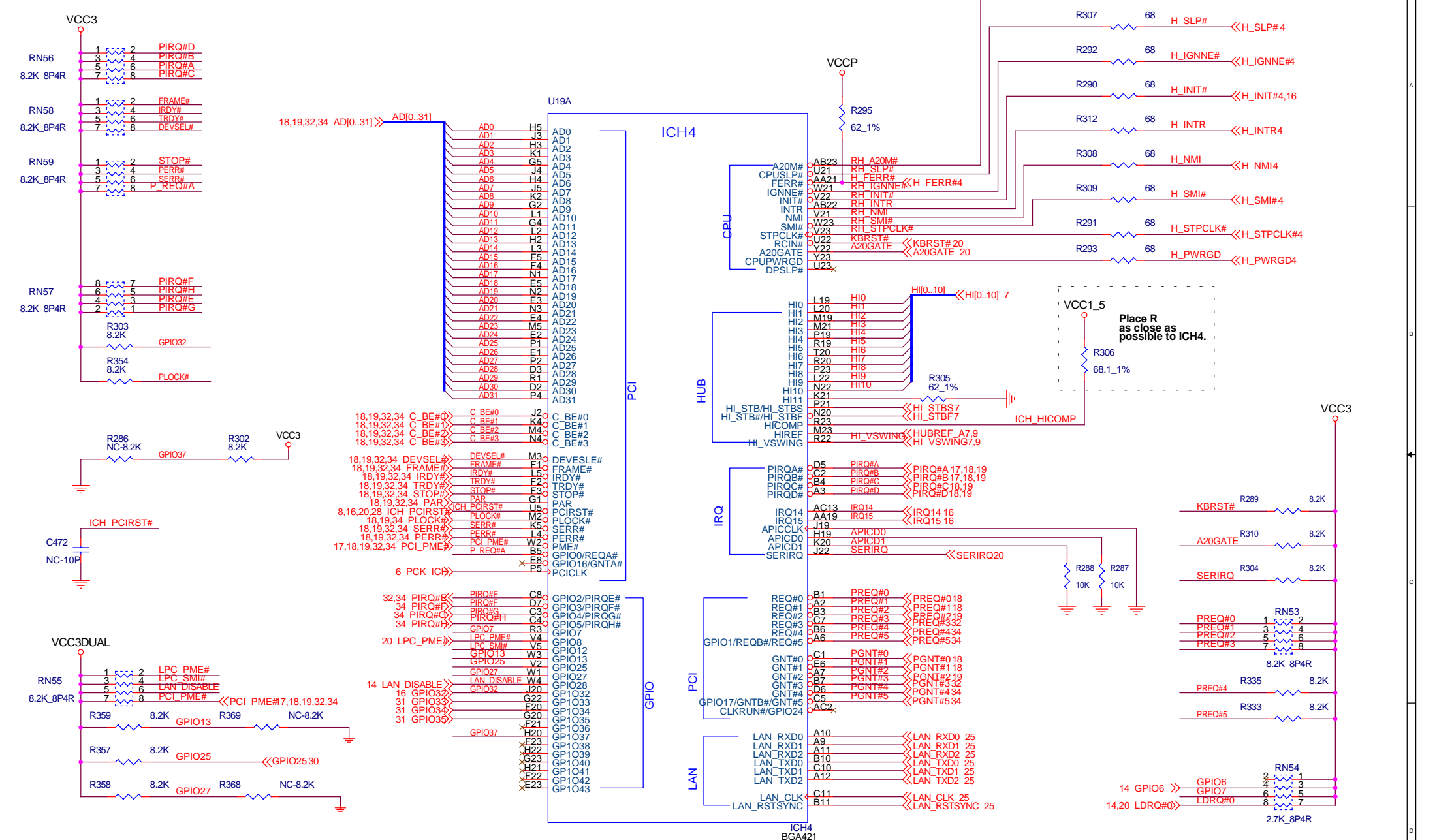


JET WAY INFORMATION			
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Size	Document Number	845LDA	Rev 0
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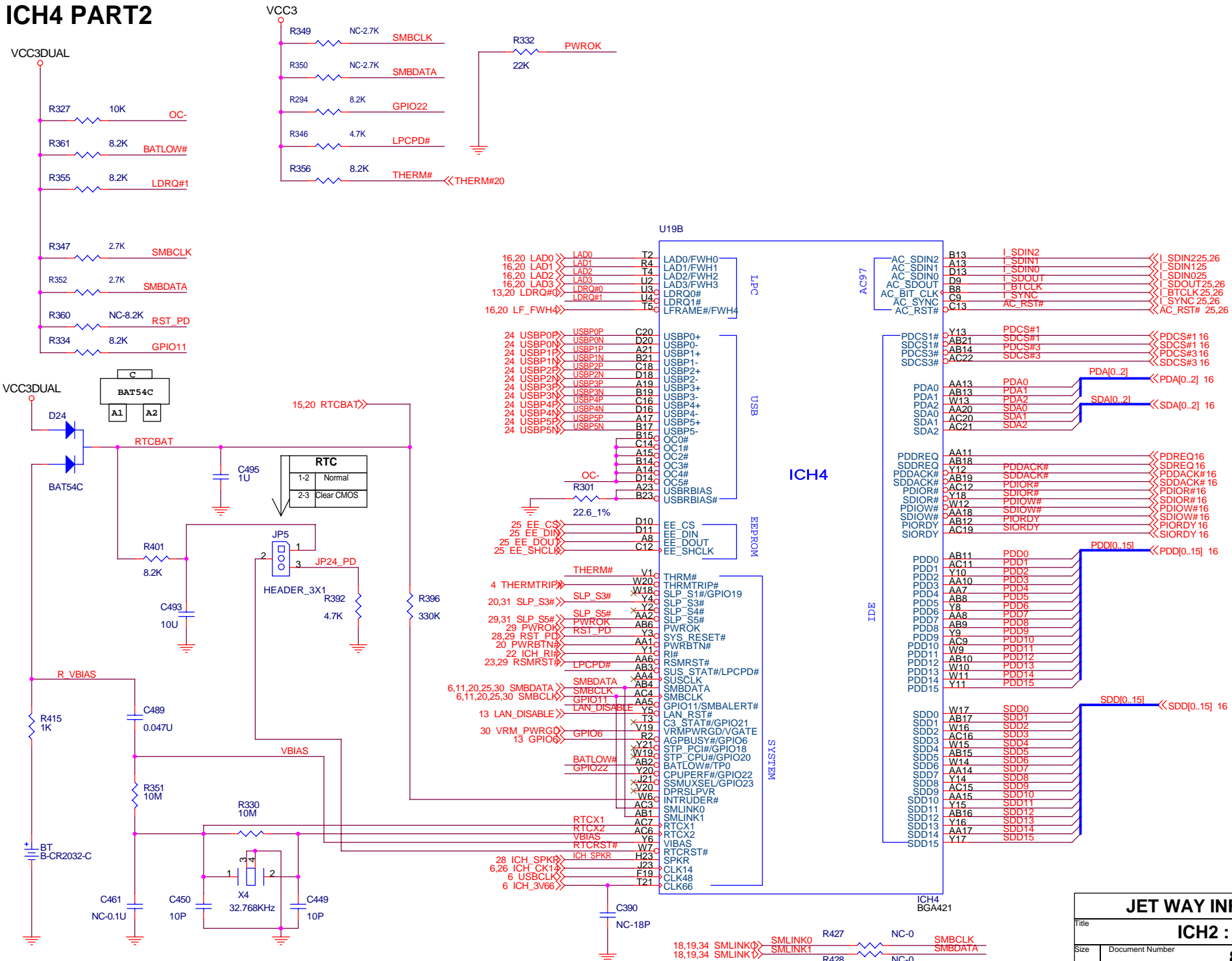
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Title			
DDR TERMINATION			
Size	Document Number	845LDA	
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# ICH4 PART1



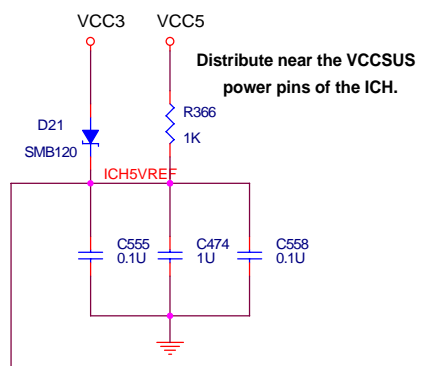
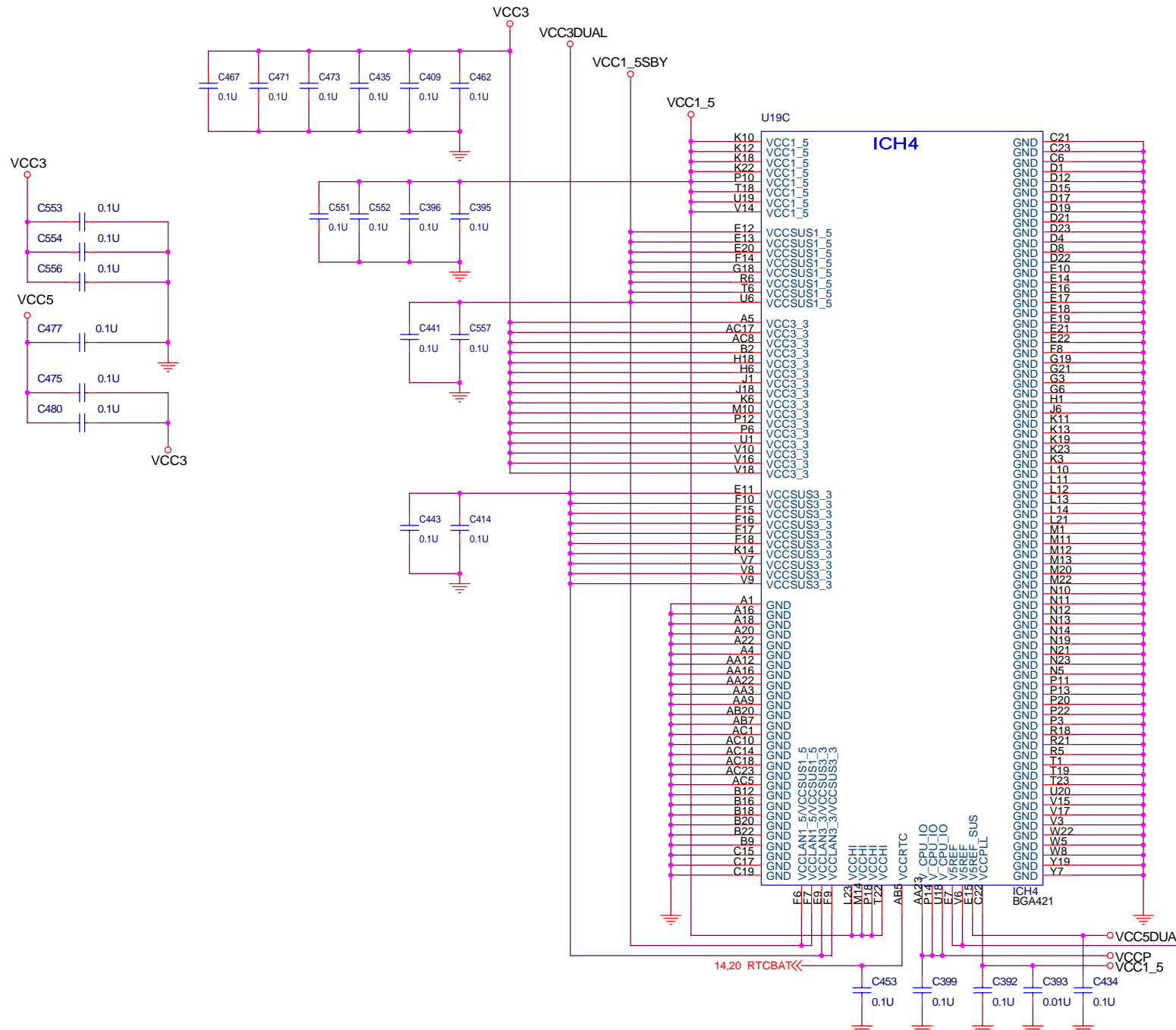
JET WAY INFORMATION			
Title <b>ICH2 : PART 1</b>			
Size	Document Number <b>845LDA</b>	Rev 0	
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# ICH4 PART2



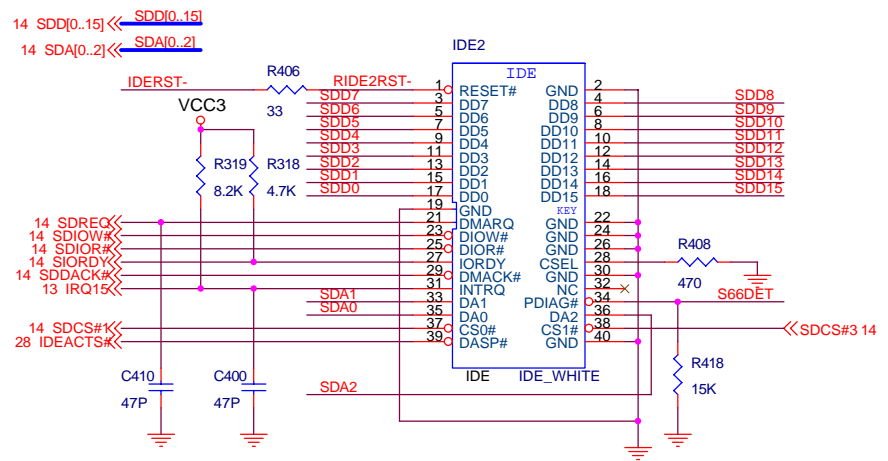
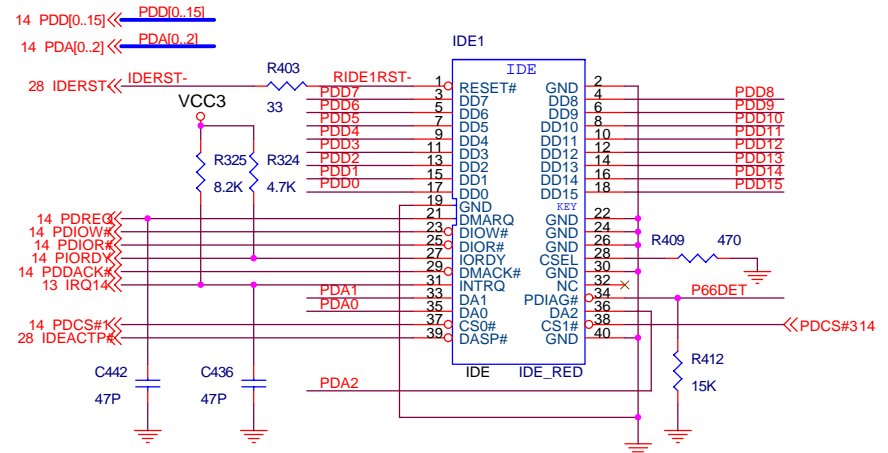
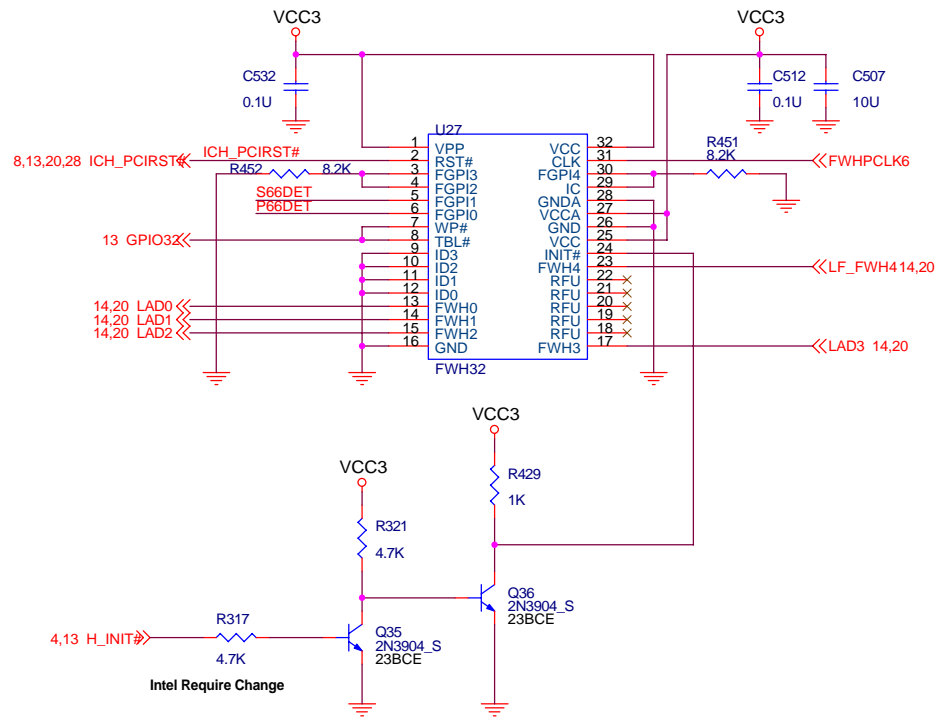
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Title <b>ICH2 : PART 2</b>		
Size	Document Number <b>845LDA</b>	Rev 0
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# ICH4 PART3



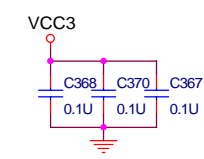
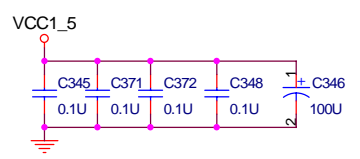
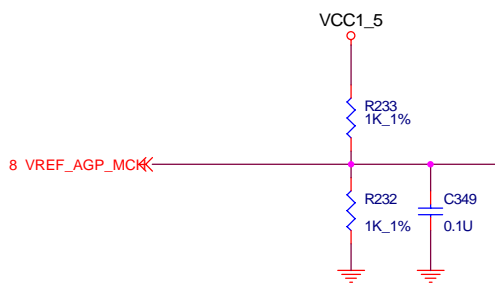
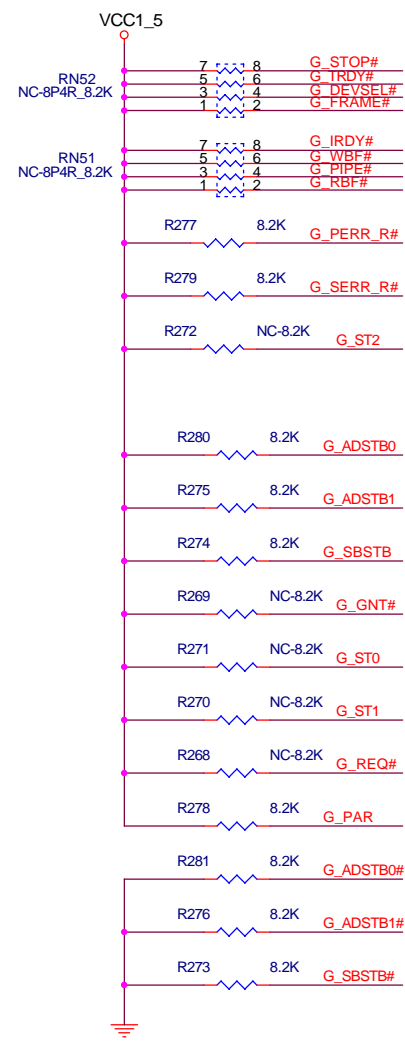
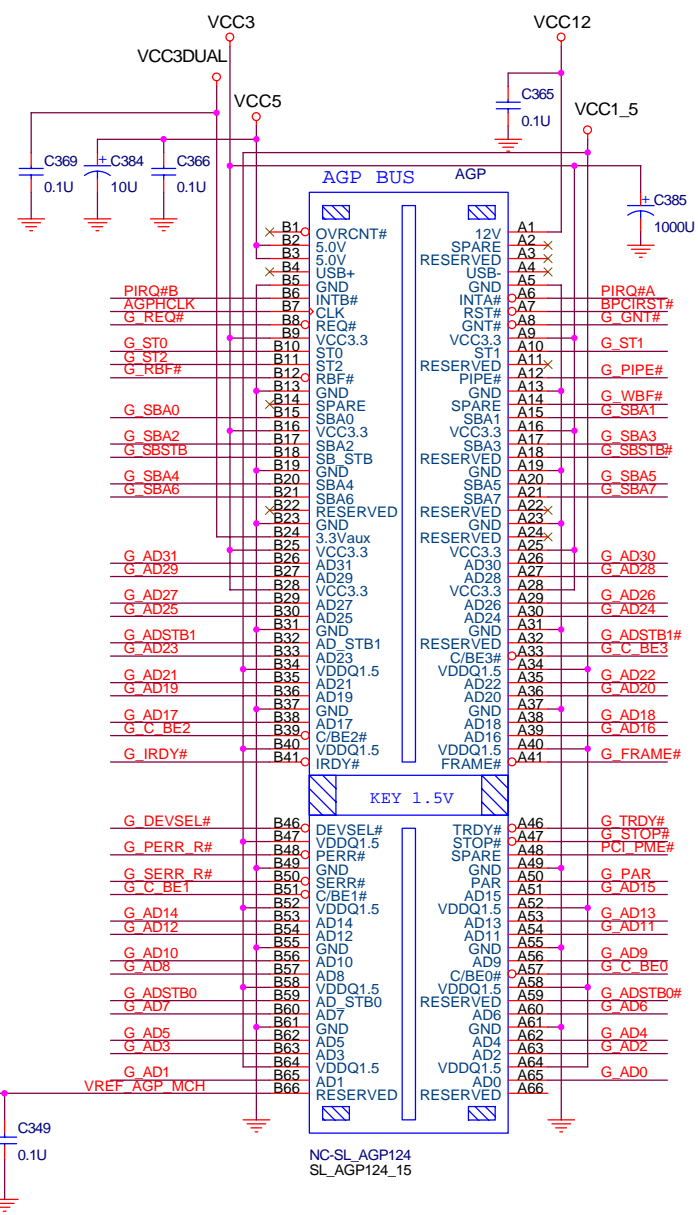
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Size	Document Number	Rev 0
		845LDA
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# FirmWare Hub (FWH) Socket

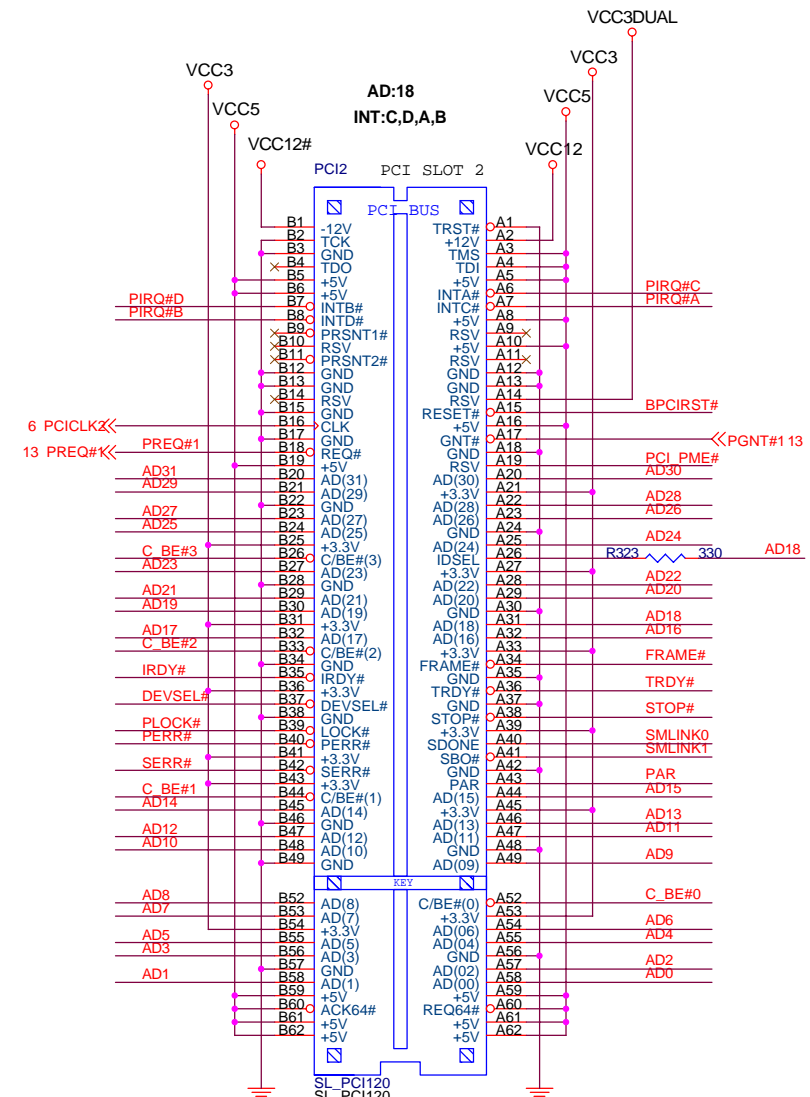
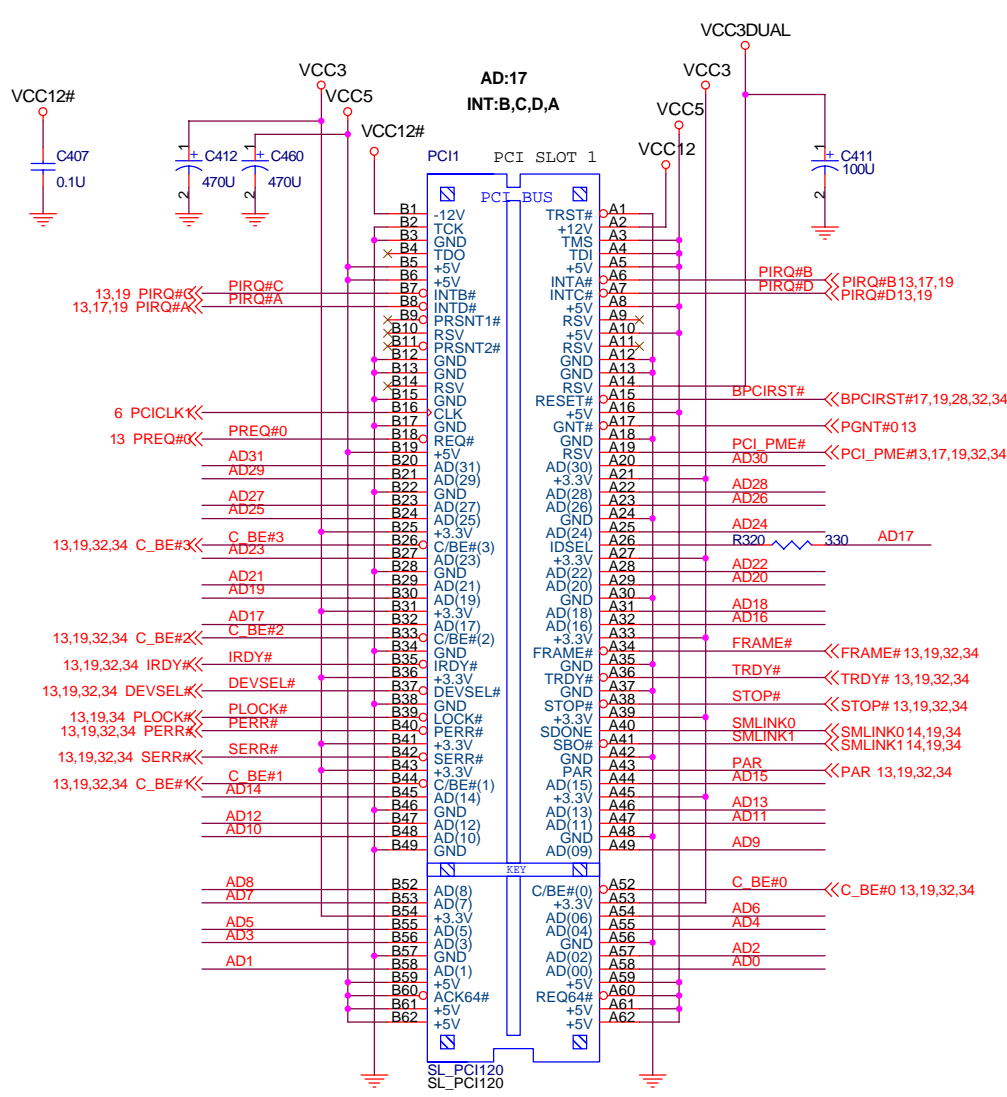


<b>JET WAY INFORMATION</b>			
Title <b>FIRMWARE HUB (FWH) / IDE(ATA100)</b>			
Size	Document Number	<b>845LDA</b>	
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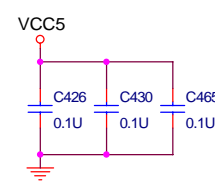
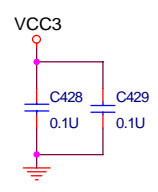
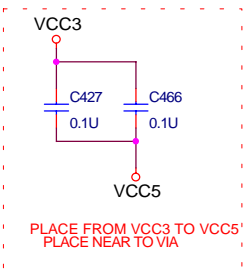
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- 8 G\_SBA[0..7] >> G\_SBA[0..7]
- 8 G\_C\_BE0 >> G\_C\_BE0
- 8 G\_C\_BE1 >> G\_C\_BE1
- 8 G\_C\_BE2 >> G\_C\_BE2
- 8 G\_C\_BE3 >> G\_C\_BE3
- 8 G\_FRAME# >> G\_FRAME#
- 8 G\_DEVSEL# >> G\_DEVSEL#
- 8 G\_IRDY# >> G\_IRDY#
- 8 G\_TRDY# >> G\_TRDY#
- 8 G\_STOP# >> G\_STOP#
- 8 G\_PAR >> G\_PAR
- 8 G\_REQ# >> G\_REQ#
- 8 G\_GNT# >> G\_GNT#
- 8 G\_PIPE# >> G\_PIPE#
- 8 G\_ADSTB0 >> G\_ADSTB0
- 8 G\_ADSTB0# >> G\_ADSTB0#
- 8 G\_ADSTB1 >> G\_ADSTB1
- 8 G\_ADSTB1# >> G\_ADSTB1#
- 8 G\_SBSTB >> G\_SBSTB
- 8 G\_SBSTB# >> G\_SBSTB#
- 8 G\_ST0 >> G\_ST0
- 8 G\_ST1 >> G\_ST1
- 8 G\_ST2 >> G\_ST2
- 8 G\_RBF# >> G\_RBF#
- 8 G\_WBF# >> G\_WBF#
- 6 AGPHCLK >> AGPHCLK
- 13,18,19 PIRQ#A >> PIRQ#A
- 13,18,19 PIRQ#B >> PIRQ#B
- 18,19,28,32,34 BPCIRST# >> BPCIRST#
- 13,18,19,32,34 PCI\_PME# >> PCI\_PME#



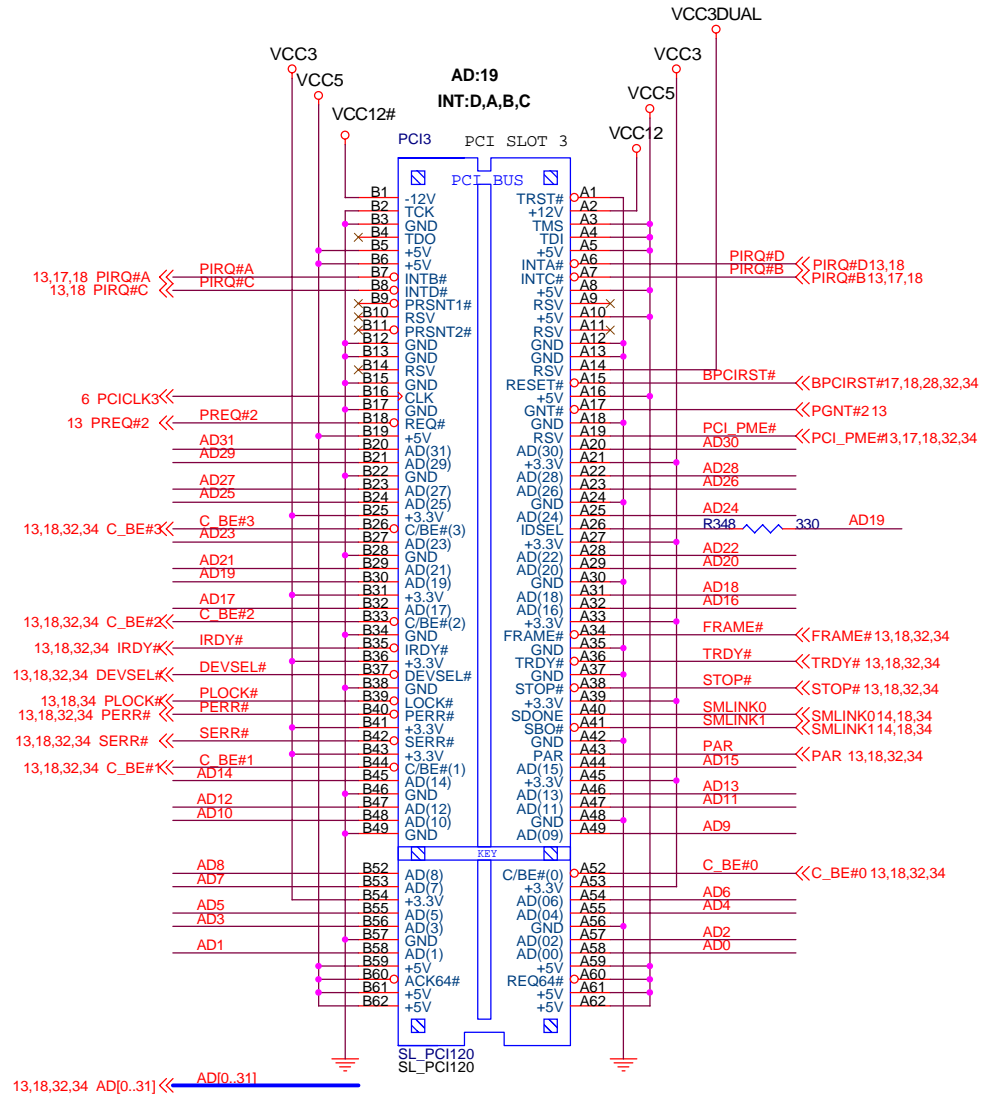
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Size A3	Document Number	<b>845LDA</b>	Rev 0
Date:	G	Sheet 17	of 34

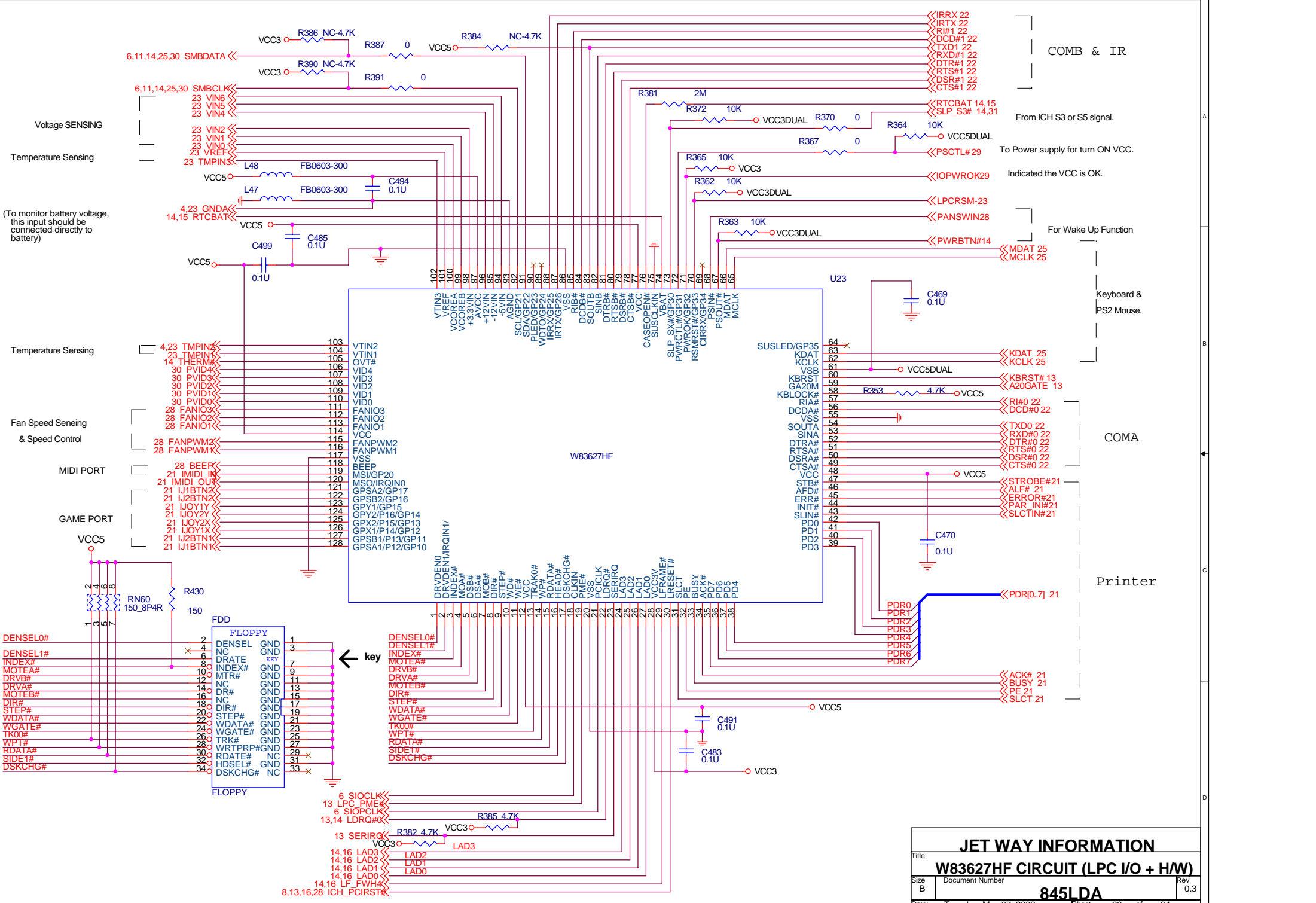


13,19,32,34 AD[0..31] << AD[0..31]



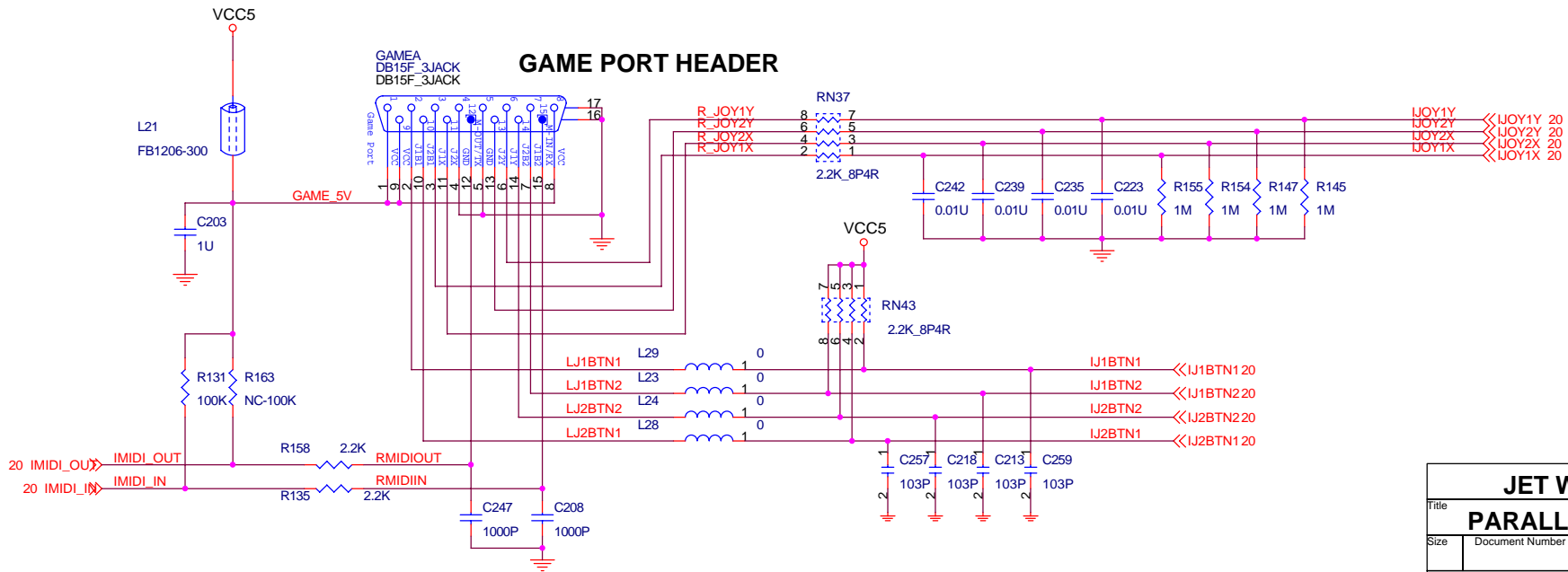
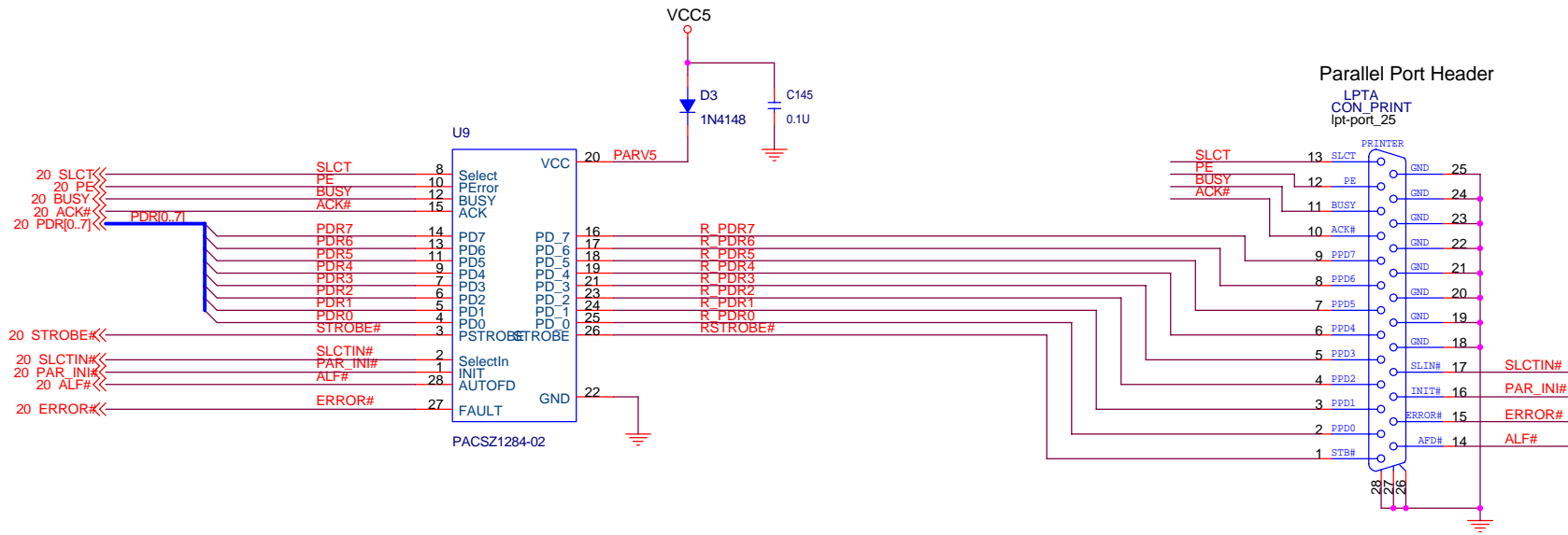
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Title			
PCI CONNECTORS 1 AND 2			
Size	Document Number	845LDA	
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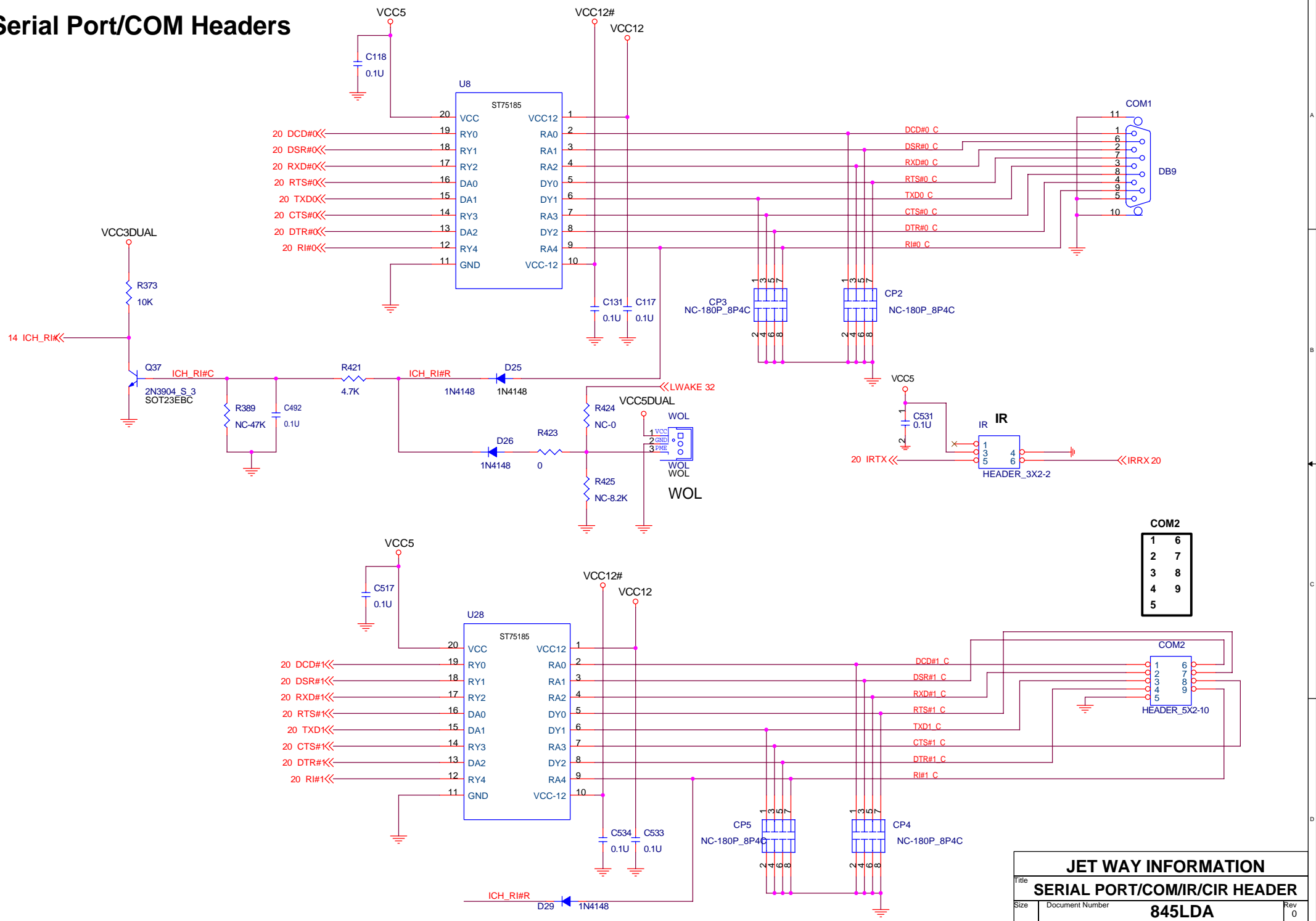
JET WAY INFORMATION		
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Size B	Document Number	Rev 0.3
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# Parallel Port Header

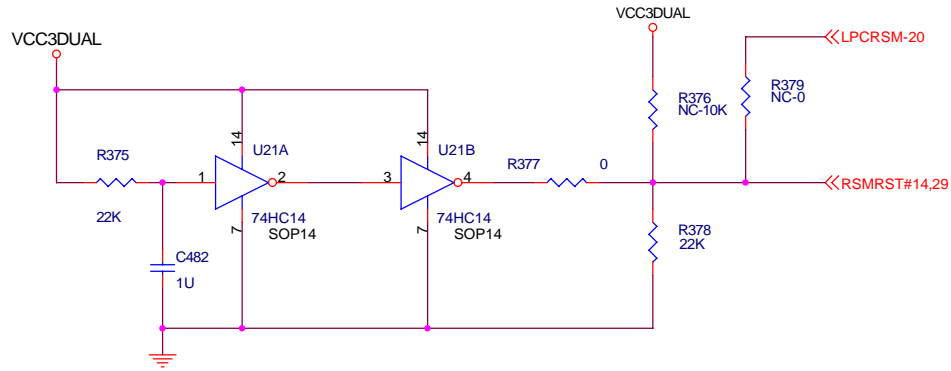
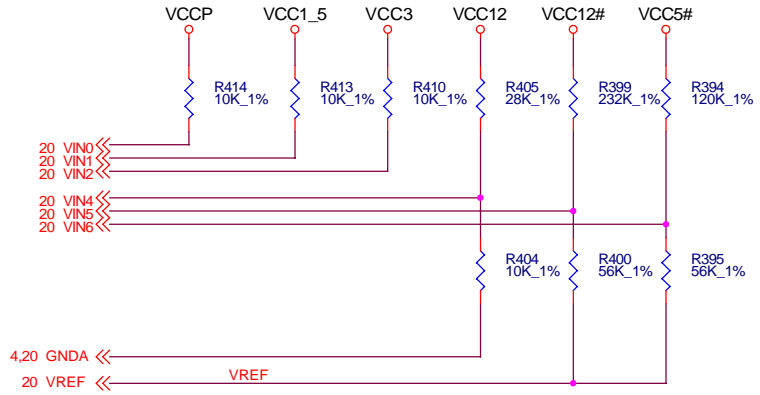
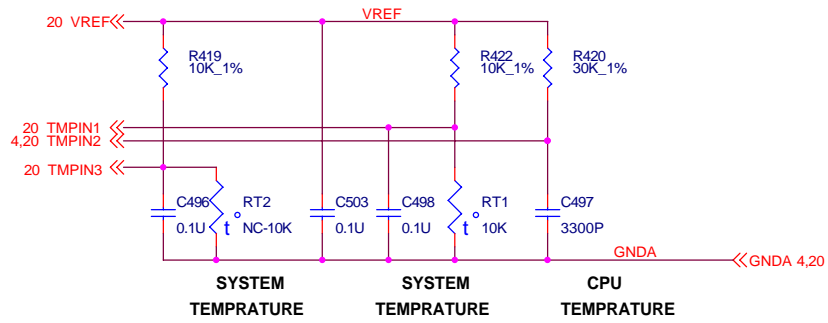


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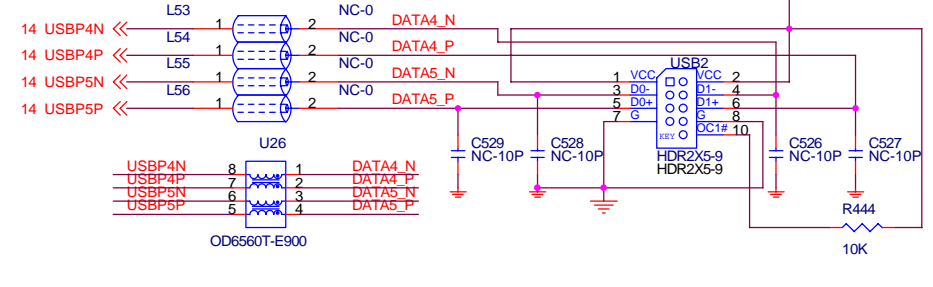
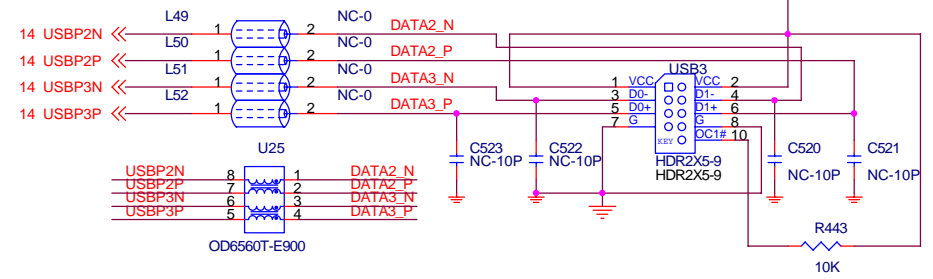
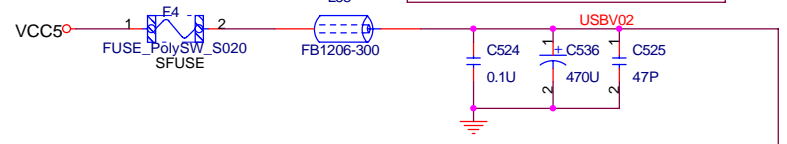
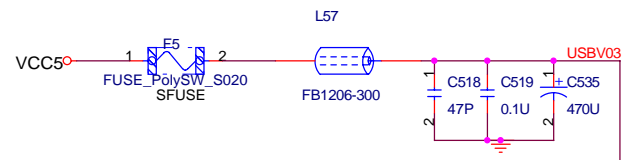
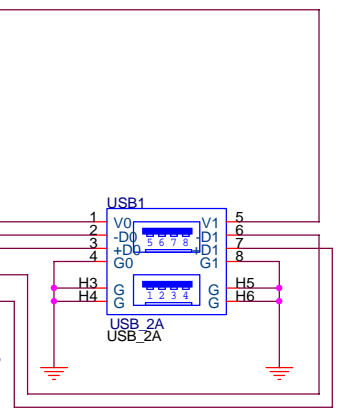
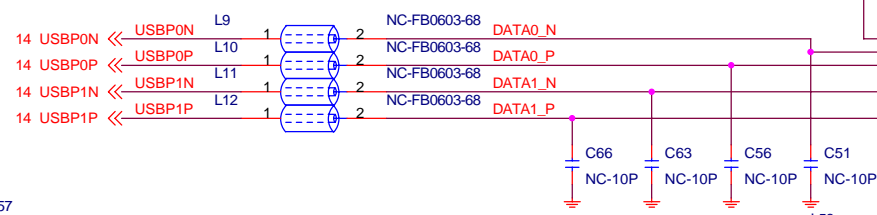
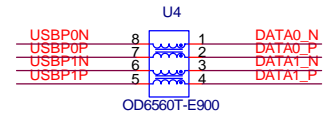
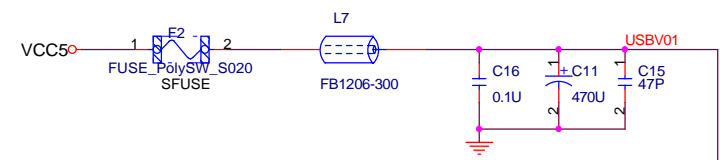
# Serial Port/COM Headers



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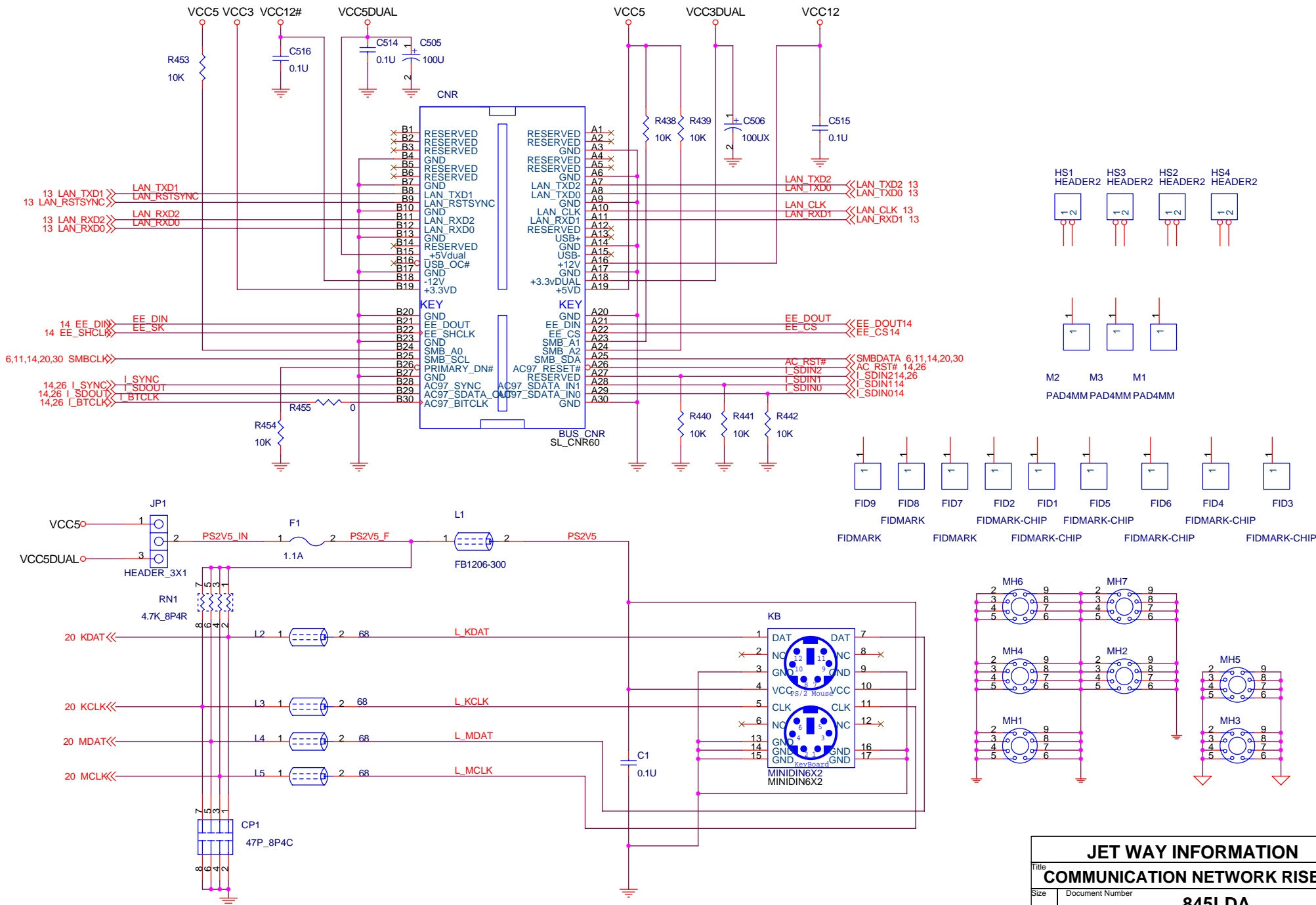


# USB Connectors

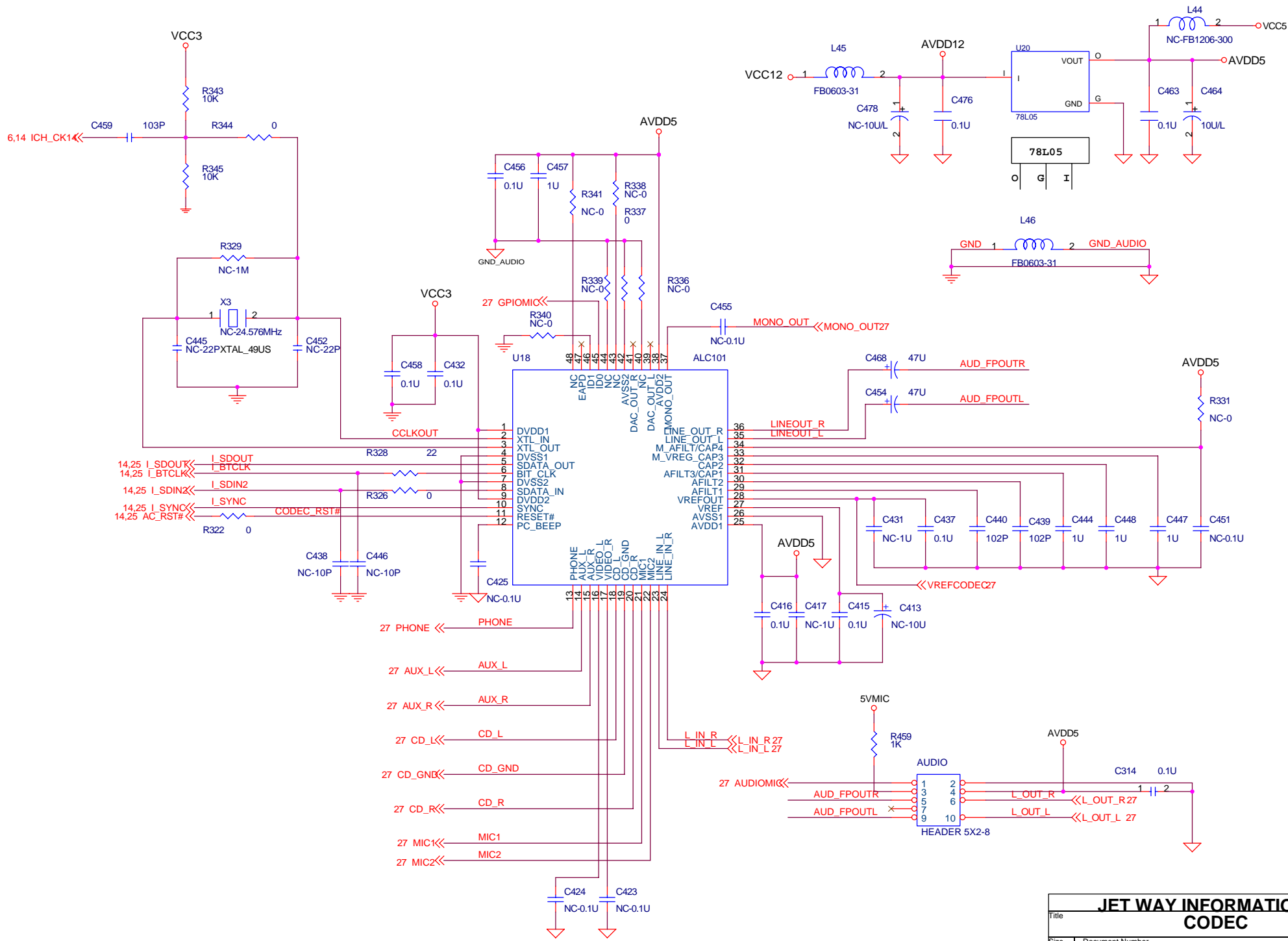


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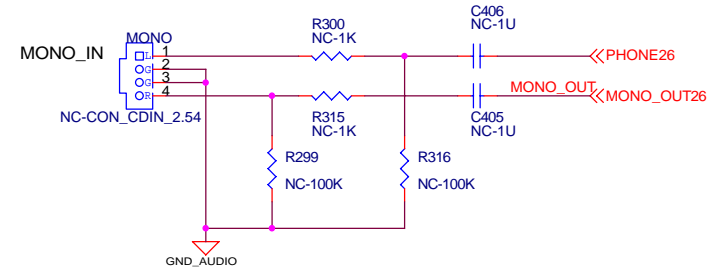
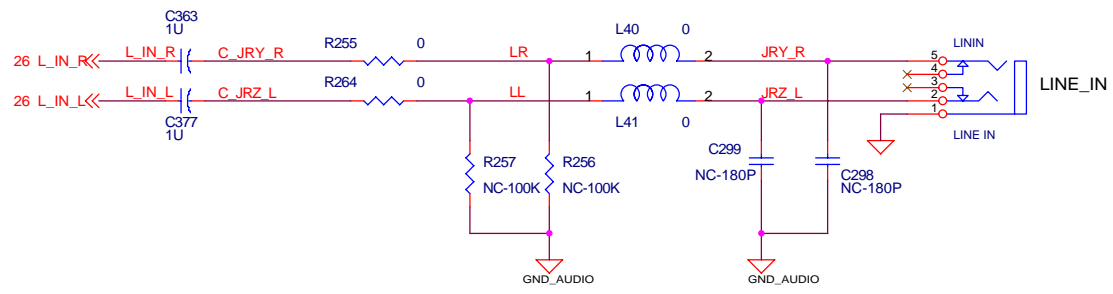
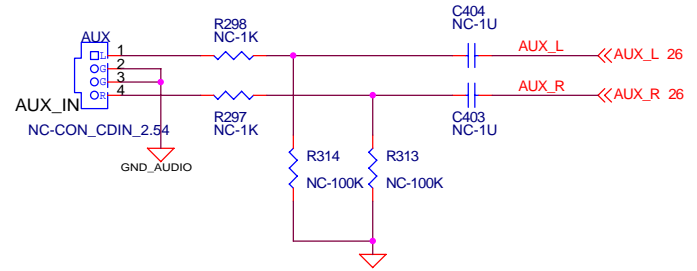
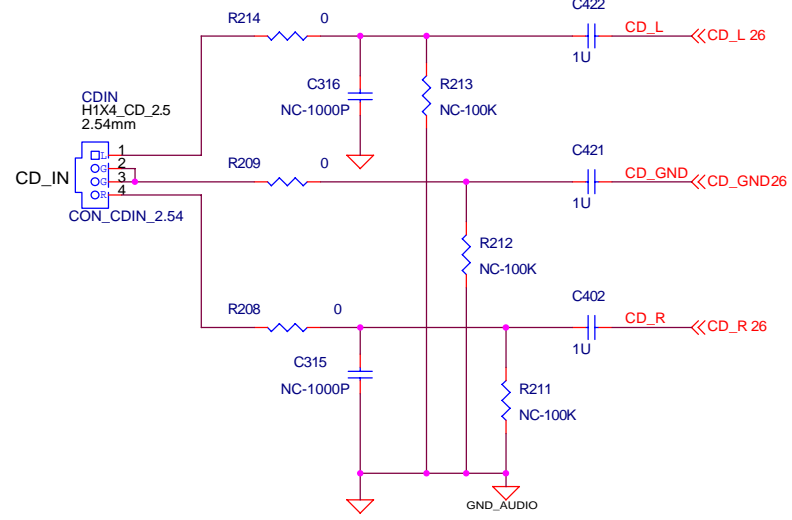
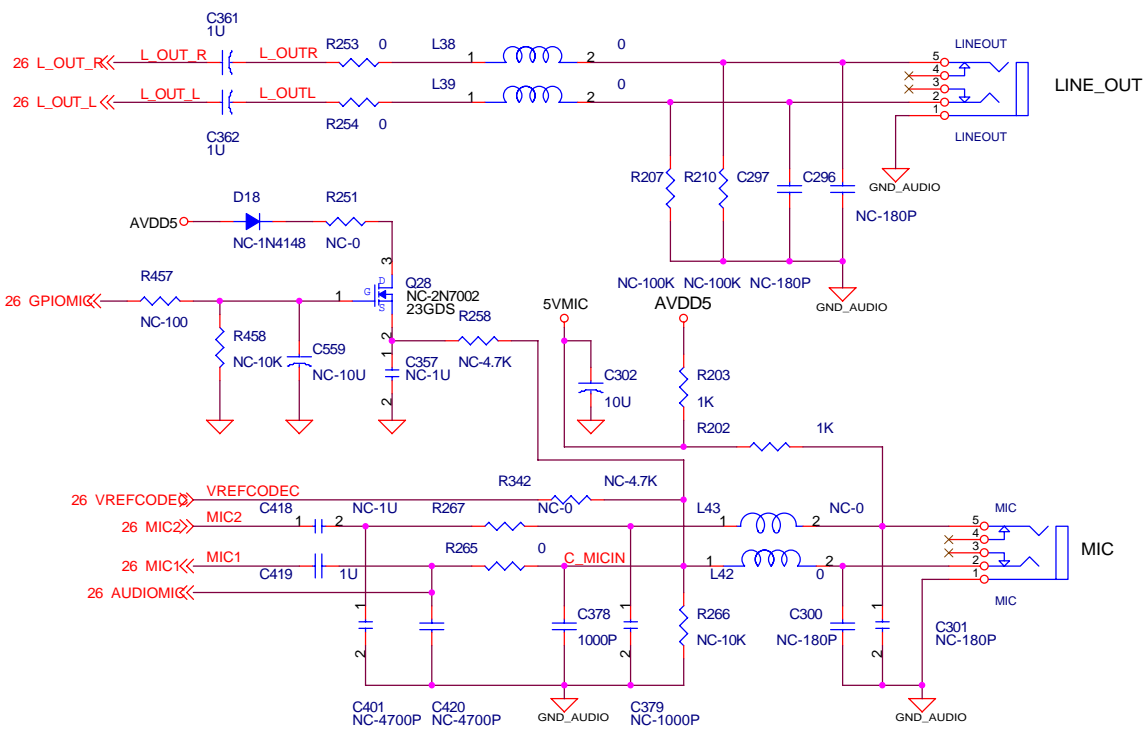
# KB/MS CONNECT & CNR RISER



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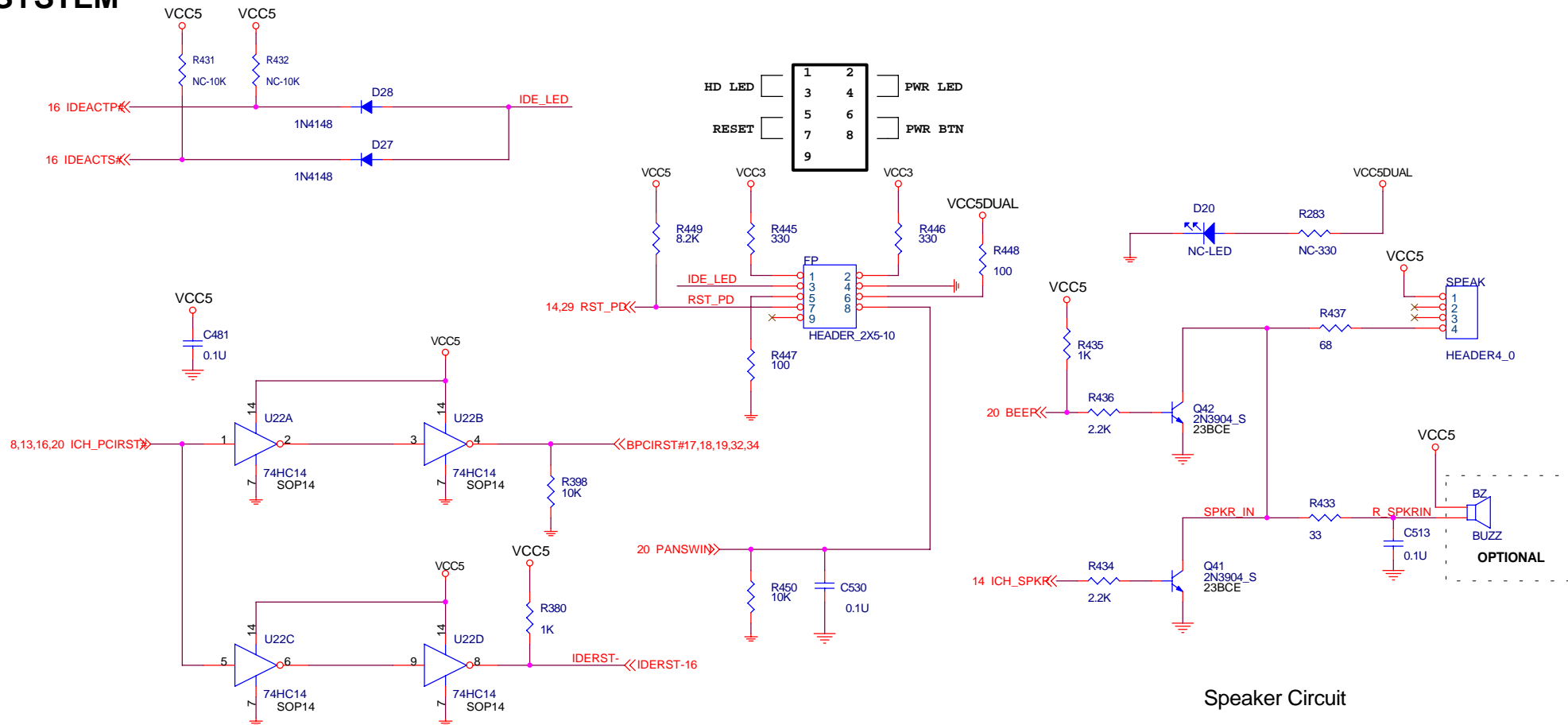


<b>JET WAY INFORMATION</b>		
<b>CODEC</b>		
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<b>AUDIO CONNECTOR</b>			
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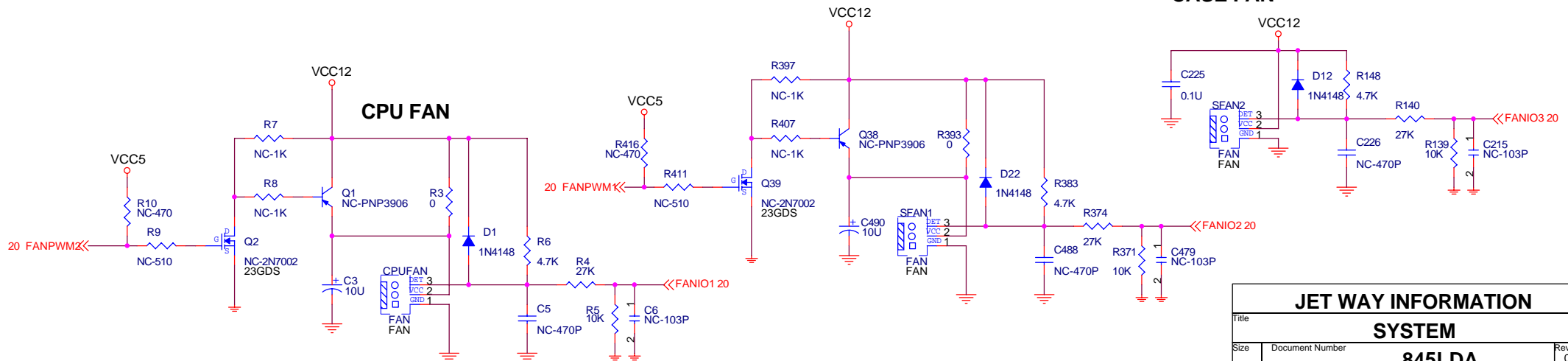
# SYSTEM



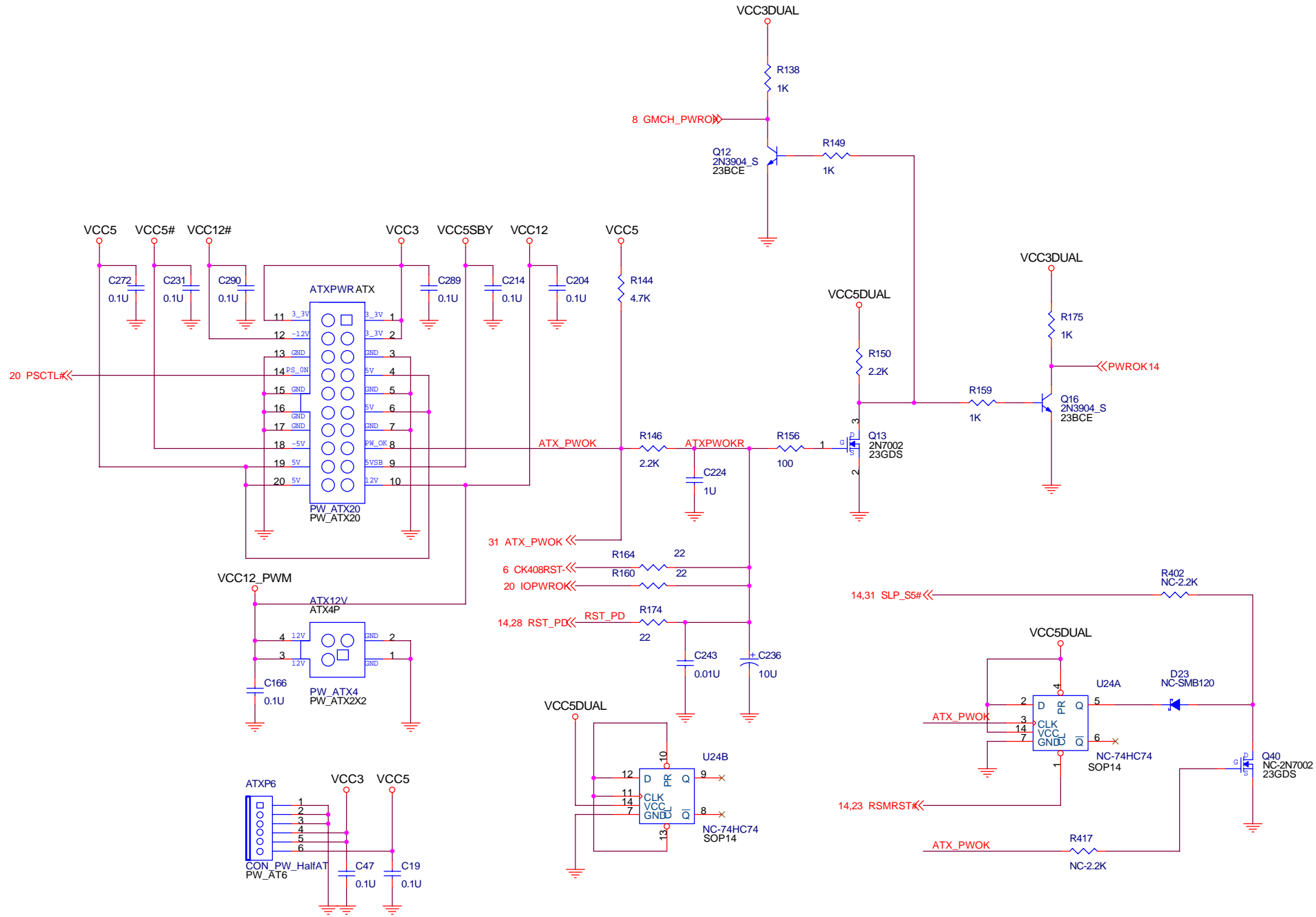
Speaker Circuit

## SYSTEM FAN

## CASE FAN

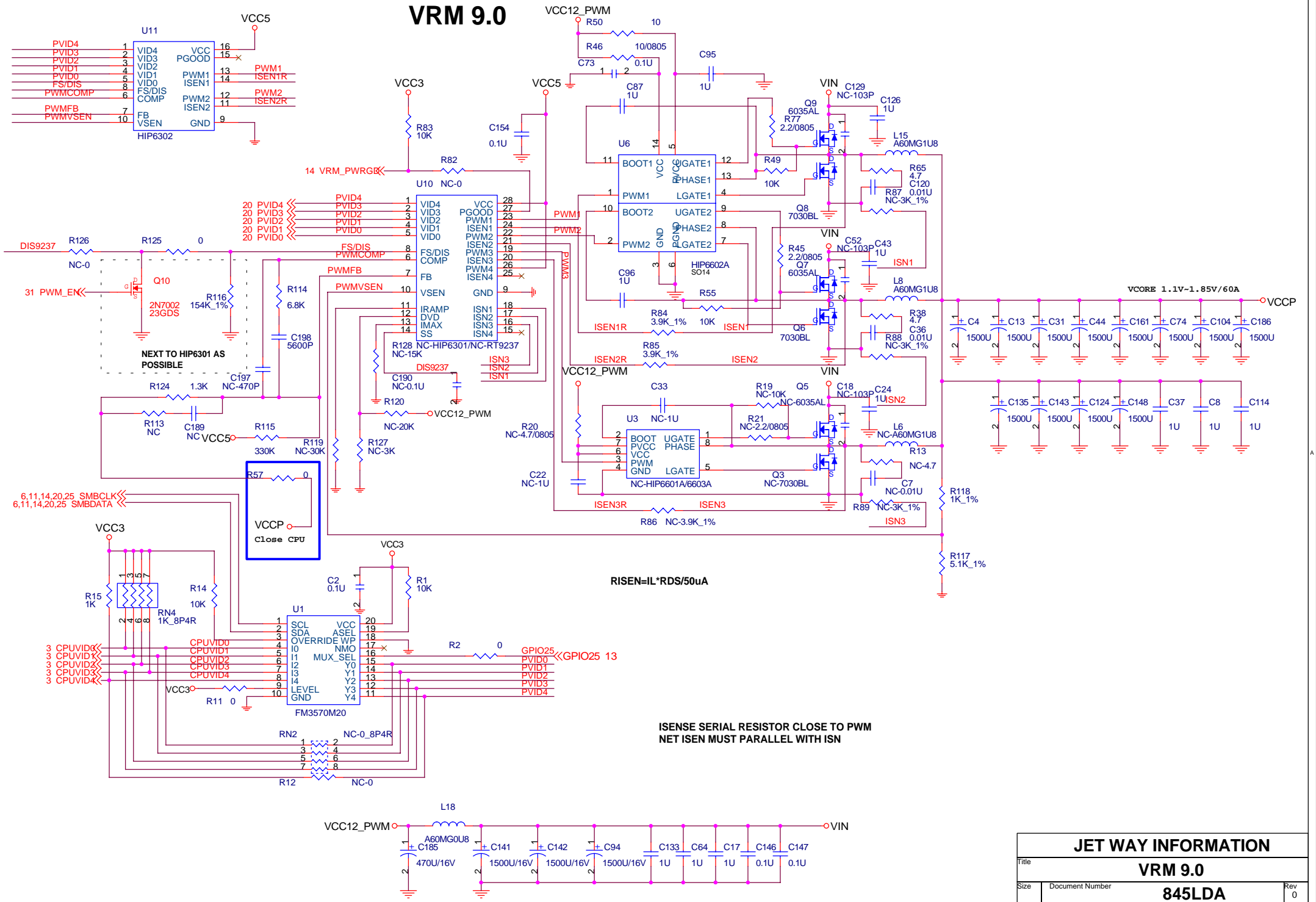


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Title		
<b>SYSTEM</b>		
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# VRM 9.0



$R_{ISEN} = I_L \cdot R_{DS} / 50\mu A$

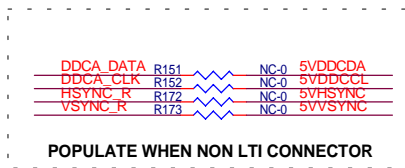
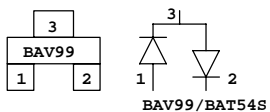
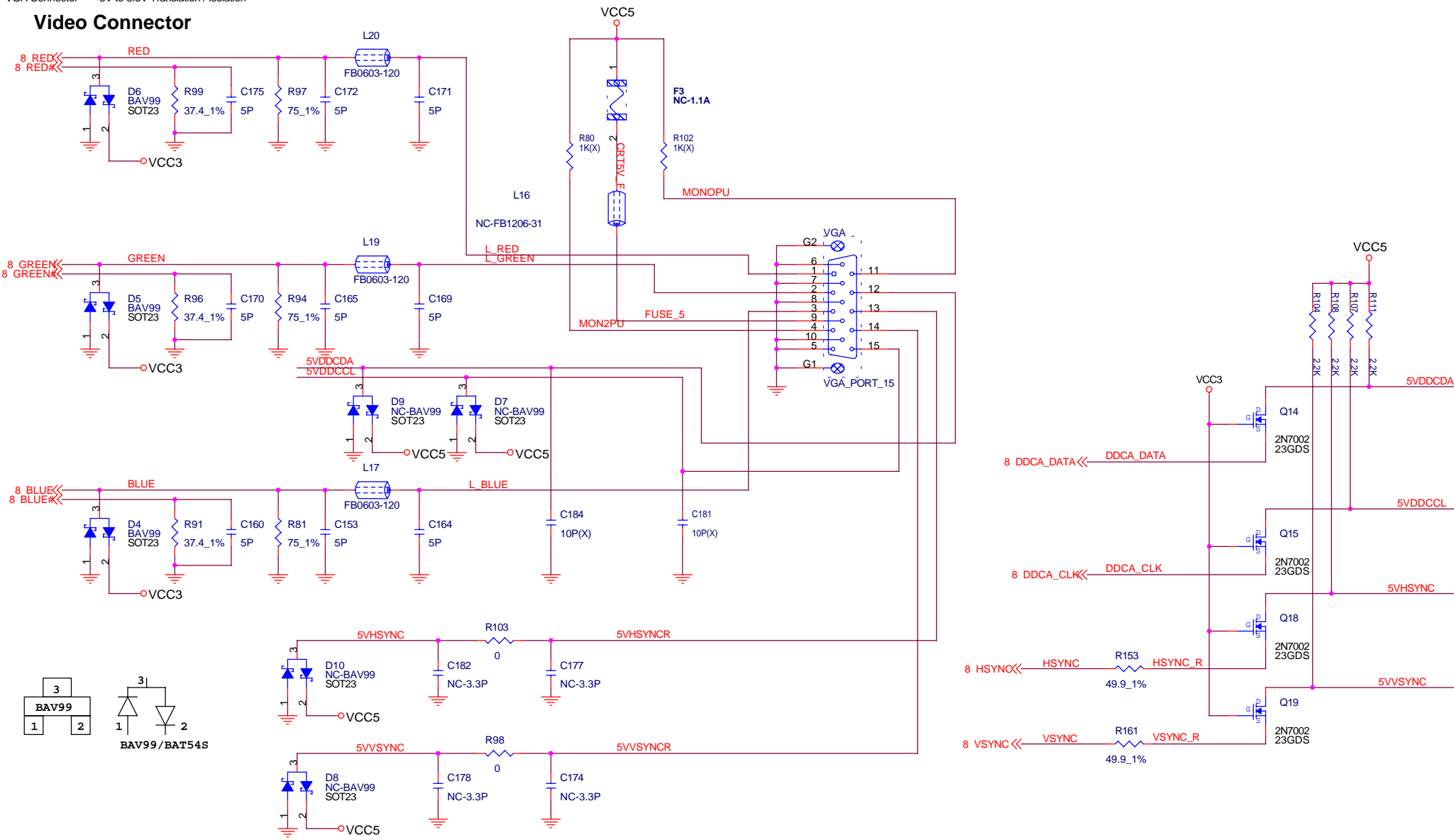
ISENSE SERIAL RESISTOR CLOSE TO PWM  
NET ISEN MUST PARALLEL WITH ISN

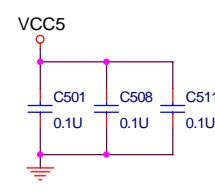
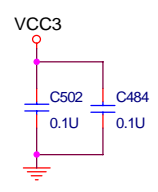
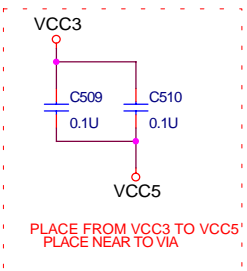
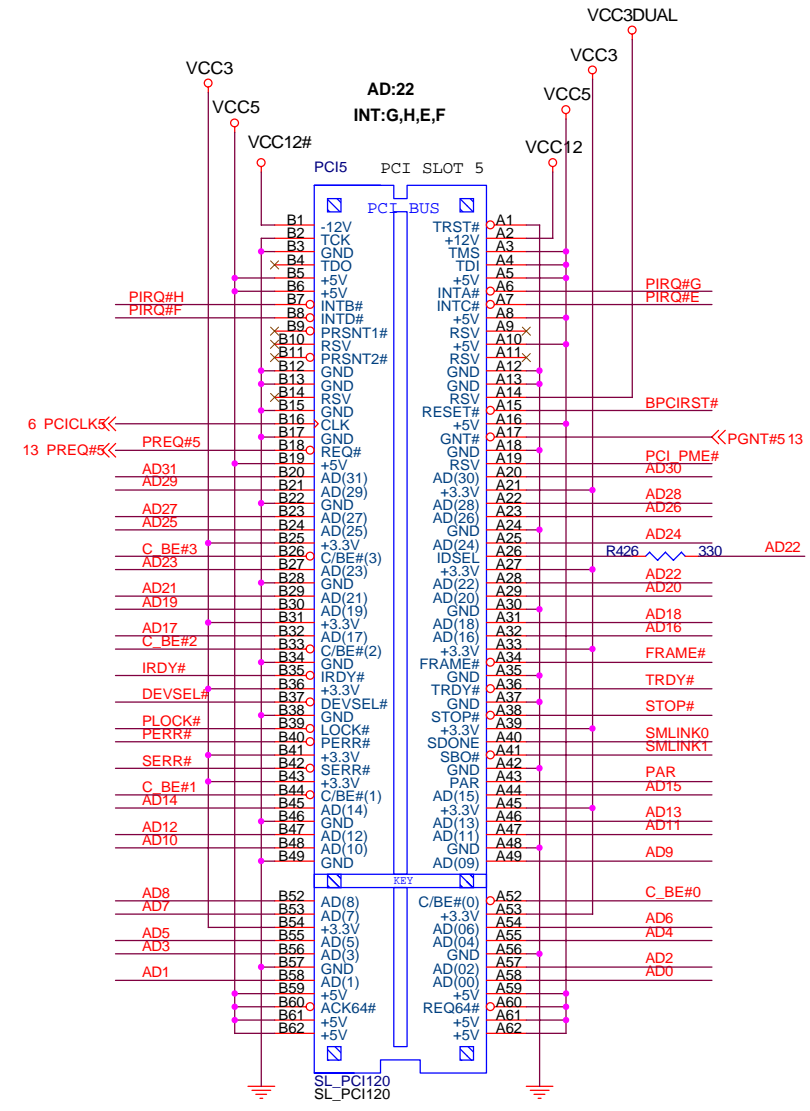
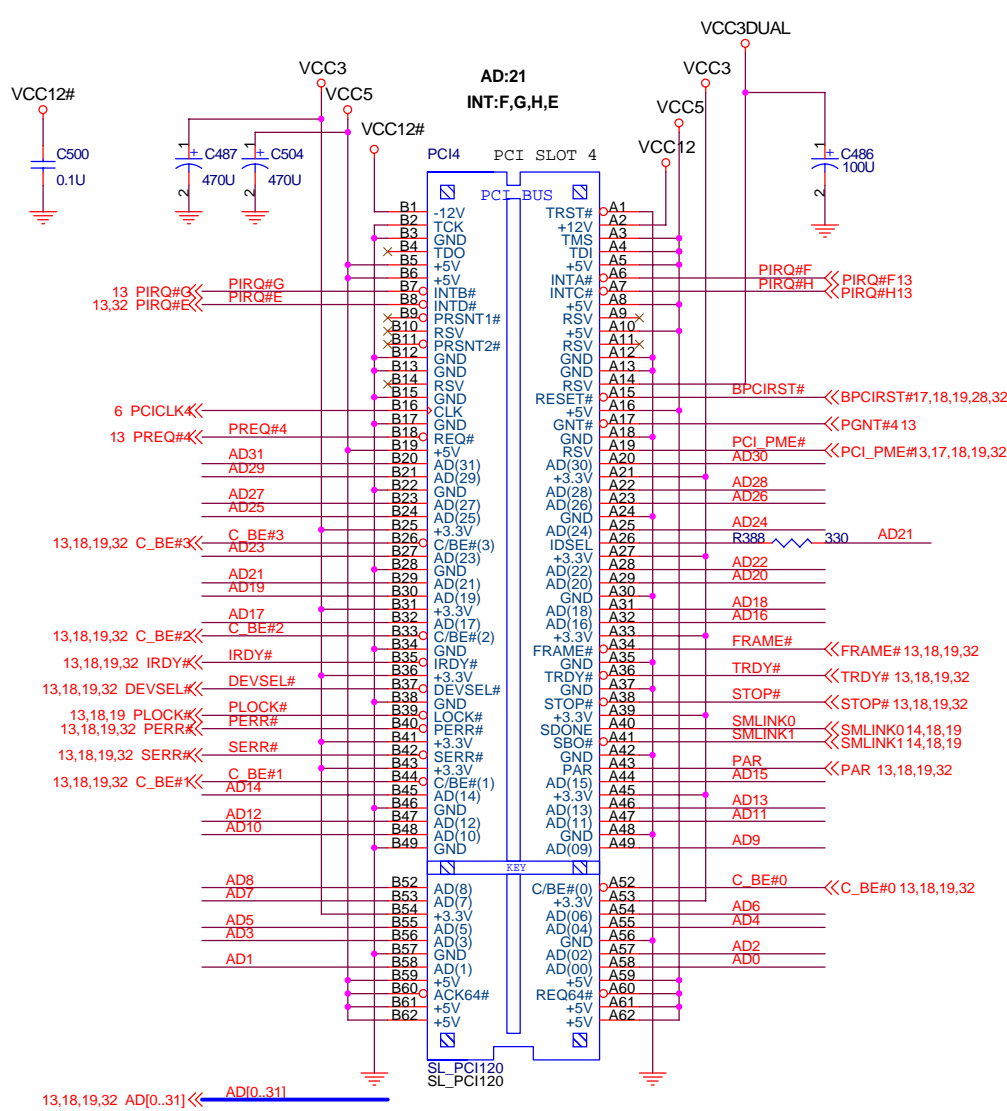
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### Video Connector





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