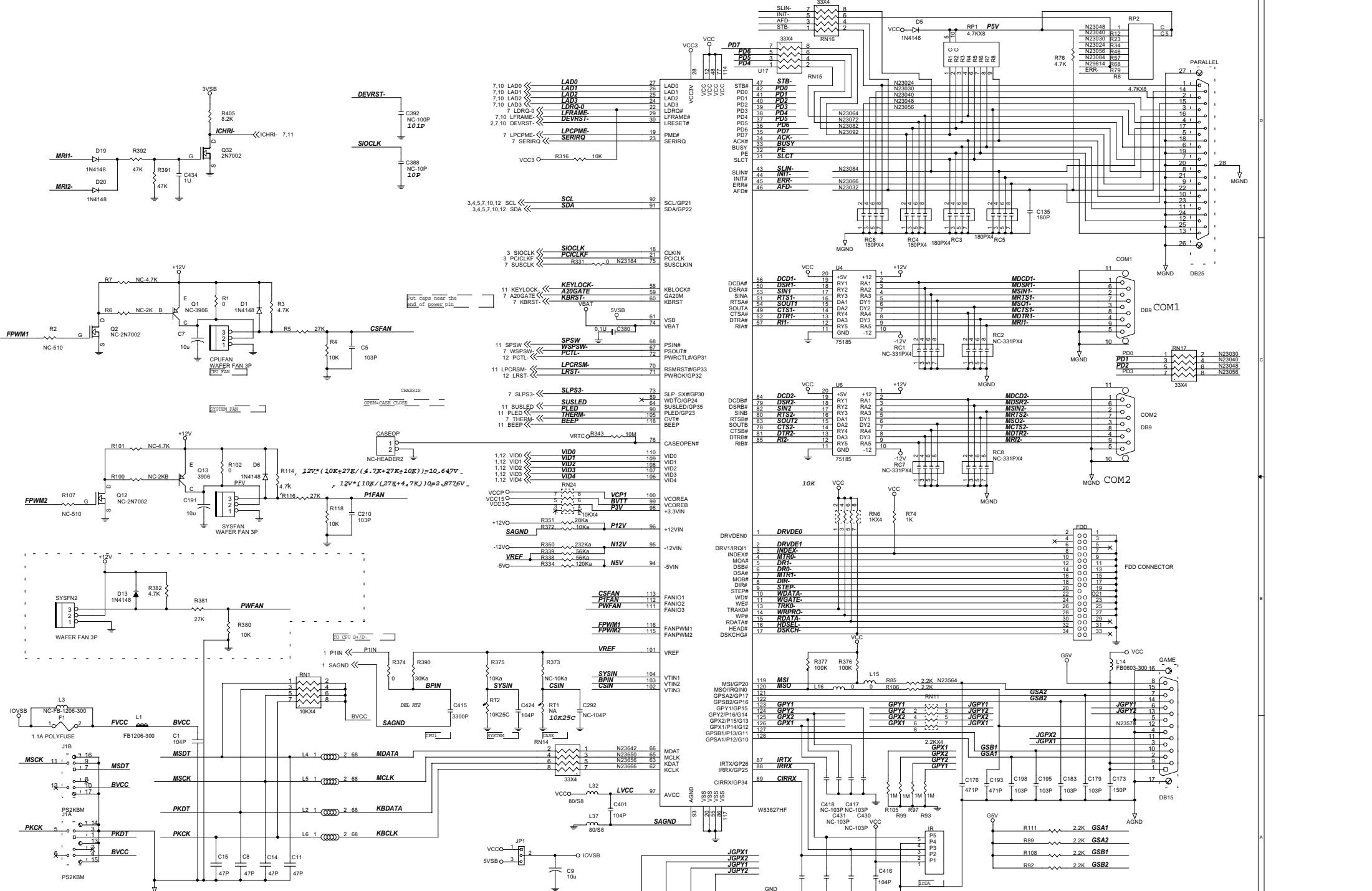


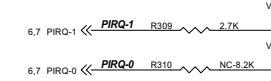
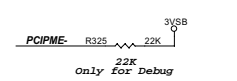
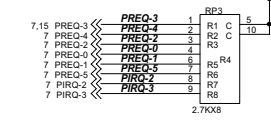
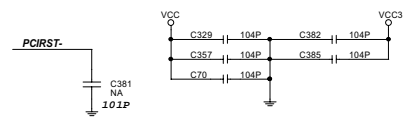
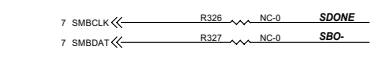
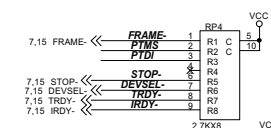
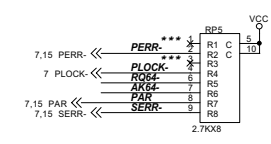
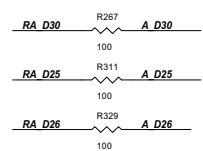
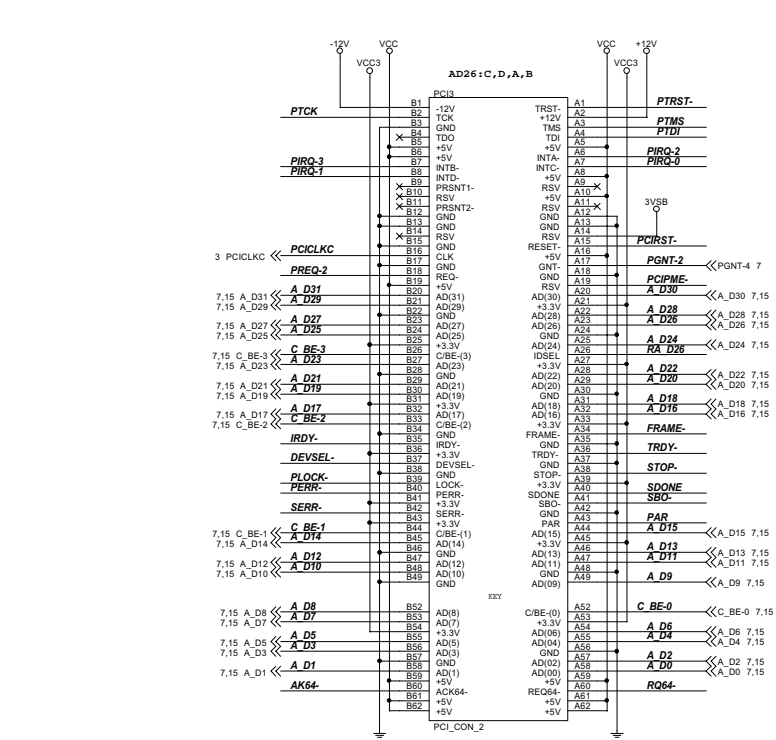
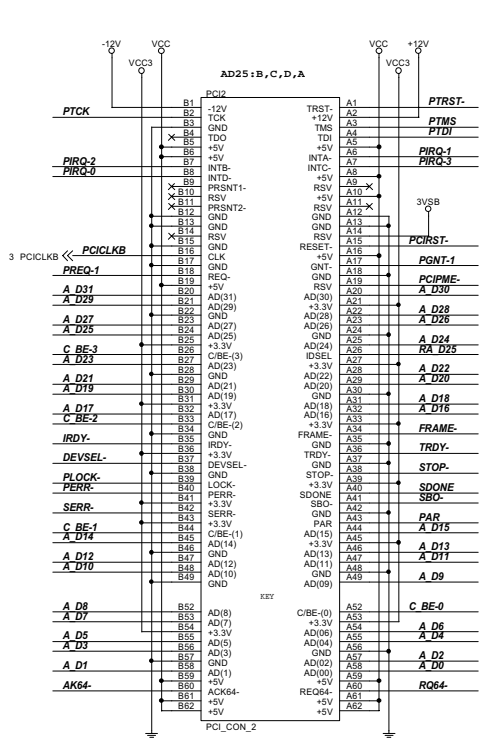
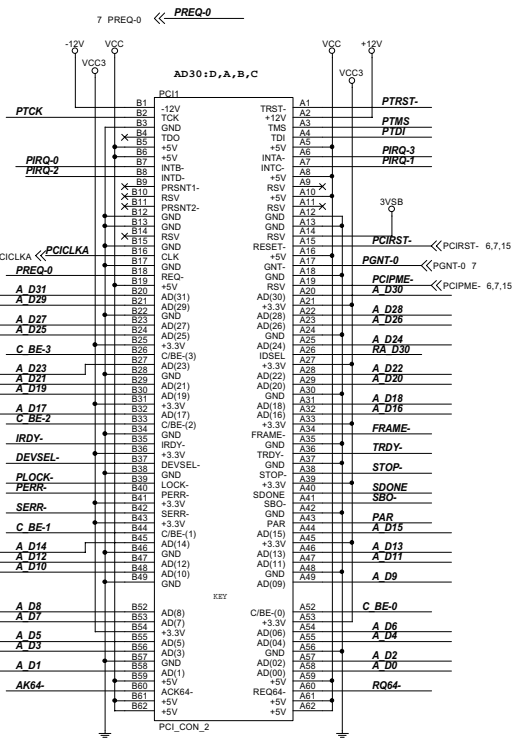
PLACE THOSE PARTS BELOW  
3RD DIMM FOR TERMINATION

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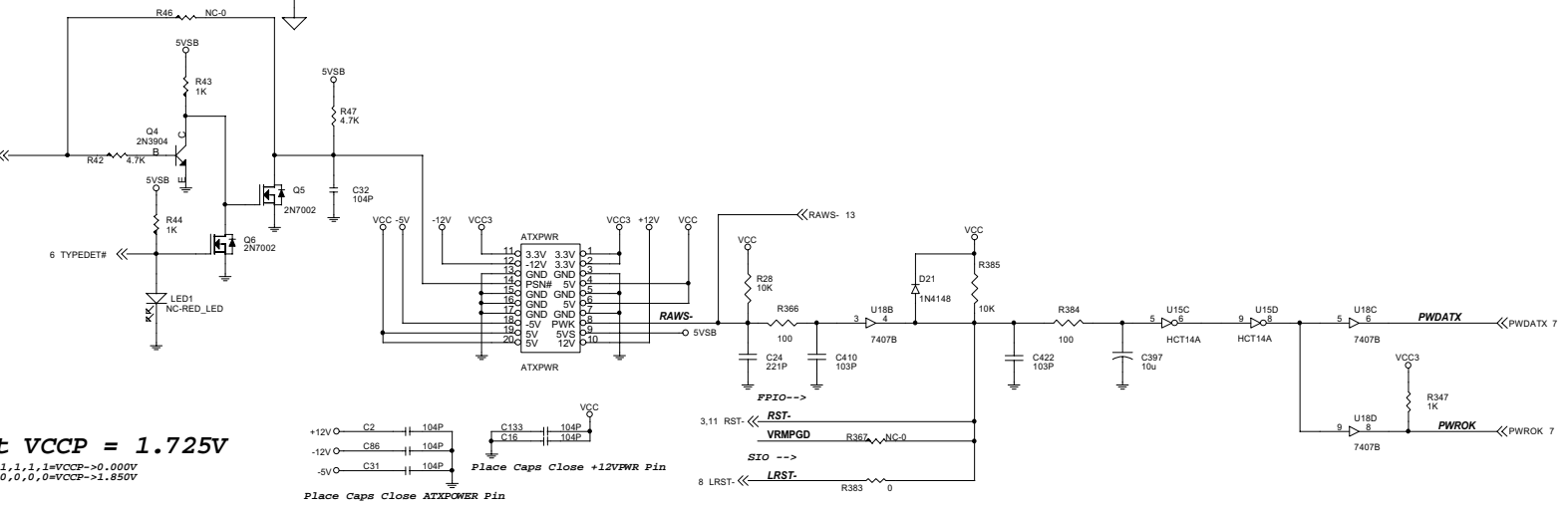
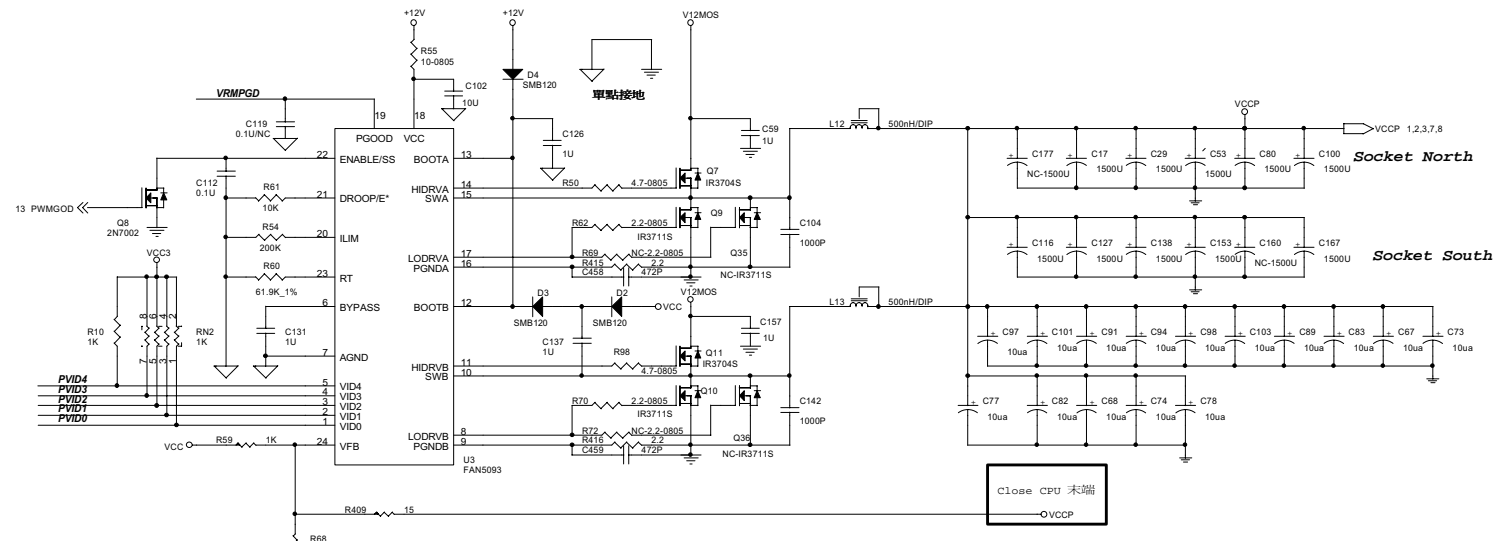








VID4	VID3	VID2	VID1	VID0	VCRDR
1	1	1	1	1	1.100
1	1	1	1	0	1.125
1	1	1	0	1	1.150
1	1	1	0	0	1.175
1	1	0	1	1	1.200
1	1	0	1	0	1.225
1	1	0	0	1	1.250
1	1	0	0	0	1.275
1	0	1	1	1	1.300
1	0	1	1	0	1.325
1	0	1	0	1	1.350
1	0	1	0	0	1.375
1	0	0	1	1	1.400
1	0	0	1	0	1.425
1	0	0	0	1	1.450
1	0	0	0	0	1.475
0	1	1	1	1	1.500
0	1	1	1	0	1.525
0	1	1	0	1	1.550
0	1	1	0	0	1.575
0	1	0	1	1	1.600
0	1	0	1	0	1.625
0	1	0	0	1	1.650
0	1	0	0	0	1.675
0	0	1	1	1	1.700
0	0	1	1	0	1.725
0	0	1	0	1	1.750
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0	0	0	0	1	1.850

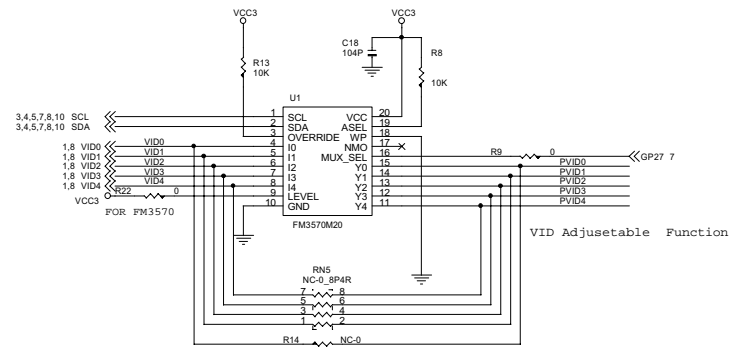


**Default VCCP = 1.725V**

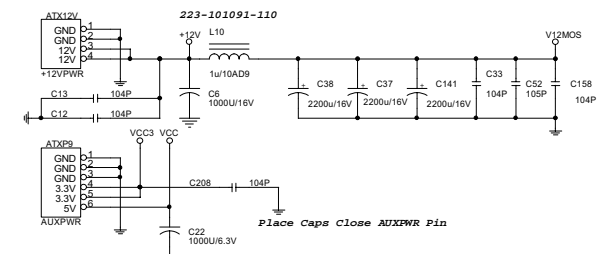
VID[4..0]=1,1,1,1,VCCP->0.000V  
 VID[4..0]=0,0,0,0,VCCP->1.850V

+12V C2 104P  
 -12V C86 104P  
 -5V C31 104P  
 Place Caps Close +12VWR Pin

VCC C133 104P  
 VCC C16 104P  
 Place Caps Close ATXPOWER Pin

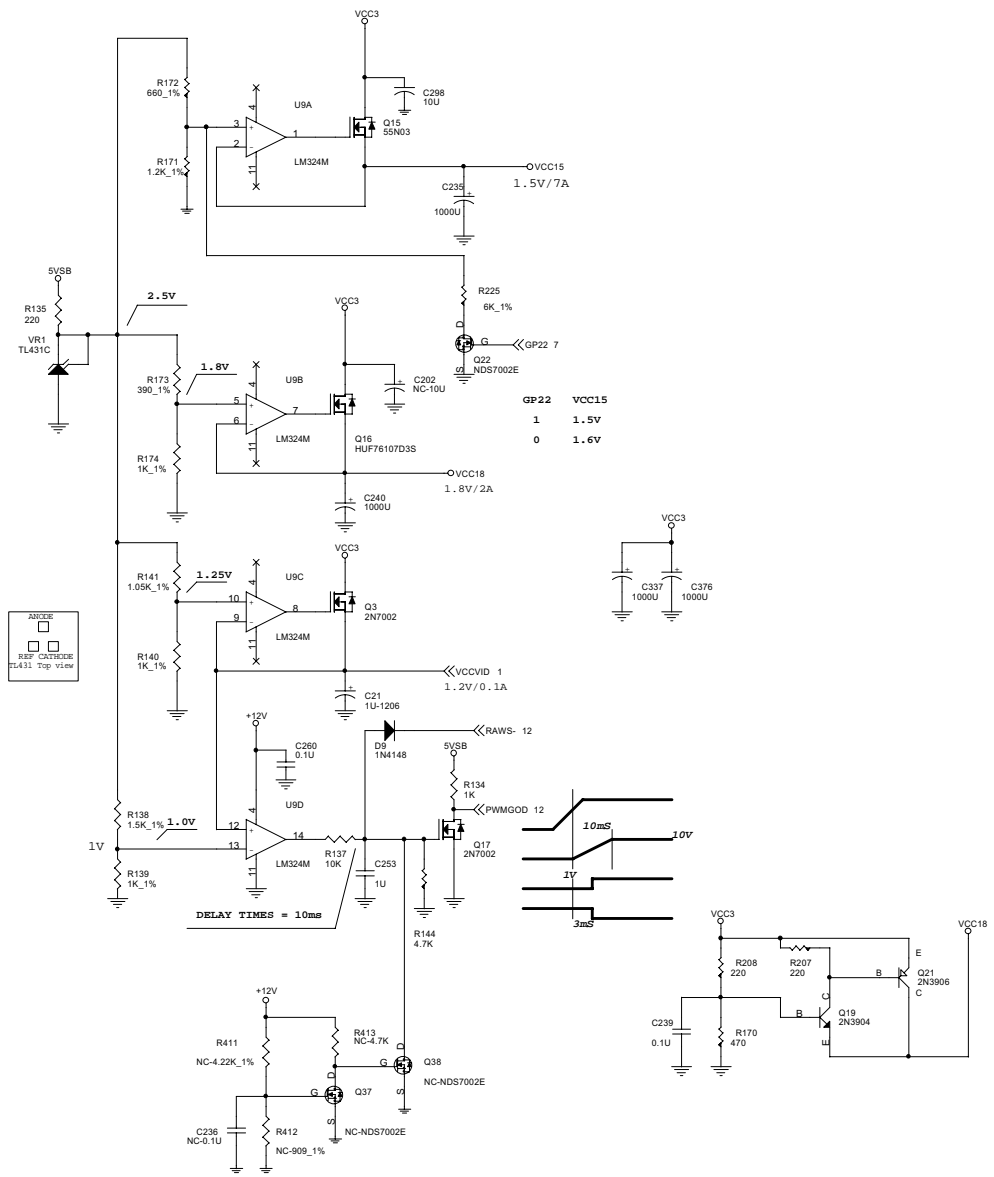


TO PWM  
 REGULATOR  
 and I/O  
 WHM



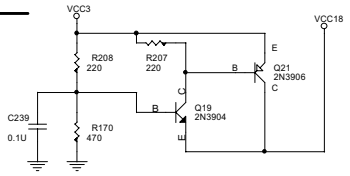
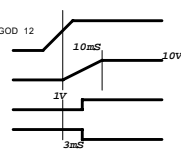
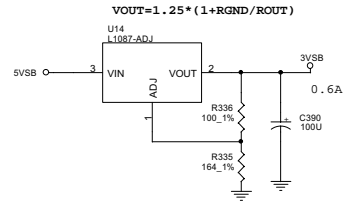
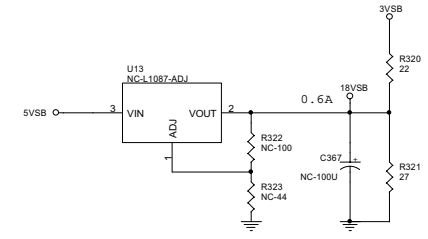
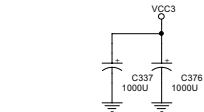
**12VATXPOWER SPEC. Ver. 1.0**

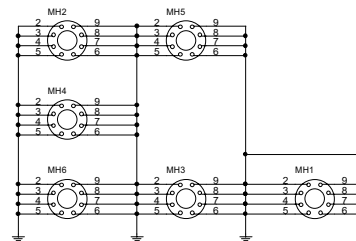
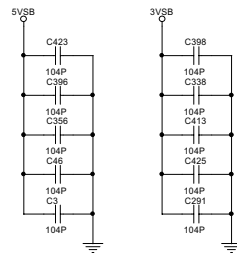
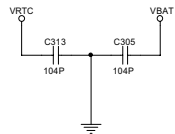
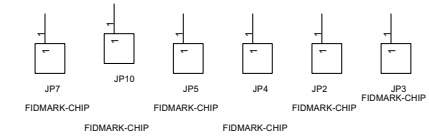
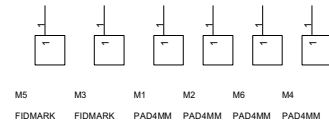
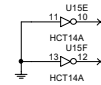
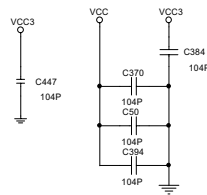
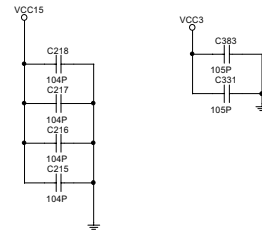
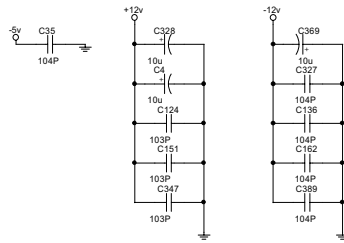
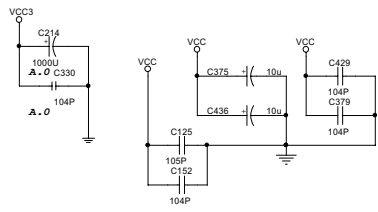
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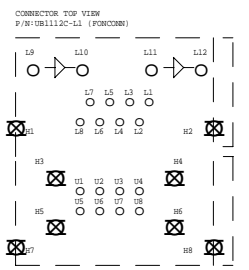
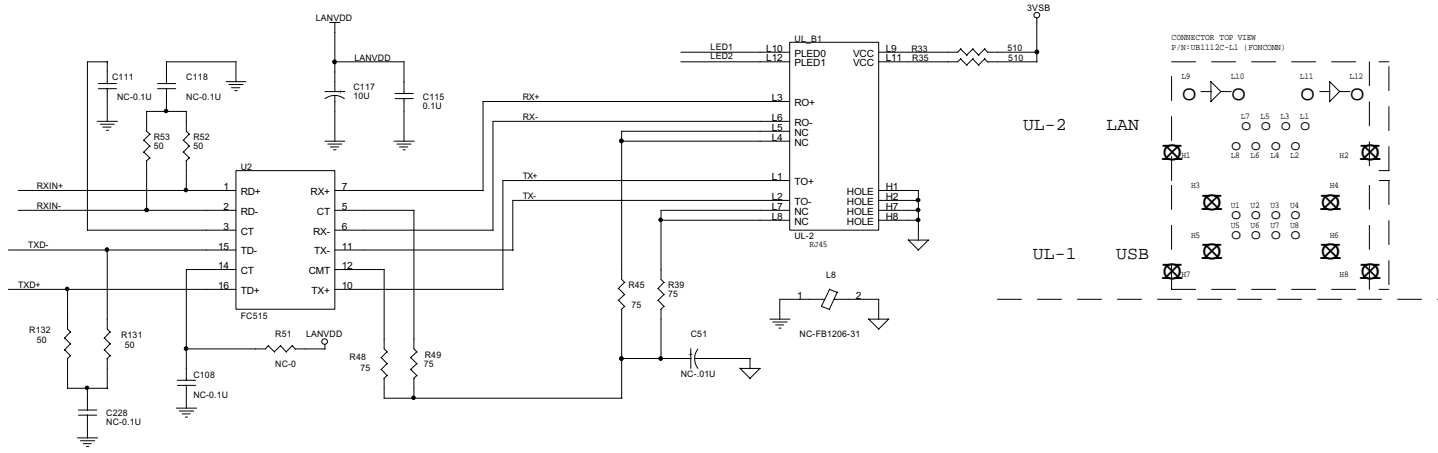
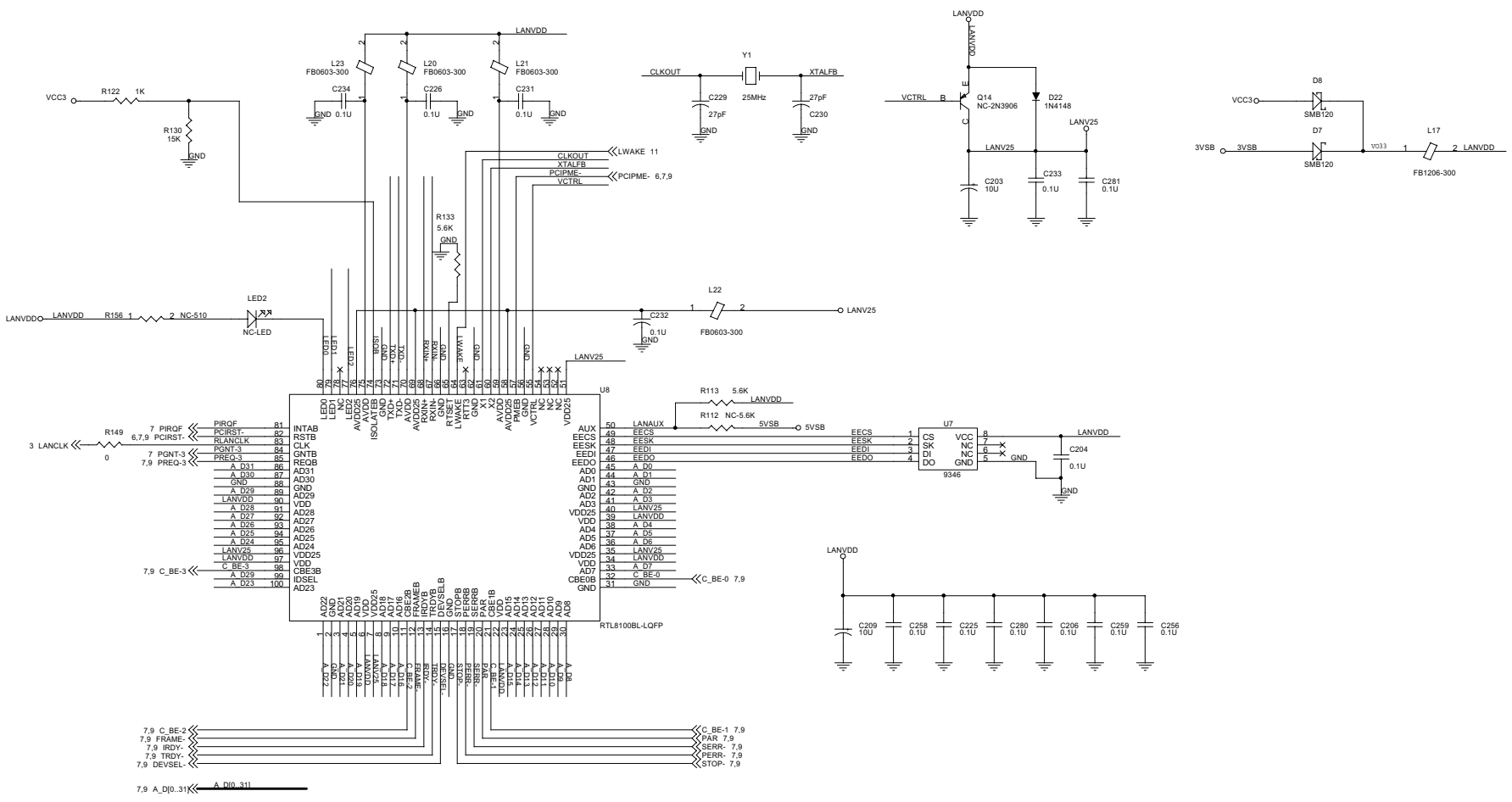
GP22

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VCC18	0	1.6V





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